RT3N55M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

DESCRIPTION

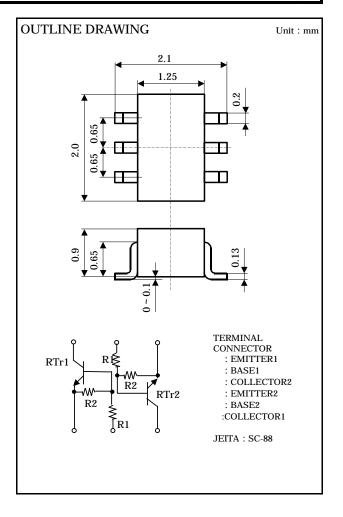
RT3N55M is a composite transistor built with two RT1N144 chips in SC-88 package.

FEATURE

Silicon NPN epitaxial type Each transistor elements are independent. Mini package for easy mounting

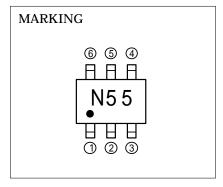
APPLICATION

Inverted circuit, switching circuit, interface circuit, driver circuit



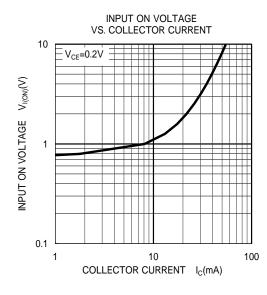
MAXIMUM RATING (Ta=25)

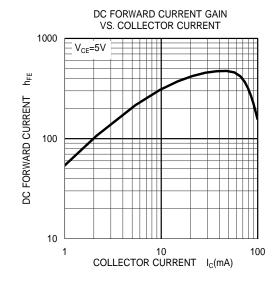
SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
Ic	Collector current	100	mA
Icm	Peak Collector current	200	mA
Pc	Collector dissipation (Total, Ta=25)	150	mW
Tj	Junction temperature	+ 150	
Tstg	Storage temperature	-55 ~ + 150	

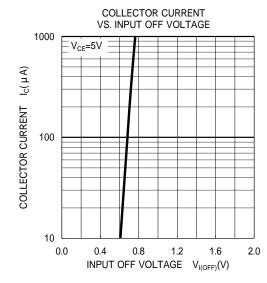


ELECTRICAL CHARACTERISTICS (Ta=25 RTr1,RTr2 to common)

Symbol	Parameter	Test conditions	Limits			T I:4
			Min	Тур	Max	Unit
V(BR)CEO	Collector to Emitter break down voltage	IC=100 µ A,R _{BE} =	50	-	-	V
ICBO	Collector cut off current	V _{CB} =50V,I _E =0	-	-	0.1	μΑ
hFE	DC forward current gain	VCE=5V,IC=5mA	50	-	-	-
VCE(sat)	Collector to Emitter saturation voltage	IC=10mA,IB=5mA	-	0.1	0.3	V
VI(ON)	Input on voltage	V _{CE} =0.2V,I _C =5mA	1	1.0	1.8	V
VI(OFF)	Input off voltage	Vce=5V,Ic=100 µ A	0.4	0.7	-	V
R ₁	Input resistor	-	7	10	13	k
R2/R1	Resistor ratio	-	4.2	4.7	5.1	-
f_T	Gain band width product	V _{CE} =6V,I _E =-10mA	-	200	-	MHz









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