RT3CLLM

Compound Transistor
For Low Frequency Amplify Application
Silicon Npn Epitaxial Type

DESCRIPTION

RT3CLLM is a compound transistor built with two 2SC3052 chips in SC-88 package.

FEATURE

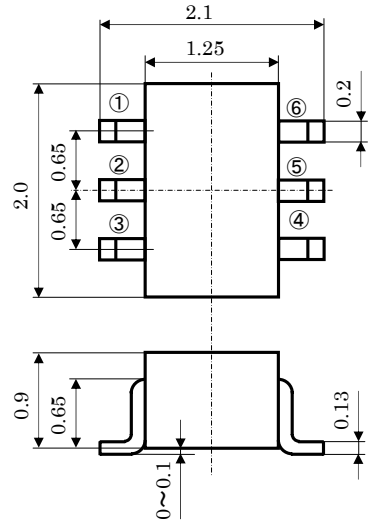
Silicon npn epitaxial type
Each transistor elements are independent.
Mini package for easy mounting

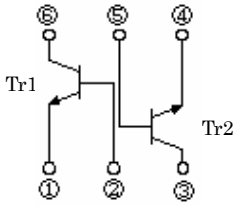
APPLICATION

For low frequency amplify application

OUTLINE DRAWING

Unit:mm





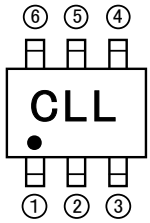
TERMINAL CONNECTOR
①:EMITTER1
②:BASE1
③:COLLECTOR2
④:EMITTER2
⑤:BASE2
⑥:COLLECTOR1

JEITA:SC-88

MAXIMUM RATING (Ta=25℃)

SYMBOL	PARAMETER	RATING	UNIT
V_{CBO}	Collector to Base voltage	50	V
V_{EBO}	Emitter to Base voltage	6	V
V_{CEO}	Collector to Emitter voltage	50	V
I_C	Collector current	200	mA
$P_{C(Total)}$	Collector dissipation(Ta=25℃)	150	mW
T_j	Junction temperature	+125	℃
T_{stg}	Storage temperature	-55～+125	℃

MARKING



ELECTRICAL CHARACTERISTICS (Ta=25℃)

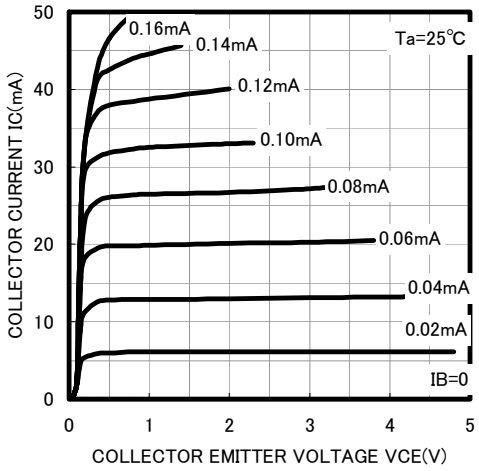
Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
$V_{(BR)CEO}$	Collector to Emitter break down voltage	I_C=100μA,R_{BE}=∞	50	-	-	V
I_{CBO}	Collector cut off current	V_{CB}=50V,I_E=0	-	-	0.1	μA
I_{EBO}	Emitter cut off current	V_{EB}=6V,I_C=0	-	-	0.1	μA
h_{FE}*	DC forward current gain	V_{CE}=6V,I_C=1mA	150	-	800	-
h_{FE}	DC forward current gain	V_{CE}=6V,I_C=0.1mA	90	-	-	-
$V_{CE(sat)}$	Collector to Emitter saturation voltage	I_C=100mA,I_B=10mA	-	-	0.3	V
f_T	Gain band width product	V_{CE}=6V,I_E=-10mA	-	200	-	MHz
C_{ob}	Collector output capacitance	V_{CB}=6V,I_E=0,f=1MHz	-	2.5	-	pF
NF	Noise figure	V_{CE}=6V,I_E=-0.1mA,f=1kHz,R_G=2kΩ	-	-	15	dB

＊: It shows h_{FE} classification in right table.

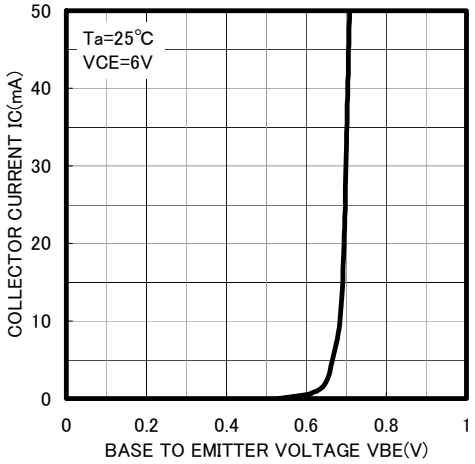
Item	E	F	G
hFE	150～300	250～500	400～800

TYPICAL CHARACTERISTICS

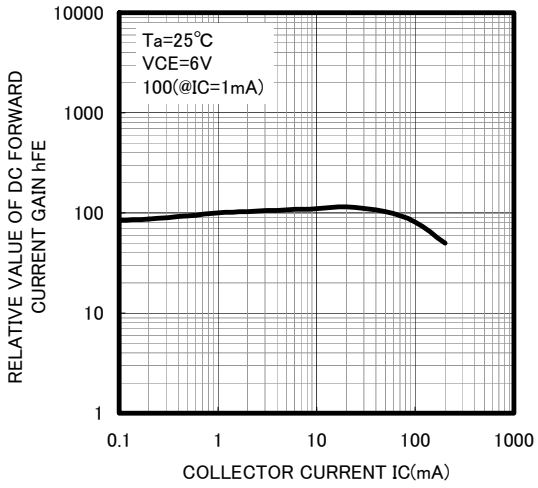
COMMON EMITTER OUTPUT



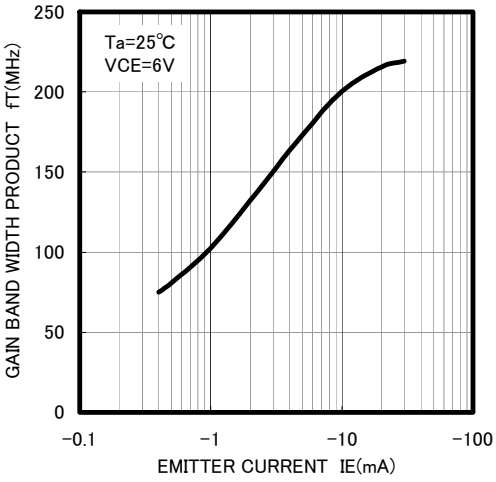
COMMON EMITTER TRANSFER



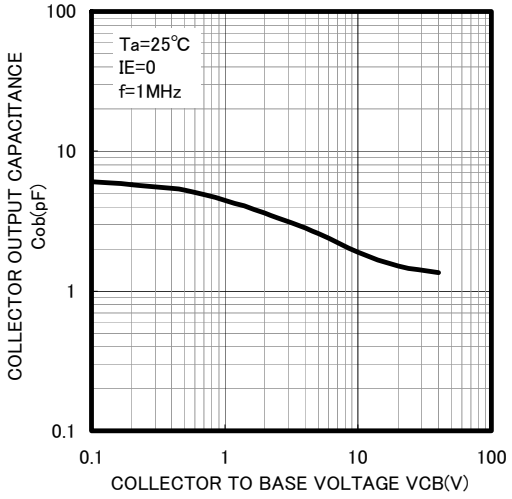
DC FORWARD CURRENT GAIN
VS. COLLECTOR CURRENT



GAIN BAND WIDTH PRODUCT
VS. EMITTER CURRENT



COLLECTOR OUTPUT CAPACITANCE
VS. COLLECTOR TO BASE VOLTAGE



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Jan.2003