Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 280 A



SOT-227

400 V

280 A

40 ns

Modules - Diode FRED Pt®

PRODUCT SUMMARY

VR

 $I_{F(AV)}$ per module at $T_C = 90 \ ^{\circ}C$

t_{rr}

Туре

FEATURES

- · Two fully independent diodes
- · Fully insulated package
- RoHS • Ultrafast, soft reverse recovery, with high COMPLIANT operation junction temperature (T_J max. = 175 °C)
- Low forward voltage drop
- Optimized for power conversion: welding and industrial **SMPS** applications
- · Easy to use and parallel
- · Industry standard outline
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified for industrial level

DESCRIPTION

The VS-UFB280FA40 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	V _R		400	V	
Continuous forward current per diode	I _F ⁽¹⁾	T _C = 90 °C	170	А	
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	1300		
Maximum power dissipation per module	PD	T _C = 90 °C	395	W	
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 minute	2500	V	
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C	

Note

⁽¹⁾ Maximum continuous forward current must be limited to 100 A to do not exceed the maximum temperature of power terminals.

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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25 \text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	400	-	-	
Forward voltage	V _{FM}	I _F = 100 A	-	1.06	1.24	V
		I _F = 100 A, T _J = 175 °C	0.85	-	0.95	
De construction de la construction	I _{RM}	$V_{R} = V_{R}$ rated	-	1.3	50	μA
Reverse leakage current		$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	0.36	-	4	mA
Junction capacitance	CT	V _R = 400 V	-	100	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A}/\mu \text{s}, V_R = 30 \text{ V}$		-	40	-	
Reverse recovery time		T _J = 25 °C	I _F = 150 A dI _F /dt = 200 A/μs V _R = 200 V	-	93	-	ns A
		T _J = 125 °C		-	172	-	
Peak recovery current I _{RRM}		T _J = 25 °C		-	10.5	-	
	IRRM	T _J = 125 °C		-	20.2	-	
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	490	-	nC
		T _J = 125 °C		-	1740	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting	Р		-	-	0.43	
Junction to case, both leg conducting	R _{thJC}		-	-	0.215	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque			-	1.3	-	Nm
Case style			SOT-227			

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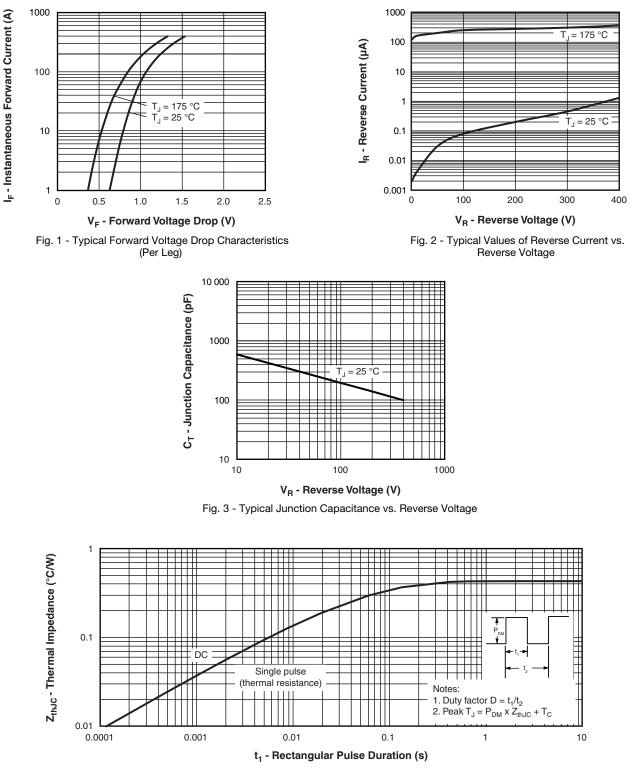
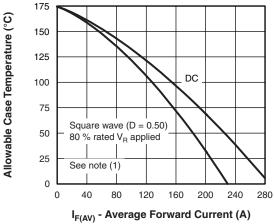
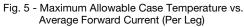


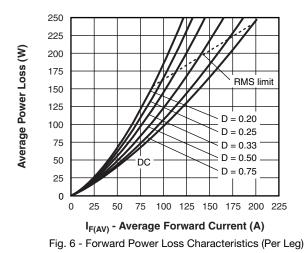
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

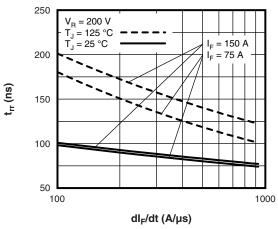
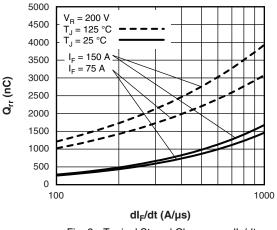


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt





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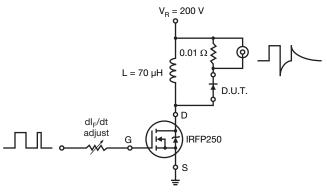


Fig. 9 - Reverse Recovery Parameter Test Circuit

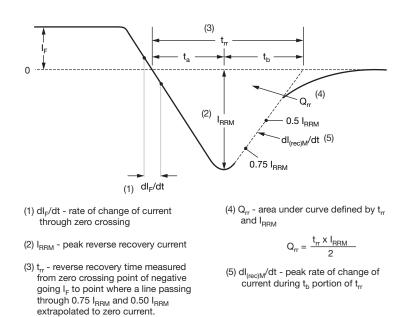
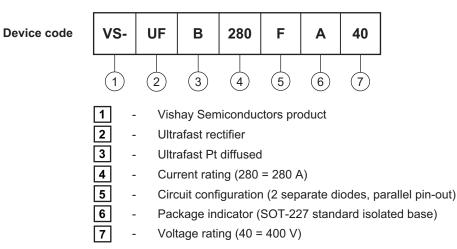


Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE



CIRCUIT CONFI	CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
2 separate diodes, parallel pin-out	F	Lead Assignment			

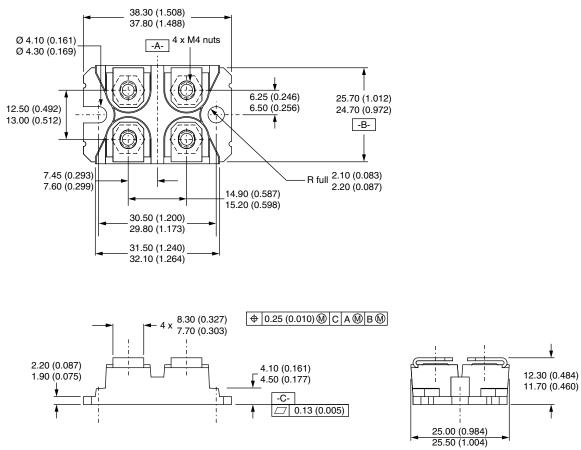
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95423			
Packaging information	www.vishay.com/doc?95425			

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SOT-227 Generation II



DIMENSIONS in millimeters (inches)

Note

· Controlling dimension: millimeter

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