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LD470WUB

Product Specification

SPECIFICATION FOR APPROVAL

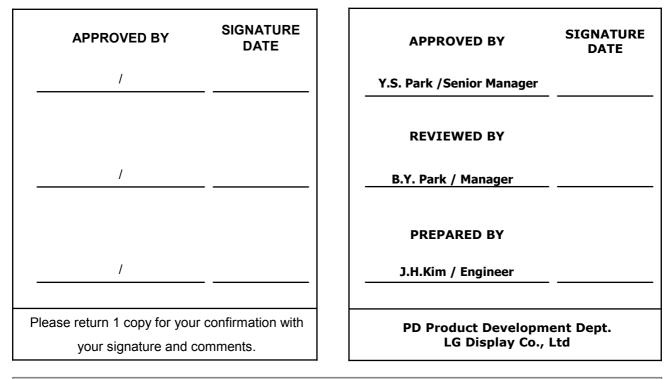
- () Preliminary Specification
- (
) Final Specification

47.0" WUXGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG DISPLAY Co., Ltd.
*MODEL	LD470WUB
SUFFIX	SAA1

*When you obtain standard approval, please use the above model name without suffix



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Product Specification

1 2	COVER CONTENTS RECORD OF REVISIONS	1
		2
	RECORD OF REVISIONS	
		3
2	GENERAL DESCRIPTION	4
	ABSOLUTE MAXIMUM RATINGS	5
3	ELECTRICAL SPECIFICATIONS	6
3-1	ELECTRICAL CHARACTERISTICS	6
3-2	INTERFACE CONNECTIONS	8
3-3	SIGNAL TIMING SPECIFICATIONS	10
3-4	SIGNAL TIMING WAVEFORMS	11
3-5	COLOR DATA REFERENCE	12
3-6	POWER SEQUENCE	13
4	OPTICAL SPECIFICATIONS	15
5	MECHANICAL CHARACTERISTICS	19
6	RELIABILITY	22
7	INTERNATIONAL STANDARDS	23
7-1	SAFETY	23
7-2	EMC	23
8	PACKING	24
8-1	DESIGNATION OF LOT MARK	24
8-2	PACKING FORM	24
9	PRECAUTIONS	25
9-1	MOUNTING PRECAUTIONS	25
9-2	OPERATING PRECAUTIONS	25
9-3	ELECTROSTATIC DISCHARGE CONTROL	26
9-4	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	26
9-5	STORAGE	26
9-6	HANDLING PRECAUTIONS FOR PROTECTION FILM	26
9-7	APPROPRIATE CONDITION FOR PUBLIC DISPLAY	27

CONTENTS

Product Specification

RECORD OF REVISIONS

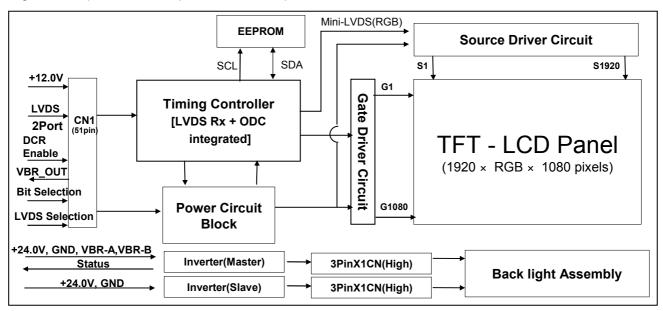
Revision No.	Revision Date	Page	Description
1.0	Aug, 11, 2008	-	Final Specification
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Ver. 1.0			3 /33

1. General Description

The LD470WUB is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 46.96 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 10-bit gray scale signal for each dot. Therefore, it can present a palette of more than 1.07 Billion colors.

It has been designed to apply the 10-bit 2-port LVDS interface.

It is intended to support Public Display where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	46.96 inch (1192.87mm) diagonal
Outline Dimension	1068.0(H) x 613.2.0 (V) x 54.7 mm(D) (Typ.)
Pixel Pitch	0.5415 mm x 0.5415 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	10Bit (D), 1.07 Billion colors
Luminance, White	700 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 219.7W (Typ.) [Logic=6.72 W, Backlight=213W(V _{BR-A} =1.65V)]
Weight	14.5 Кg (Тур.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 13%)
Possible Display Type	Landscape and Portrait Enabled

Ver. 1.0

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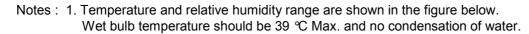
Product Specification

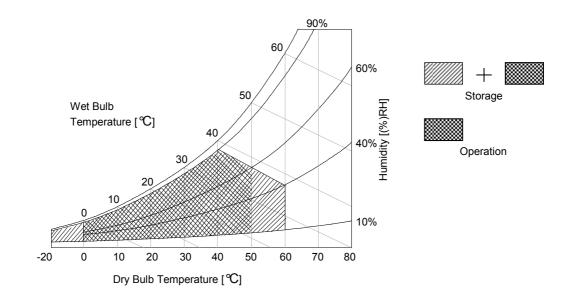
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol Value		Unit	Remark		
Γc	Faiailletei		Min	Min Max		Kendik	
Power Input	LCM	VLCD	-0.3	+14.0	VDC	at 25 ± 2 ℃	
Voltage	Voltage Backlight inverter		-0.3	+27.0	Vdc		
ON/OFF Con	ON/OFF Control Voltage		-0.3	+5.5	VDC		
Brightness C	Brightness Control Voltage		0	+5.0	VDC		
Operating Te	Operating Temperature		0	+50	°C		
Storage Tem	Storage Temperature		-20	+60	°C	Noto 1.2	
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2	
Storage Humidity		Hs⊤	10	90	%RH		





3. Electrical Specifications

3-1. Electrical Characteristics

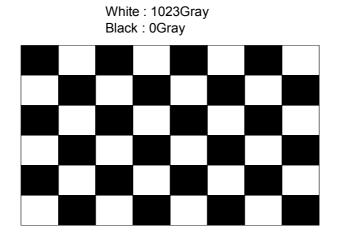
It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the CCFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note			
		Min	Тур	Max				
Circuit :	Circuit :							
Power Input Voltage	V _{LCD}	11.4	12.0	12.6	V _{DC}			
Dewer leput Current	I _{LCD}	470	560	650	mA	1		
Power Input Current		650	770	890	mA	2		
Power Consumption	P _{LCD}	-	6.72	8.20	Watt	1		
Rush current	I _{RUSH}	-	-	5	А	3		

Notes: 1. The specified current and power consumption are under the V_{LCD}=12.0V, 25 ± 2 °C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

- 2. The current is specified at maximum current pattern.
- 3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).



Mosaic Pattern(8 x 6)

Product Specification

Parameter		Symbol		Values			Notes	
i didificici			Symbol	Min	Тур	Max	Unit	Notes
Inverter :								
Power Supply Inp	Power Supply Input Voltage			22.8	24.0	25.2	Vdc	1
Power Supply Inp	out Voltage	Ripple		-	-	0.5	Vp-p	1
	After Aging	.	IBL_A	-	8.9	10.3	Α	VBR-A = 1.65V 1
Power Supply		J	IDL_A	-	10.0	11.0	Α	VBR-A = 3.3V 1
Input Current	Input Current	22		-	11.0	12.0	Α	VBR-A = 1.65V 2
	Before Aging		IBL_B	-	12.0	13.0	Α	VBR-A = 3.3V 2
Power Supply Inp	Power Supply Input Current (In-Rush)		Irush	-	-	14.0	A	VBL = 22.8V VBR-B = 3.3V VBR-A = 1.65V
Power Consumpt	ion		PBL	-	213	250	W	VBR-A = 1.65V 1
	Brightness	Adjust	Vbr-a	0.0	1.65	3.3	Vdc	
Input Voltage for Control System	On/Off	On	V on	2.5	-	5.0	Vdc	
Signals	On/Oπ	Off	V off	-0.3	0.0	0.8	Vdc	
Brightness Adjust		Vbr-b	0	-	3.3	V		
Lamp:								
Discharge Stabilization Time			Ts			3	min	3
Life Time				50,000			Hrs	4

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Notes :

The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend input Voltage is $24.0V \pm 5\%$.

2. Electrical characteristics are determined within 30 minutes at 25± 2℃. The specified currents are under the typical supply Input voltage 24V.

3. The brightness of the lamp after lighted for 5minutes is defined as 100%.

TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current. The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on. 4. Specified Values are for a single lamp which is aligned horizontally.

The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A : 1.65V & VBR-B :3.3V), on condition of continuous operating at 25± 2°C 5. The duration of rush current is about 10ms.

Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25± 2 °C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A: 1.65V & VBR-B: 3.3V), it is total power consumption. The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and Master 14-pin and Slave 12-pin connectors are used for the integral backlight system.

3-2-1. LCD Module

- LCD Connector(CN1): FI-RE51S-HF or Equivalent, Refer to below table.

- Mating Connector : FI-RE51HL

No	Symbol	Description	No	Symbol	Description
1	GND	Ground	27	Bit Select	'H' = 10Bit (D)
2	NC	No Connection	28	RA2N	SECOND CHANNEL A-
3	NC	No Connection	29	RA2P	SECOND CHANNEL A+
4	NC	No Connection	30	RB2N	SECOND CHANNEL B-
5	NC	No Connection	31	RB2P	SECOND CHANNEL B+
6	NC	No Connection	32	RC2N	SECOND CHANNEL C-
7	LVDS Select	'H' =JEIDA , 'L' = VESA	33	RC2P	SECOND CHANNEL C+
8	VBR_EXT	External VBR	34	GND	Ground
9	VBR_OUT	VBR output	35	RCLK2N	SECOND CLOCK CHANNEL CIK-
10	DCR_Enable	'H' = Enable , 'L' = Disable	36	RCLK2P	SECOND CLOCK CHANNEL CIk+
11	GND	Ground	37	GND	Ground
12	RA1N	FIRST CHANNEL A-	38	RD2N	SECOND CHANNEL D-
13	RA1P	FIRST CHANNEL A+	39	RD2P	SECOND CHANNEL D+
14	RB1N	FIRST CHANNEL B-	40	RE2N	SECOND CHANNEL E-
15	RB1P	FIRST CHANNEL B+	41	RE2P	SECOND CHANNEL E+
16	RC1N	FIRST CHANNEL C-	42	GND	Ground
17	RC1P	FIRST CHANNEL C+	43	GND	Ground
18	GND	Ground	44	GND	Ground
19	RCLK1N	FIRST CLOCK CHANNEL CIk-	45	GND	Ground
20	RCLK1P	FIRST CLOCK CHANNEL CIk+	46	GND	Ground
21	GND	Ground	47	NC	No connection
22	RD1N	FIRST CHANNEL D-	48	VLCD	Power Supply +12.0V
23	RD1P	FIRST CHANNEL D+	49	VLCD	Power Supply +12.0V
24	RE1N	FIRST CHANNEL E-	50	VLCD	Power Supply +12.0V
25	RE1P	FIRST CHANNEL E+	51	VLCD	Power Supply +12.0V
26	GND	Ground	-	-	-

Notes : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the IEA 664 Standard.
- 4. Specific pins(pin No. #2~#6) are used for internal data process of the LCD module. If not used, these pins are no connection.
- 5. Specific pin No. #44 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

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LD470WUB

Product Specification

3-2-2. Backlight Inverter

Master -Inverter Connector : S14B-PH-SMC (JST) or Equivalent - Mating Connector : PHR-14 or Equivalent

Slave

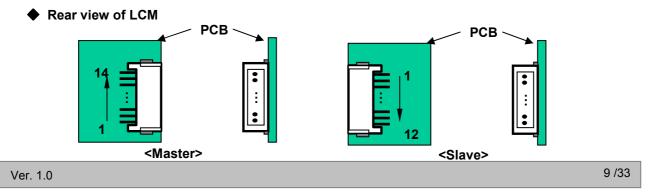
-Inverter Connector : S12B-PH-SMC (JST) or Equivalent -Mating Connector : PHR-12 or Equivalent

Table 5. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Master	Slave	Note
1	VBL	Power Supply +24.0V	VBL	VBL	
2	VBL	Power Supply +24.0V	VBL	VBL	
3	VBL	Power Supply +24.0V	VBL	VBL	
4	VBL	Power Supply +24.0V	VBL	VBL	
5	VBL	Power Supply +24.0V	VBL	VBL	
6	GND	Backlight Ground	GND	GND	
7	GND	Backlight Ground	GND	GND	
8	GND	Backlight Ground	GND	GND	1
9	GND	Backlight Ground	GND	GND	
10	GND	Backlight Ground	GND	GND	
11	Vbr-a	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	VBR-A	Don't care	2, 3
12	VON/OFF	0.0V ~ 5.0V	On/Off	Don't care	
13	Vbr-b	Burst dimming voltage DC 0.0V ~ 3.3V	VвR-в	-	3
14	Status	Normal : Upper 3.0V Abnormal : Under 0.7V	Status	-	4

Notes : 1. GND should be connected to the LCD module's metal frame.

- If Pin #11 is open, V_{BR-A} = 1.65V. When apply over 1.65V(~ 3.3V) continuously, its luminance is increasing however lamp's life time is decreasing.
 It could be usable for boost up luminance when using DCR (=Dynamic contrast ratio) function only.
- 3. Minimum Brightness : VBR-B = 0V Maximum Brightness : VBR-B = 3.3V
- 4. Even though Pin #14 is open, there is no effect on inverter operating, The output terminal of inverter.
- 5. Each impedance of pin #11,12 and 13 is $140[K\Omega]$, $41[K\Omega]$, $125[K\Omega]$



3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE (DE Only	Mode)
--------------------------------	-------

1	TEM	Symbol	Min	Тур	Мах	Unit	Note
	Display Period	tH∨	-	960	-	tclk	
Horizontal	Blank	tнв	72	140	320	tclk	
	Total	thp	1060	1100	1280	tclk	2200/2
	Display Period	tvv	-	1080	-	Lines	
Vertical	Blank	tvв	12	45	86	Lines	
	Total	tvp	1090	1125	1166	Lines	

п	EM	Symbol	Min	Тур	Мах	Unit	Note
	DCLK	fclk	70.5	74.25	78	MHz	148.5/2
Frequency	Horizontal	fH	64.1	67.5	70.9	KHz	
	Vertical	fv	57	60	63	Hz	

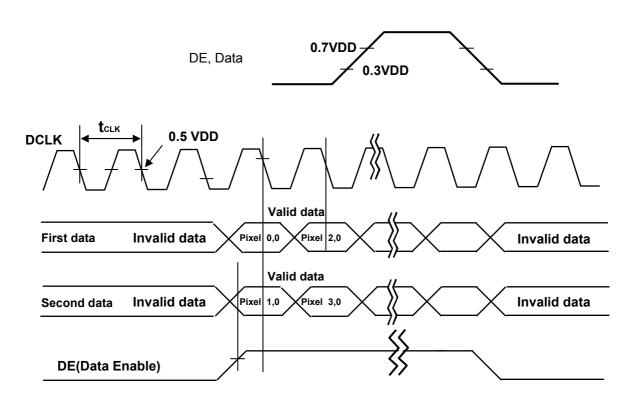
Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

Ver. 1.0

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3-4. Signal Timing Waveforms

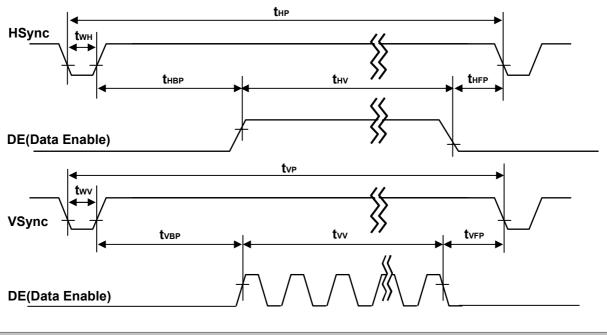


Product Specification

* Reference : Sync. Relation

* the = thee + twh +thee

* $t_{VB} = t_{VFP} + t_{WV} + t_{VBP}$



Ver. 1.0

Product Specification

3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 10-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 8 provides a reference for color versus data input.

Table 8.	COLOR DATA	REFERENCE
1 4010 01		

														۱r	ιpι	ut	Со	lor	Da	ata												
Co	olor	MSB				REI			LS			MS		0.0				EEN		0.0		LSB									LS	
	Γ	R9																				GO									B1	
	Black	0	0 	0 	0 	0 	0 • • • •	0	0 • • • •	0 	0 	0) 	0 	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (1023) Green	1 • • • • •	1 • • •	1 • • •	1 	1 • • • •	1 • • •	1	1 • • • •	1 	1 •••	0) 	0 • • •	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	(1023)	0	0 	0 	0	0 	0	0	0	0 	0 	1	1	1	1 	1	1 	1	1	1	1 	1	0	0	0	0	0	0	0	0		
Basic	Blue (1023)	0	0	0	0	0	0	0	0	0	0	0)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1 	1	1	
Color	Cyan	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	1	1	0	C)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	 1	1	1	1	1	1	1	1	1	1	1		1	1	0	0	0	0	0	0	0	0	0	0
	White	1	 1	 1	 1	 1	 1	1	 1	 1	 1	1	1	••• 1	 1	1				 1		1	1	1	 1		· · · 1	 1	···· 1		1	
	RED (000)	0	0	0	0	0	0	0 () (0	0	0	C)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0 (D (0	 1	0		•••)	 0	0	 0	 0	0	0		0	0	0	 0	 0	 0	 0	 0	 0	0	 0
RED			•••	• • •	• • •	•••	• • •	•••	•••	•••	•••		•••	••	•••		•••	•••	•••	••	• • • •			••••	•••	•••	•••	•••	•••	•••	• • • •	
	RED (1022)	· · · · · · 1	 1	 1	 1	 1	 1	1 1	 1	· · · 1 = 0	 0	0		•••)	 0	 0	 0				 0	0	0	0	 0	 0	 0	 0	 0	 0		 0
	RED (1023)			 1	 1	 1	 1	 1	 1	: 1	1 1	0		•••)	 0	0	 0						0		 0	 0	 0		 0	 0		 0
	GREEN (000)	-	0	0	0	0	0	0	0	0	0	0		_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	0
	GREEN (001)		 0	 0	 ^	 	 ^	 	••••	•••	 0		• •	 0		 0	 0				 0		 0	••••	 0	 0	 0	 ^	 0	 0		
GREEN			••••	••••	••••	••••	••••	••••	••••	••••	••••	.		•••	· · ·		••••				• • • •		ŀ		••••		•••			•••	••••	••••
GREEN	GREEN			••••	••••	••••	•••	••••	••••	••••	••••		• •					Ξ.		•••				••••	••••	••••	••••	••••	••••	••••	••••	••••
	(1022) GREEN		••••	0		••••		••••	••••	••••	0 	<mark>.</mark>											0	0	0		0	0			0	
	(1023)	0	0	0	0	0	0	0	0	0	0	1		1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	BLUE (000)	0	0 	0 	0	0 	0	0	0	0	0 	0)	0 	0	0	0	0	0	0	0	0	0	0	0				0	0	0
	BLUE (001)	⁰	0	0 	0	0	0	0	0	0	0 	0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																																
	BLUE (1022)	0	0	0	0	0	0	0	0	0	0	0	C)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0
	BLUE (1023)	0	0	0	0	0	0	0	0	0	0	0	()	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

Ver. 1.0

Product Specification

3-6. Power Sequence

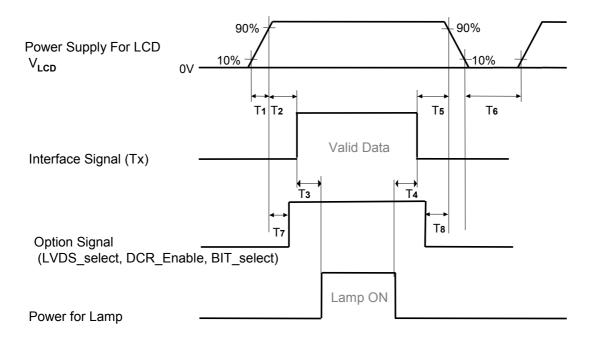


Table 9. POWER SEQUENCE

Deremeter		Value								
Parameter	Min	Тур	Max	Unit	Notes					
T1	0.5	-	20	ms						
T2	0	-	-	ms	3,5					
T3	200	-	-	ms	4					
T4	200	-	-	ms	4					
T5	0	-	-	ms	3,5					
T6	2.0	-	-	S	2,6					
T7	0	-	T2	ms	5					
Т8	0	-	-	ms	5					

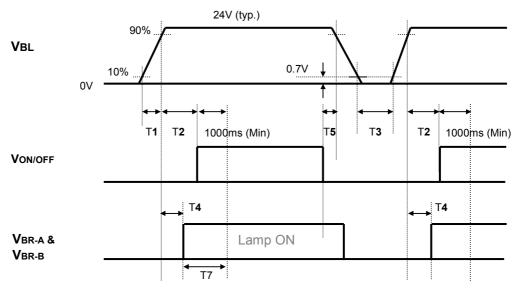
Note: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
- 3. The case when the T2/T5 exceed 3x(1/fv), it operates protection pattern (Black pattern) till valid signal inputted. There is no reliability problem. (ex. 60Hz : 3x(1/60Hz) = 50ms)
- 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 5. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V_{LCD}), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
- 6. T6 should be measured after the Module has been fully discharged between power off and on period.

Product Specification

3-6-2. Sequence for Inverter

Power Supply For Inverter



3-6-3. Deep condition for Inverter

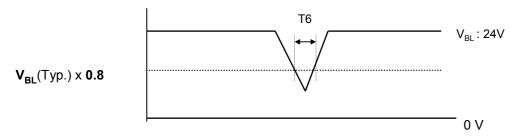


Table 10. Power Sequence for Inverter

Parameter		Values		Units	Remarks		
Falameter	Min	Тур	Max	Units	Remarks		
T1	20	-	-	ms	1		
T2	500	-	-	ms			
Т3	200	-	-	ms			
T4	0		-	ms	2		
T5	10	-	-	ms			
Т6	-	-	10	ms	V_{BL} (Тур) х 0.8		
T7	1000	-	-	ms	3		

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

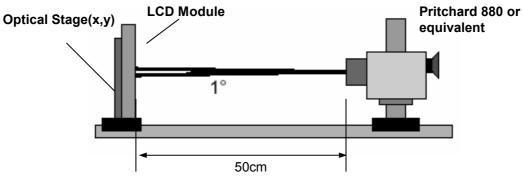
2. T4(max) is less than T2.

3. In T7 section, V_BR-B is recommended 3.3V & V_BR-A = 1.65V

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at 25 ± 2 °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.





Ta= 25± 2 °C, V_{LCD}=12.0V, fv=60Hz, Dclk=148.5MHz VBR_A=1.65V, VBR-B=3.3V

Table T1. UP	ICAL CHARACT	ERISTICS	DCIK-140.5WI12 VBK_A-1.05V, VBK-B-5.5V								
Dor	ameter	Symbol		Value	Unit	Note					
Par	amelei	Symbol	Min	Тур	Max	Unit	Note				
Contrast Ratio		CR	700	1000	-		1				
Surface Lumina	nce, white	L _{WH}	550	700	-	cd/m ²	2				
Luminance Varia	ation	δ _{WHITE} 5P	-	-	1.3		3				
Response Time	Gray-to-Gray	G to G	-	9	14	ms	4				
	RED	Rx		0.638							
	RED	Ry		0.333							
	GREEN	Gx		0.285							
Color Coordinate		Gy	Тур	0.610	Тур						
[CIE1931]	BLUE	Bx	-0.03	0.144	+0.03						
	BLUE	Ву		0.064							
	WHITE	Wx		0.279							
	VUILE	Wy		0.292							
Viewing Angle (CR>10)										
x a	ıxis, right(φ=0°)	θr	89	-	-						
x a	ıxis, left (φ=180°)	θΙ	89	-	-		_				
y a	ıxis, up (φ=90°)	θu	89	-	-	degree	5				
y a	ıxis, down (థ=270°)	θd	89	-	-						
Gray Scale			-	-	-		6				

Table 11. OPTICAL CHARACTERISTICS

Ver. 1.0

Product Specification

Notes :1. Contrast Ratio(CR) is defined mathematically as :

- CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
 - Surface Luminance at position n with all white pixels CRn =
 - Surface Luminance at position n with all black pixels
 - n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.
 - ※ DCR Application : Refer to Appendix IV

2. Surface luminance are determined after the unit has been 'ON' and 60min after lighting the backlight in a dark environment at 25±2 °C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.

- 3. The variation in surface luminance , δ WHITE is defined as : $\delta \text{ WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}}) / \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}})$ Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_{P}) and from G(M) to G(N) (Decay Time, Tr_{D}). For additional information see the FIG. 3. (N<M) * G to G Spec is average of measured time (N, M = 0 (Black) ~ 1023(White), 128 gray step).
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 12.

Gray Level	Luminance [%] (Typ.)
LO	0.12
L63	0.29
L127	1.10
L191	2.69
L255	5.12
L319	8.67
L383	13.3
L447	18.1
L511	23.7
L575	29.9
L639	36.9
L703	45.6
L767	55.4
L831	65.2
L895	76.2
L959	85.9
L1023	100

Table 12. GRAY SCALE SPECIFICATION

Measuring point for surface luminance & measuring point for luminance variation.

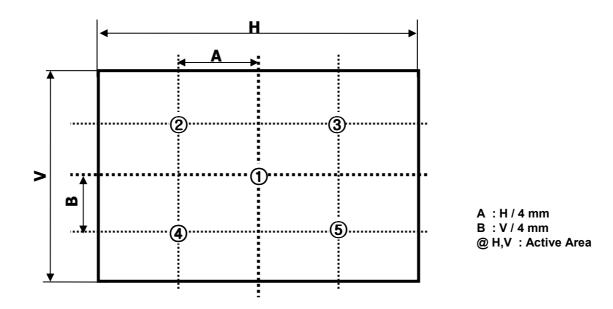
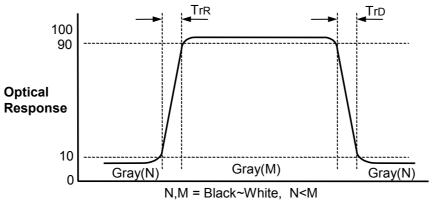


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".





Dimension of viewing angle range

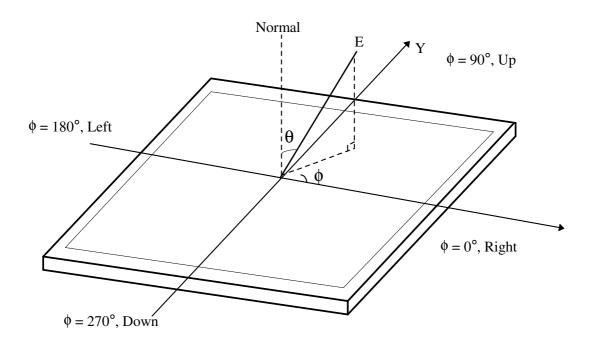


FIG. 4 Viewing Angle

5. Mechanical Characteristics

Table 13 provides general mechanical characteristics.

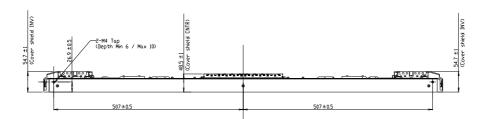
Item		Value			
	Horizontal	1068.0 mm			
Outline Dimension	Vertical	613.2 mm			
	Depth	54.7 mm			
Denal Area	Horizontal	1046 mm			
Bezel Area	Vertical	591.2 mm			
Active Dieplay Area	Horizontal	1039.68 mm			
Active Display Area	Vertical	584.82 mm			
Weight	14.5 Kg (Typ.), 16.5 Kg (Max.)				

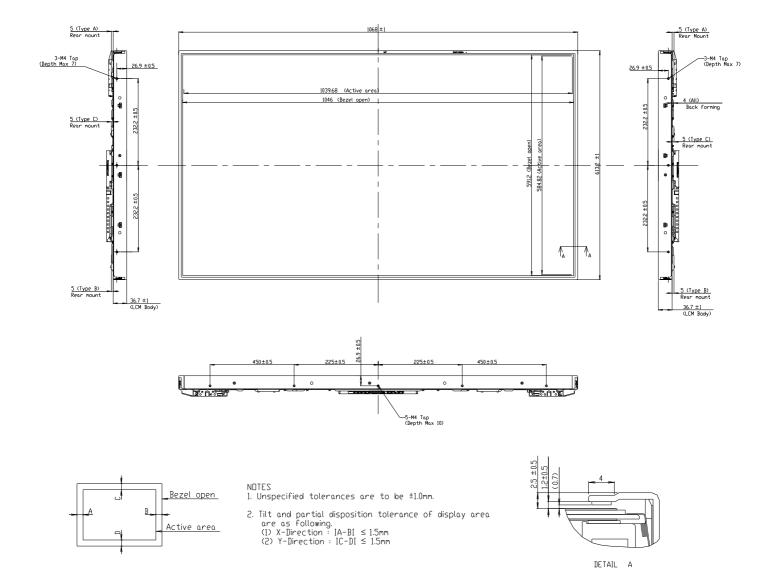
Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

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<FRONT VIEW>

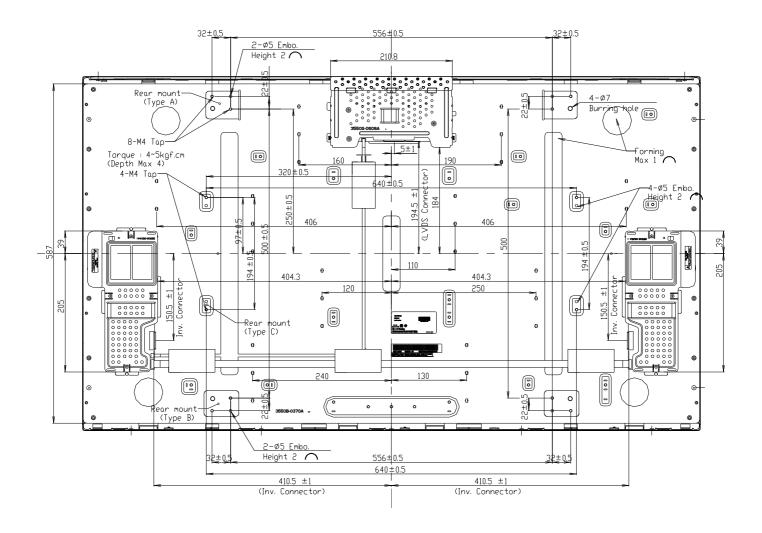




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Product Specification

<REAR VIEW>



6. Reliability

Table 14. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition						
1	High temperature storage test	Ta= 60 ℃ 240h						
2	Low temperature storage test	Ta= -20℃ 240h						
3	High temperature operation test	Ta= 50℃ 50%RH 240h						
4	Low temperature operation test	Ta= 0℃ 240h						
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction						
6	Shock test (non-operating)	Shock level :50G(X,Y axis) , 35G(Z axis) Waveform : half sine wave, 11ms Direction : ± X, ± Y, ± Z One time each direction						
7	Humidity condition Operation	Ta= 40 ℃, 90%RH						
8	Altitude operating storage / shipment	0 - 15,000 feet 0 - 40,000 feet						

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition, European Committee for Electrotechnical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.
- d) IEC 60950-1:2001, First Edition, The International Electrotechnical Commission (IEC) Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
 CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
 EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)

- D : YEAR
- F: PANEL CODE
- H : ASSEMBLY CODE

E : MONTH G : FACTORY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR										
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) Package quantity in one Pallet : 12 pcs
- b) Pallet Size : 1220mm(W) X 1140mm(D) X 860mm(H)

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
- And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.
- (11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

9-7. Appropriate Condition for Public Display

- Generally large-sized LCD modules are designed for consumer applications (TV).

Accordingly, a long-term display like in Public Display (PD) application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

- 1. Normal operating condition
 - Temperature: 0 ~ 40 °C
 - Operating Ambient Humidity : 10 ~ 90 %
 - Display pattern: dynamic pattern (Real display)

Note) Long-term static display can cause image sticking.

- 2. Operating usages under abnormal condition1
 - a. Ambient condition
 - Well-ventilated place is recommended to set up PD system.
- b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.[3.2)]

Ver. 1.0

Product Specification

- 3. Operating usages to protect against image sticking due to long-term static display
- a. Suitable operating time: under 18 hours a day.
- b. Static information display recommended to use with moving image.
- Cycling display between 5 minutes' information(static) display and 10 seconds' moving image.
- c. Background and character (image) color change
- Use different colors for background and character, respectively.
- Change colors themselves periodically.
- d. Avoid combination of background and character with large different luminance.
- 1) Abnormal condition just means conditions except normal condition.
- 2) Black image or moving image is strongly recommended as a screen save.
- 4. Lifetime in this spec. is guaranteed only when PD is used according to operating usages.

Product Specification

APPENDIX-I-1

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="L")

Host System	THC	63LVD103				
30 Bit	or C	ompatible				Timing
RED0	33					Controller
RED1	34		F	I-RE51S-	HF	
RED2	35					
RED3	36		31			
RED4	37	TA-		12	<u>100</u> Ω ≷	RA1N
RED5		TA+	30	13	100% <	RA1P
RED6						
RED7	61	TB-	29	14		RB1N
RED8	4		28		100Ω ≷	
RED9	5	TB+		15		- RB1P
GREEN0	40		05			
GREEN1	41	TC-	25	16	<u> </u>	RC1N
GREEN2	42	TC+	24	17	<u>100</u> Ω	RC1P
GREEN3	44					
GREEN4		TOUK	23	10		
GREEN5	46	TCLK-	22	19	<u>100</u> Ω ≷	RCLK1N
GREEN6	62	TCLK+		20	100% <	RCLK1P
GREEN7	63					
GREEN8	6	TD-	21	22	<u>_</u>	RD1N
GREEN9	8	TD+	20	23	<u>100</u> Ω ≷	RD1P
BLUE0	48	10.		20		
BLUE1	49		19			
BLUE2	50	TE-	18	24	4000	RE1N
BLUE3	52	TE+	10	25	<u>100</u> Ω ≷	RE1P
BLUE4	53					
BLUE5	54			7		VESA / JEIDA
BLUE6	64					
BLUE7	1					
BLUE8	9					
BLUE9	11					
Hsync	55					
Vsync	57		GND		LCM Module	
Data Enable	58		Ð			
	12					

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

Product Specification

APPENDIX-I-2

Required signal assignment for Flat Link (Thine : THC63LVD103) Transmitter(Pin7="H")

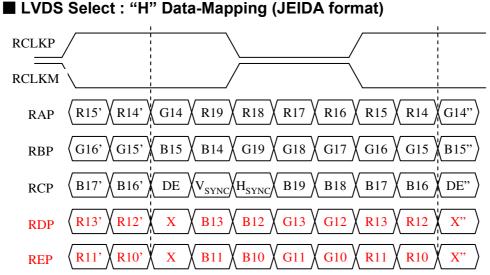
, , , , , , , , , , , , , , , , , , ,		63LVD103 ompatible		Timing		
RED0	- 4	•		I		Controller
RED0 RED1			FL	-RE51S-I	HF	Controller
RED1	59					
RED2 RED3	61					
RED3		TA-	31	12		RA1N
RED5	33		30		100Ω ≷	
RED5	34	TA+		13		RA1P
RED0 RED7	35		29			
RED7	30	TB-		14		RB1N
RED0 RED0	37	TB+	28	15	<u>100</u> Ω ≷	RB1P
GREEN0	$\frac{38}{6}$					
GREENU GREENI	8	TC-	25	16		RC1N
GREEN1	°		24		<u>100</u> Ω ≷	
GREEN2 GREEN3	63	TC+		17		RC1P
GREEN4	40		22			
GREEN4 GREEN5	40	TCLK-	23	19	>	RCLK1N
GREENS GREEN6	41	TCLK+	22	20	<u>100</u> Ω ≷	RCLK1P
GREEN7	42					
GREEN8	44	TD-	21	22		RD1N
GREEN9	45		20		<u>100</u> Ω ≷	
BLUE0	9	TD+	-	23	10032 2	RD1P
BLUE1	9 11		10			
BLUE2	64	TE-	19	24	>	RE1N
BLUE2 BLUE3	04 1	TE+	18	25	<u>100</u> Ω ≷	RE1P
BLUE3 BLUE4	48	_				
BLUE4 BLUE5	40			7		
BLUES BLUE6	49 50			7		VESA / JEIDA
BLUE7	50 52					
BLUE8	53					
BLUE8 BLUE9	53 54			1		
Hsync	54 55					
Vsync	55 57		VCC		LCM Module	
Data Enable	57 58					
	58 12					

Notes:

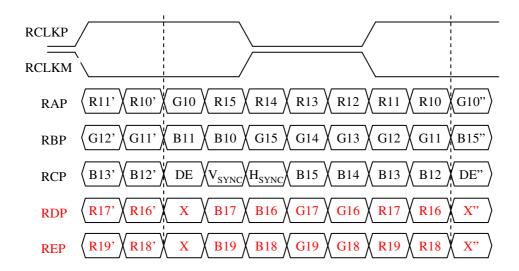
- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- II

LVDS Data-Mapping info. (10bit)



LVDS Select : "L" Data-Mapping (VESA format)

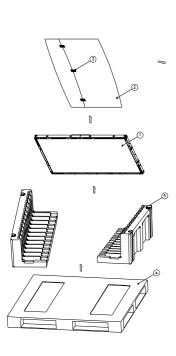


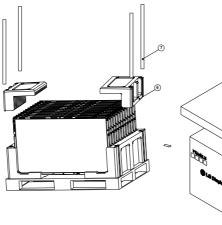
Ver. 1.0

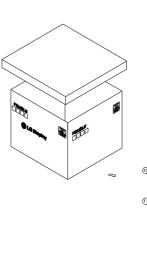
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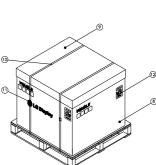
Product Specification





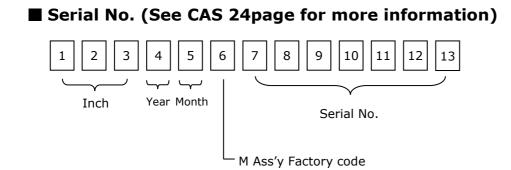






NO.	DESCRIPTION	MATERIAL
1	LCD Module	
2	BAG	47INCH
3	TAPE	MASKING 20MMX50M
4	PALLET	PAPER 1300X1140X130MM
5	PACKING,BOTTOM	EPS
6	PACKING,TOP	EPS
7	ANGLE,POST	PAPER
8	ANGLE, PACKING	PAPER
9	BAND,CLIP	STEEL
10	BAND	PP
11	LABEL	YUPO 80G 100X100







APPENDIX- IV Pallet Label

