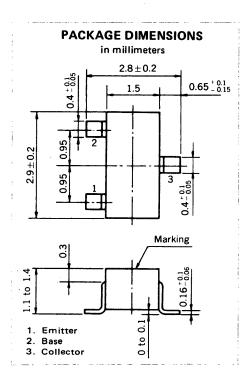


## SILICON TRANSISTOR

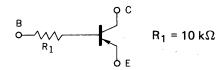
# FN1A4Z

### MEDIUM SPEED SWITCHING RESISTOR BUILT-IN TYPE PNP TRANSISTOR MINI MOLD



#### **FEATURES**

Resistor Built-in TYPE



Complementary to FA1A4Z

### ABSOLUTE MAXIMUM RATINGS

Maximum Voltages and Currents (T<sub>a</sub> = 25 °C) Collector to Base Voltage -60  $V_{CBO}$ Collector to Emitter Voltage **V<sub>CEO</sub>** -50Emitter to Base Voltage  $V_{EBO}$ -5 Collector Current (DC) -1001<sub>C</sub> mΑ Collector Current (Pulse) -200lc. mΑ **Maximum Power Dissipation Total Power Dissipation** at 25 °C Ambient Temperature 200 mW Maximum Temperatures Junction Temperature °C  $T_i$ 150 Storage Temperature Range -55 to +150 °C

### ELECTRICAL CHARACTERISTICS $(T_a = 25 \, ^{\circ}C)$

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Collector Cutoff Current	ICBO			-100	nΑ	V <sub>CB</sub> = -50 V, I <sub>E</sub> = 0	
DC Current Gain	hFE1*	135	190	600		$V_{CE} = -5.0 \text{ V, I}_{C} = -5.0 \text{ mA}$	
DC Current Gain	hFE2*	100	170			$V_{CE} = -5.0 \text{ V}, I_{C} = -50 \text{ mA}$	
Collector Saturation Voltage	VCE(sat)*		-0.07	-0.2	V	I <sub>C</sub> = -5.0 mA, I <sub>B</sub> = -0.25 mA	
Low-Level Input Voltage	V <sub>IL</sub> *		-0.57	-0.5	V	$V_{CE} = -5.0 \text{ V, I}_{C} = -100 \mu\text{A}$	
High-Level Input Voltage	V <sub>IH</sub> *	-2.0	-0.9		V	$V_{CE}$ = $-0.2$ V, $I_{C}$ = $-5.0$ mA	
Input Resistor	R <sub>1</sub>	7.0	10	13.0	kΩ		
Turn-on Time	ton			0.2	μs	$V_{CC}$ = -5 V, $V_{in}$ = -5 V $R_L$ = 1 kΩ $PW$ = 2 $\mu$ s, Duty Cycle $\leq$ 2 %	
Storage Time	t <sub>stg</sub>			5.0	μs		
Turn-off Time	toff			6.0	μs		

<sup>\*</sup> Pulsed: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2 %

#### h<sub>FE</sub> Classification

Marking	M67	M68	M69					
hFE1	135 to 270	200 to 400	300 to 600					

### TYPICAL CHARACTERISTICS (T<sub>B</sub> = 25 °C)

