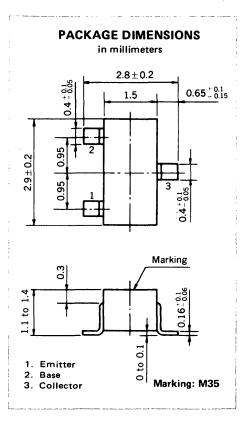
DATA SHEET



SILICON TRANSISTOR

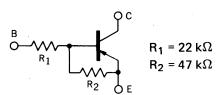
FN1F4N

MEDIUM SPEED SWITCHING RESISTOR BUILT-IN TYPE PNP TRANSISTOR MINI MOLD



FEATURES

Resistors Built-in TYPE



Complementary to FA1F4N

ABSOLUTE MAXIMUM RATINGS

Maximum Voltages and Currents ($T_a = 25^{\circ}$	C)		
Collector to Base Voltage	V _{CBO}	-60	V
Collector to Emitter Voltage	V _{CEO}	-50	V
Emitter to Base Voltage	V _{EBO}	5	V
Collector Current (DC)	I _C	-100	mΑ
Collector Current (Pulse)	۱ _C	-200	mΑ
Maximum Power Dissipation			
Total Power Dissipation			
at 25 °C Ambient Temperature	Ρ _T	200	mW
Maximum Temperatures			•
Junction Temperature	Тj	150	°C
Storage Temperature Range	T _{stg}	-55 to +150	°C

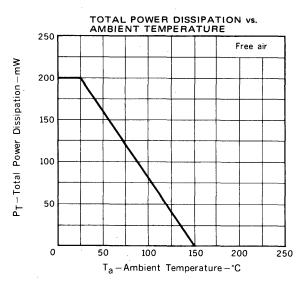
ELECTRICAL CHARACTERISTICS ($T_a = 25$ °C)

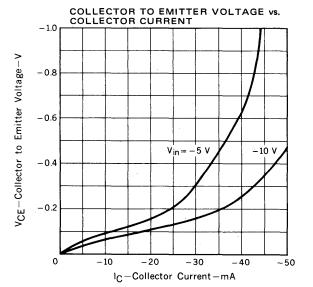
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cutoff Current	СВО			-100	nA	$V_{CB} = -50 V, I_E = 0$
DC Current Gain	^h FE1 [*]	85	150	340		$V_{CE} = -5.0 \text{ V}, 1_{C} = -5.0 \text{ mA}$
DC Current Gain	hFE2*	95	200			V _{CE} = -5.0 V, I _C = -50 mA
Collector Saturation Voltage	V _{CE(sat)} *		-0.04	-0.2	v	I _C = -5.0 mA, I _B = -0.25 mA
Low-Level Input Voltage	VIL*		0.85	-0.6	v	$V_{CE} = -5.0 \text{ V}, I_{C} = -100 \ \mu\text{A}$
High-Level Input Voltage	∨ін*	-3.0	-1.3		v	$V_{CE} = -0.2 \text{ V}, I_{C} = -5.0 \text{ mA}$
Input Resistor	R ₁	15.4	22.0	28.6	kΩ	
E-B Resistor	R ₂	32.9	47.0	61.1	kΩ	
Turn-on Time	t _{on}		0.2	0.3	μs	$V_{CC} = -5 V$, $V_{in} = -5 V$ $R_L = 1 k\Omega$ $PW = 2 \mu s$, Duty Cycle $\leq 2 \%$
Storage Time	t _{stg}		1.5	5.0	μs	
Turn-off Time	t _{off}		2.0	6.0	μs	

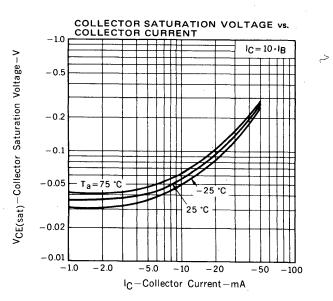
* Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2 %

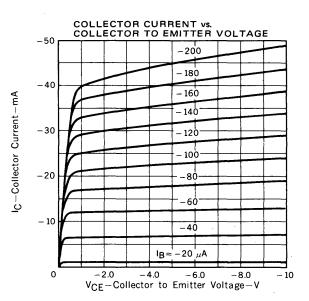
NEC cannot assume any responsibility for any circuits shown or represent that they are free from patent infringement.

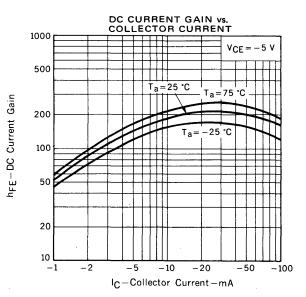
TYPICAL CHARACTERISTICS ($T_{B} = 25$ °C)

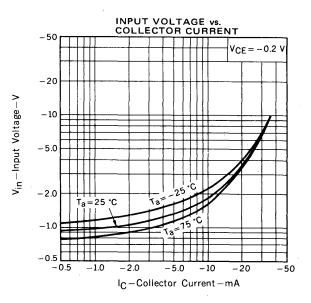




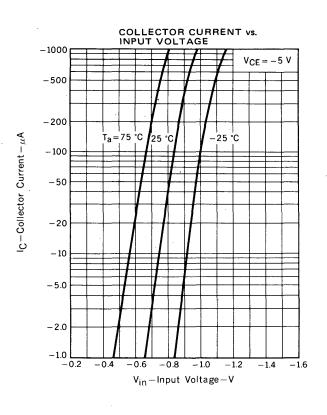


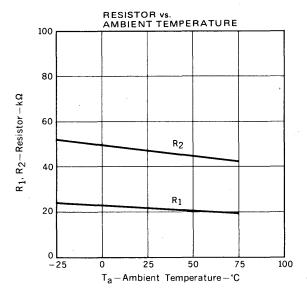


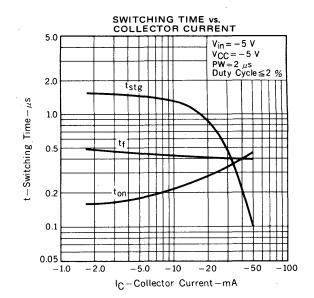




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