TOSHIBA

MICROWAVE SEMICONDUCTOR TECHNICAL DATA

ΑΤΑ

FEATURES

- HIGH POWER Pout=47.0dBm at Pin=41.0dBm
- HIGH GAIN GL=9.0dB at 8.5GHz to 9.6GHz

MICROWAVE POWER GaN HEMT TGI8596-50

■ BROAD BAND INTERNALLY MATCHED HEMT HERMETICALLY SEALED PACKAGE

RF PERFORMANCE SPECIFICATIONS (Ta= 25°C)

CHARACTERISTICS	SYMBOL	CONDITIONS	UNIT	MIN.	TYP.	MAX.
Output Power	Pout	VDS= 24V	dBm	46.0	47.0	—
Drain Current	IDS1	IDSset≅1.5A	А	-	5.0	6.0
Power Added Efficiency	ηadd	f = 8.5 to 9.6GHz	%	-	31	—
		@Pin = 41dBm				
Linear Gain	GL	@Pin = 20dBm	dB	7.0	9.0	_
Channel Temperature Rise	∆Tch	(VDS X IDS1 + Pin – Pout)X Rth(c-c)	°C	I	130	150

Recommended gate resistance(Rg) : Rg= 13.3 Ω(TYP.)

ELECTRICAL CHARACTERISTICS (Ta= 25°C)

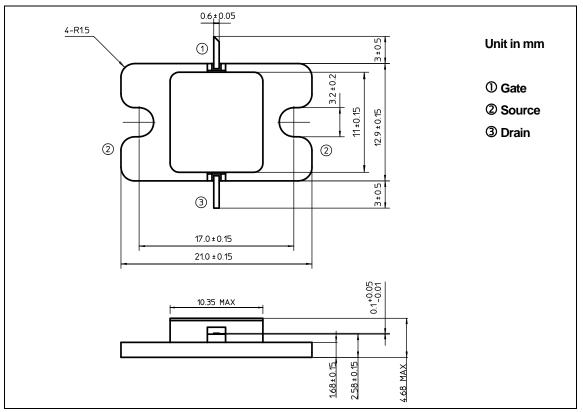
CHARACTERISTICS	SYMBOL	CONDITIO	NS U	JNIT	MIN.	TYP.	MAX.
Transconductance	gm	VDS= 5V		S	_	4.5	
		IDS= 5.0A					
Pinch-off Voltage	VGSoff	VDS= 5V		V	-1	-4	-6
		IDS= 23mA					
Saturated Drain Current	IDSS	VDS= 5V		А		15	
		VGS= 0V					
Gate-Source Breakdown	VGSO	IGS= -10mA		V	-10		
Voltage							
Thermal Resistance	Rth(c-c)	Channel to Case	e •(C/W	_	_	1.6

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' TGI8596-50 '

CHARACTERISTICS	SYMBOL	UNIT	RATING
Drain-Source Voltage	VDS	V	50
Gate-Source Voltage	VGS	V	-10
Drain Current	IDS	А	15
Total Power Dissipation (Tc= 25 °C)	PT	W	140
Channel Temperature	Tch	°C	250
Storage	Tstg	°C	-65 to +175

PACKAGE OUTLINE (7- AA04A)



HANDLING PRECAUTIONS FOR PACKAGE MODEL

Soldering iron should be grounded and the operating time should not exceed 10 seconds at 260°C.