

FX606

N-Channel Silicon MOSFET

Ultrahigh-Speed Switching Applications

Features

- · Composite type composed of two low ON-resistance N-channel MOSFET chips for ultrahigh-speed switching and low-voltage drive.
- · Facilitates high-density mounting.

Switching Time Test Circuit

 $I_D = 1A$

RL=300

V_{DD}=30V

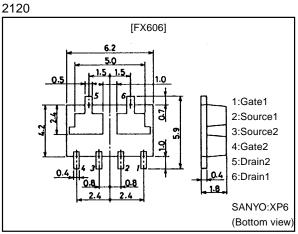
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FX606 S

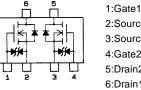
- \cdot The FX606 is formed with two chips, each being
- equivalent to the 2SK1470, placed in one package.
- · Matched pair characteristics.

Package Dimensions

unit:mm



Electrical Connection



2:Source1 3:Source2 4:Gate2 5:Drain2 6:Drain1

(Top view)

Specifications

\$50Ω

Vin

Vin

PW=10#s D.C.≦1%

18% Л

PG

(л)

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		60	V
Gate-to-Source Voltage	V _{GSS}		±15	V
Drain Current (DC)	۱D		2	A
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	8	А
Allowable Power Dissipation	PD	Tc=25°C, 1 unit	6	W
	PD	Mounted on ceramic board (750mm ² ×0.8mm) 1 unit	1.5	W
Total Dissipation	PT	Mounted on ceramic board (750mm ² ×0.8mm)	2	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

· Marking:606

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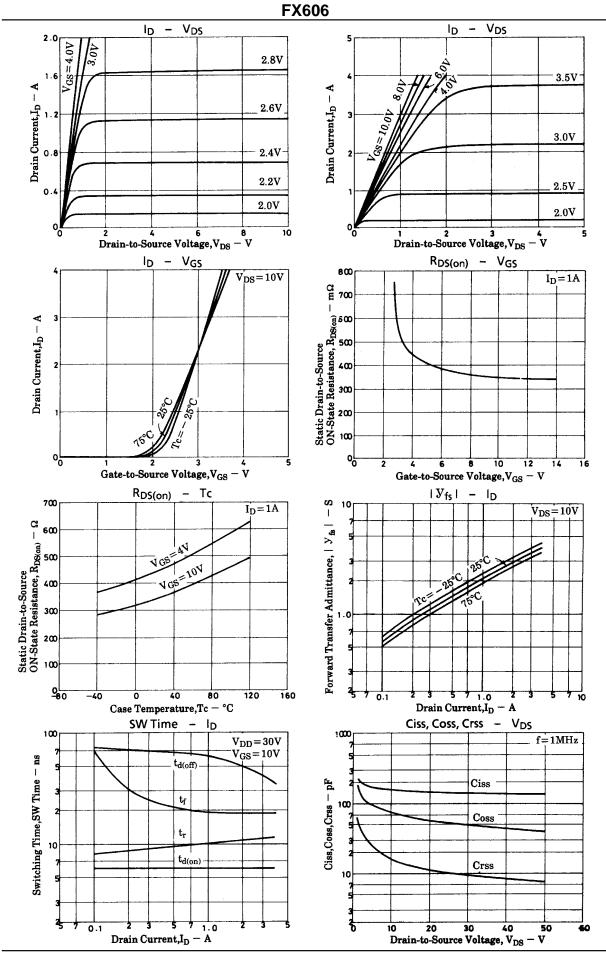


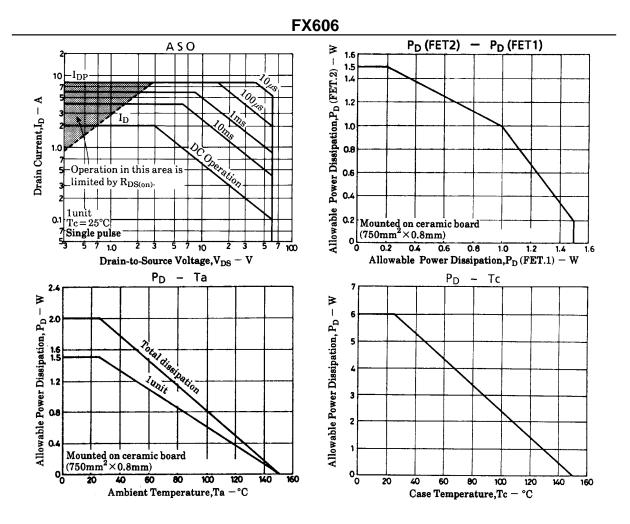
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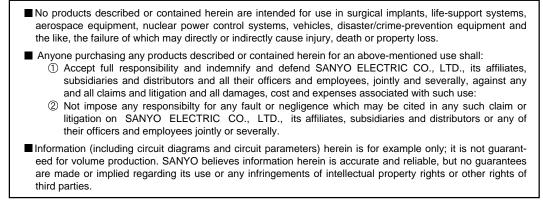
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Electrical Characteristics at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
D-S Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	60			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =60V, V _{GS} =0			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±12, V _{DS} =0			±10	μA
Cutoff Voltage	VGS(off)	V _{DS} =10V, ID=1mA	1.0		2.0	V
Forward Transfer Admittance	Y _{fs}	V _{DS} =10V, I _D =1A	1.2	2.0		S
Static Drain-to-Source ON-State Resistance	R _{DS(on)}	I _D =1A, V _{GS} =10V		0.35	0.45	Ω
	R _{DS(on)}	ID=1A, VGS=4V		0.45	0.6	Ω
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		150		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		60		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz		12		pF
Turn-ON Delay Time	^t d(on)	See specified Test Circuit		6		ns
Rise Time	tr	See specified Test Circuit		10		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		60		ns
Fall Time	t _f	See specified Test Circuit		20		ns
Diode Forward Voltage	V _{SD}	I _S =1.2A, V _{GS} =0		1.0		V







This catalog provides information as of May, 1998. Specifications and information herein are subject to change without notice.