

HZM6.8ZMFA

Silicon Planar Zener Diode for Surge Absorb

REJ03G1211-0300
(Previous: ADE-208-783B)

Rev.3.00

Jun 14, 2005

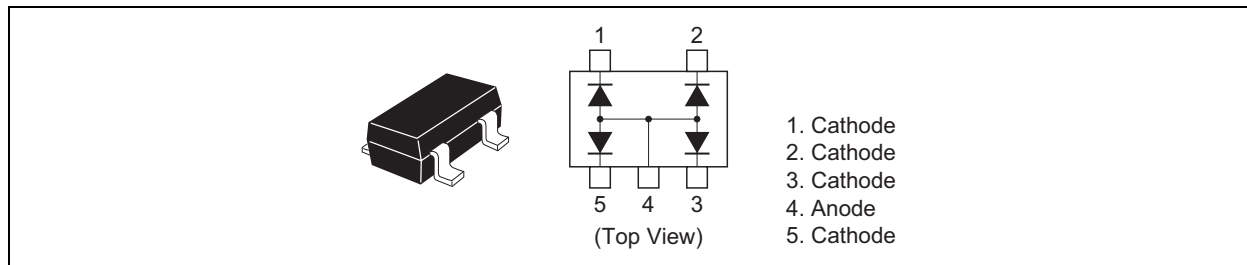
Features

- HZM6.8ZMFA has four devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 25 \text{ pF max}$) and can protect ESD of signal line.
- MPAK-5 Package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code (Previous Code)
HZM6.8ZMFA	68N	MPAK-5	PLSP0005ZC-A (MPAK-5)

Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	200	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	Vz	6.47	—	7.00	V	Iz = 5 mA, 40 ms pulse
Reverse current	IR	—	—	2	μA	VR = 3.5 V
Capacitance	C	—	—	25	pF	VR = 0 V, f = 1 MHz
Dynamic resistance	rd	—	—	30	Ω	Iz = 5 mA
ESD-Capability *2	—	25	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse.

Notes: 1. Per one device

2. Failure criterion ; IR > 2 μA at VR = 3.5 V.

Main Characteristic

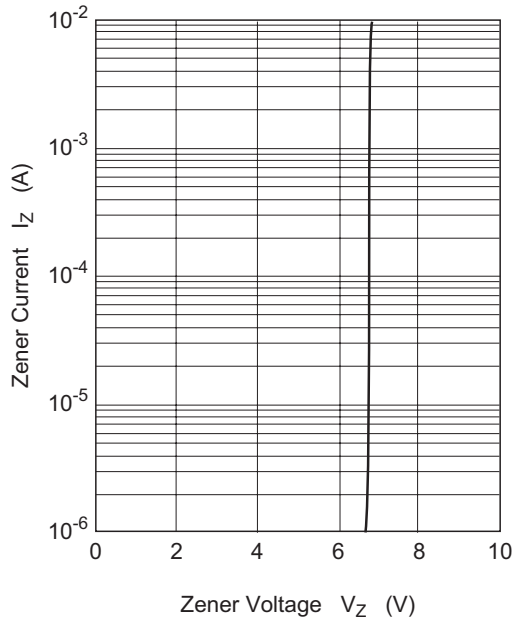


Fig.1 Zener current vs. Zener voltage

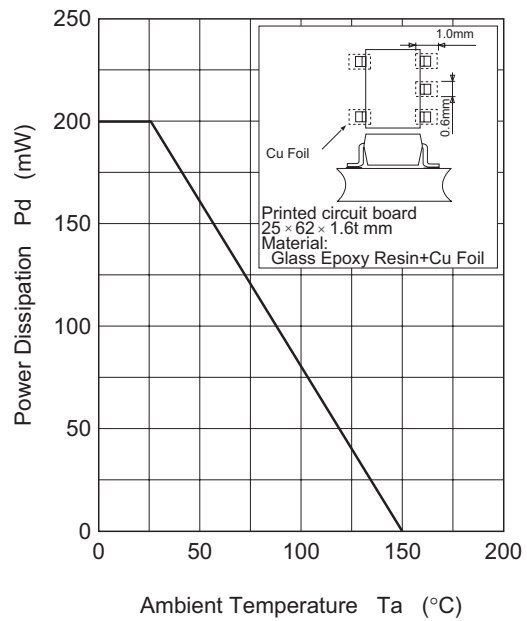


Fig.2 Power Dissipation vs. Ambient Temperature

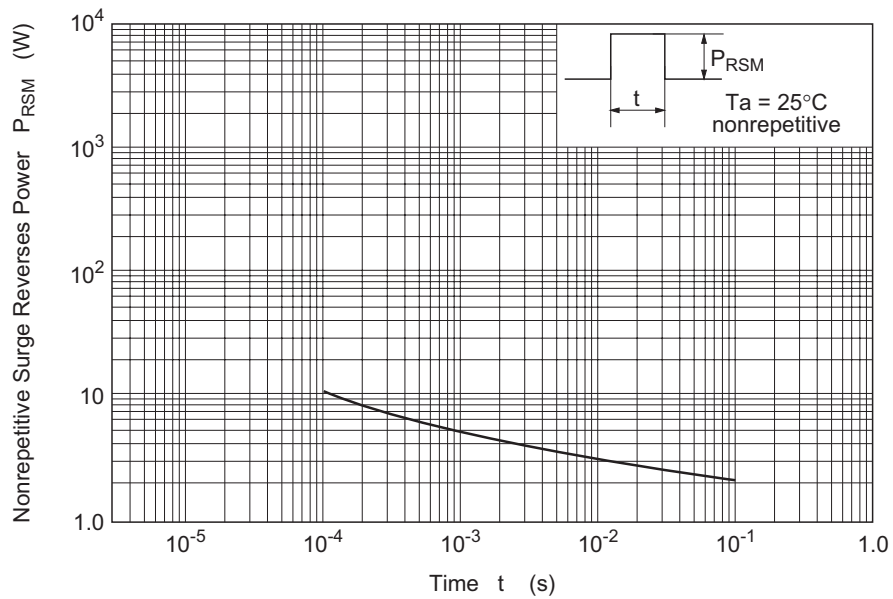


Fig.3 Surge Reverse Power Ratings

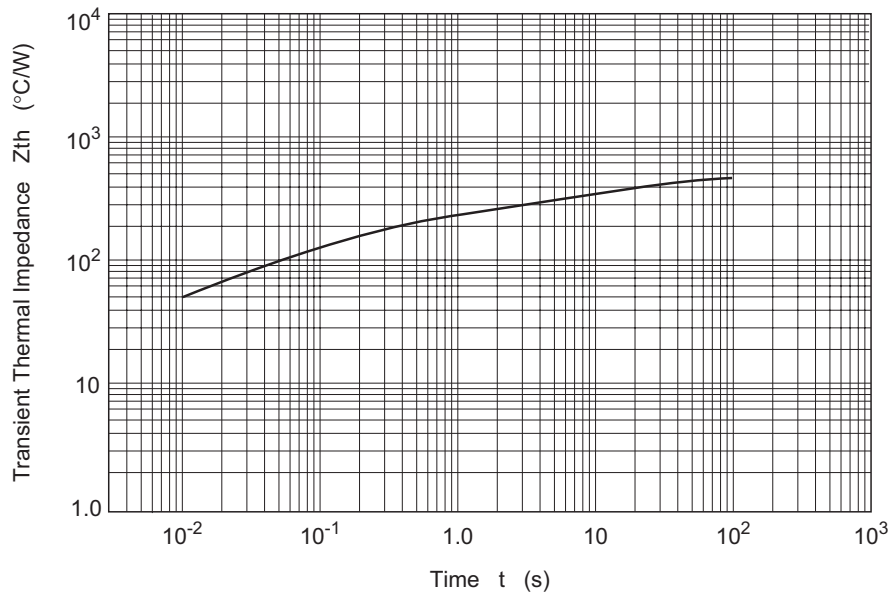
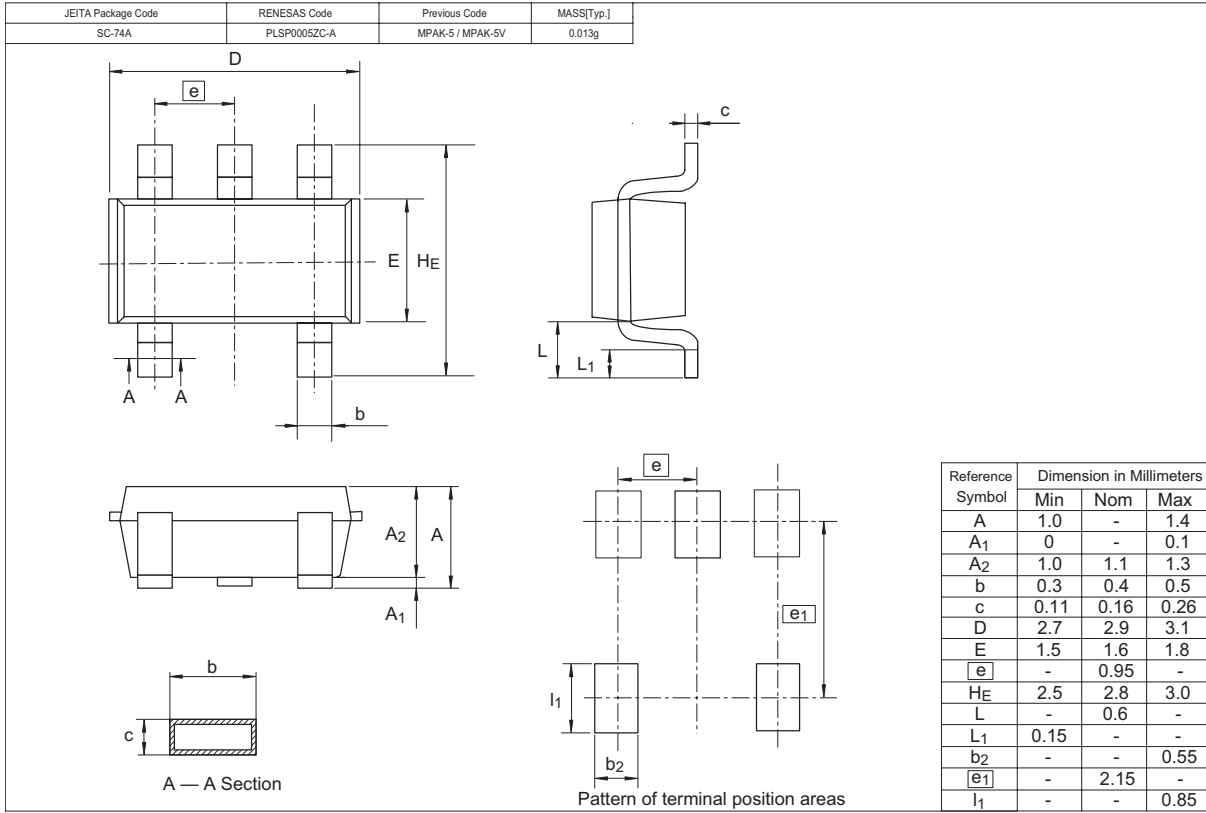


Fig.4 Transient Thermal Impedance

Package Dimensions



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Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.

7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.

10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.

Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

