

# COMMON MODE CHOKE

## P/N: 8STA DATA SHEET

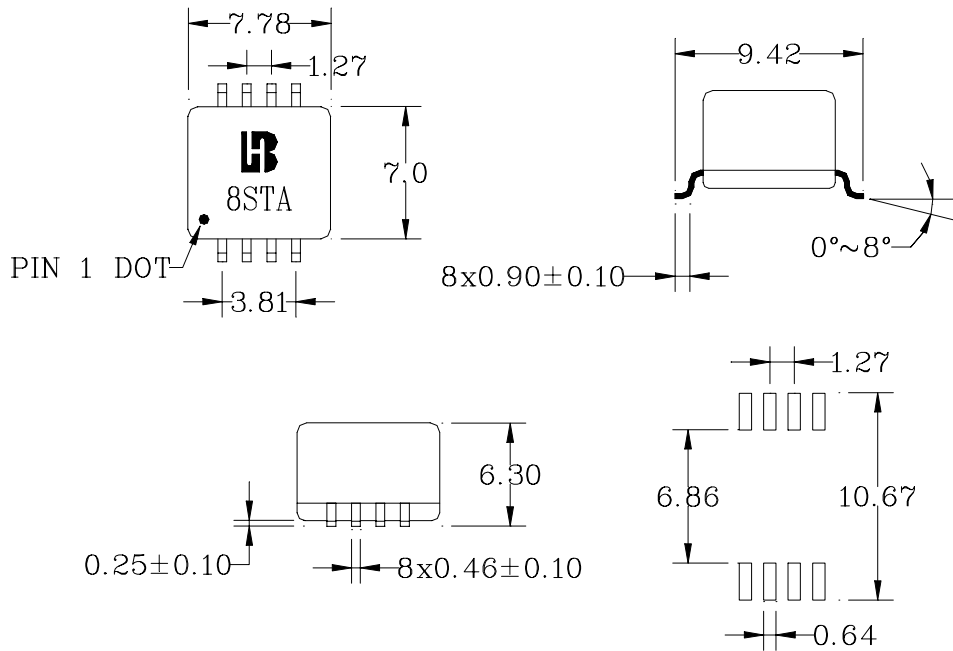
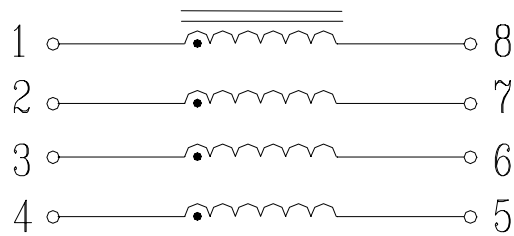
### A. GENERAL SPECIFICATIONS:

1. USED IN SUPPRESSION OF EMI NOISE.
2. FOR ETHERNET, TOKEN RING.

### B. ELECTRICAL SPECIFICATIONS: (25° C)

1. PRIMARY INDUCTANCE (Lp) : 18uH MIN @ 100KHz/0.1V
2. LEAKAGE INDUCTANCE(L.L) : 0.2uH Max @100KHz/0.1V
3. Cw/w: 10pF Max @100KHz/0.1V
4. TURN RATIO : 1:1:1:1
5. DCR: 0.25 OHM MAX
6. HI-POT: 500 Vrms/0.5mA 1Sec.

### C. DIMENSIONS & SCHEMATIC & MARKING (UNIT: mm)



Tolerance : ±0.25

PCB suggested pad layout

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Date : 2000/8/23

Drawing no.: Pulse Transformer

REV.:0

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