

- Pletronics' VPB7 Series is a voltage quartz crystal controlled precision square wave generator with a PECL output.
- Tape and Reel or cut tape packaging.

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- 10.9 MHZ to 1.17 GHz
- Enable/Disable Function on pad 2
- Output frequency is synthesized.
- Low Jitter
- RoHS 6/6 Compliant



# Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 2.18 grams or .82 grams or 1.51 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

#### **Absolute Maximum Ratings:**

Parameter	Unit
V <sub>cc</sub> Supply Voltage	-0.5V to +4.6V
Vi Input Voltage	-0.5V to V <sub>cc</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>cc</sub> + 0.5V
I <sub>o</sub> Output Current	-50mA

### **Thermal Characteristics**

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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#### Part Number:

VPB7029036	EG	000	050	- 312.5M	-XX	
						Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
						Frequency in MHZ
						Pullability in ppm (Vcontrol) APR 050 = ± 50 ppm minimum is standard
						Series Model
						<b>Temperature Range</b> <b>EG</b> = -10 to +70°C <b>LK</b> = -40 to +85°C
						Series Model

### **Part Marking:**

PLE VPB7	Marking Legend:				
FF.FFF M	PLE = Pletronics				
<ul> <li>YMDXX</li> </ul>	<i>FF.FFF</i> M = Frequency in MHZ				
	YMD = Date of Manufacture (year-month-day)				
	All other marking is internal factory codes				

#### Codes for Date Code YMD

Code	7		8		9	0		1		2				
Year	2007	2	008	_	009	2010	20	)11		2012				
Code	Δ	<b>`</b>	В	С	D	E	F	G		н	J	ĸ	L	М
Month	JA	N F	EB	MAR	APF	R MAY	′ JUN	JU	L	AUG	SEP	OCT	NOV	DEC
Code	1	2		3	4	5	6	7		8	9	Α	в	С
Day	1	2		3	4	5	6	7		8	9	10	11	12
Code	D	Е	I	F	G	н	J	K		L	М	Ν	Р	R
Day	13	14	1	5	16	17	18	19		20	21	22	23	24
Code	Т	U	'	V	W	Х	Y	Z						
Day	25	26	2	27	28	29	30	31						

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### Electrical Specification for 3.30V ±10% over the specified temperature range and the frequency range of 10.9 MHZ to 766 MHZ and 876 MHZ to 1,175MHz

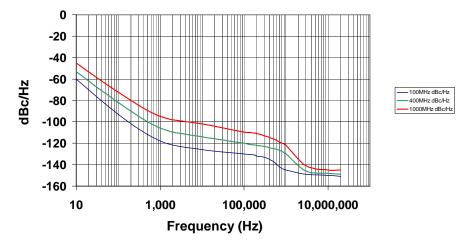
Item	Min	Max	Unit	Condition
Pullability, Absolute Pull Range	-50	+50	ppm	APR includes the effect of temperature stability, aging, supply voltage and load.
Output Waveform		PECL / E	ECL	
Output High Level	2.12	2.49	volts	Referenced to Ground, $V_{CC} = 3.3 V$
	0.82	1.19	volts	Referenced to termination voltage, $V_{CC}$ = 3.3 V
	-1.18	-0.81	volts	Referenced to Vcc, $V_{cc} = 3.3 V$
Output Low Level	1.83	1.99	volts	Referenced to Ground, $V_{CC}$ = 3.3 V
	0.53	0.69	volts	Referenced to termination voltage, $V_{CC}$ = 3.3 V
	-1.47	-1.31	volts	Referenced to Vcc, $V_{cc} = 3.3 V$
Output Symmetry	47	53	%	at 50% point of $V_{cc}$ (See load circuit)
Modulation Bandwidth	10	-	KHz	Vcontrol = 1.65V <u>+</u> 1.50 V , -3dB
Vcontrol Resistance (Pad 1)	20	-	Kohm	
Voltage vs Frequency Linearity	-10	+10	%	Vcontrol = 1.65V <u>+</u> 1.50 V
Jitter	-	0.8	pS RMS	12 KHz to 20 MHZ from the output frequency
	-	3.2	pS RMS	10 Hz to 20 MHZ from the output frequency
Output $T_{RISE}$ and $T_{FALL}$	100	300	pS	Vth is 20% and 80% of waveform
V <sub>cc</sub> Supply Current (I <sub>cc</sub> )	-	100	mA	
Enable/Disable Internal Pull-up	50	-	Kohm	to V <sub>cc</sub>
V disable	-	0.8	volts	Referenced to pad 3
V enable	2.00	-	volts	Referenced to pad 3
Output leakage $V_{OUT} = V_{CC}$	-50	+50	uA	Pad 1 low, device disabled
$V_{OUT} = 0V$	-50	+50	uA	
Enable time	-	10	nS	Time for output to reach a logic state
Disable time	-	10	nS	Time for output to reach a high Z state
Start up time	-	5	mS	Time for output to reach specified frequency
Operating Temperature Range	-10	+70	°C	Standard Temperature Range
	- 40	+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55	+125	°C	

Specifications with Pad 2 E/D open circuit or connected to  $V_{cc}$ 

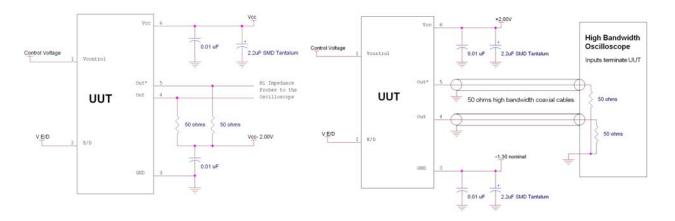


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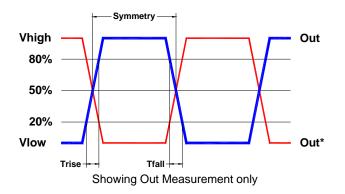
### **Typical Phase-Noise Response**



### Load Circuit







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### Reliability: Environmental Compliance

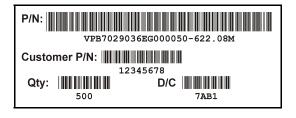
Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

### **ESD** Rating

Model	Minimum Voltage	Conditions		
Human Body Model	2000	MIL-STD-883 Method 3115		
Charged Device Model	1500	JESD 22-C101		

### Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII



Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

**RoHS** Compliant

2nd LvL Interconnect

Category=e4 Max Safe Temp=260C for 10s 2X Max

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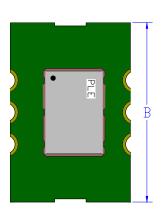
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#### Mechanical:



FR4 PCB Base:

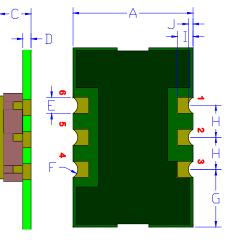
Label:

FR4 base

Solder masked

All via holes tented on bottom Copper Clad ½ oz. Typical Gold plated 0.02 μinch (0.5 μm)

Laser engraved on the 5x7 mm oscillator that is mounted on the



Pin 3 Ground plane is typical

Not to scale

1		
	Inches	mm
А	0.380 <u>+</u> 0.010	9.65 <u>+</u> 0.25
В	0.550 <u>+</u> 0.010	13.97 <u>+</u> 0.25
С	0.177 <u>+</u> 0.010	4.50 <u>+</u> 0.25
D <sup>1</sup>	0.026 typ.	0.66
E1	0.050	1.27
F <sup>1</sup>	0.028 R	0.72 R
G1	0.180	4.57
H <sup>1</sup>	0.100	2.54
I <sup>1</sup>	0.050	1.27
$J^1$	0.015	0.38

<sup>1</sup> Typical Dimensions

Pad	Function	Note
1	Vcontrol	Modulates the output frequency
2	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.80 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{cc}$ if the oscillator is to be always on.
3	Ground (GND)	
4	Output	Both outputs must be terminated and biased for proper operation. The ideal termination is 50 ohms connected to 2.0V below the Supply Voltage.
5	Output*	The outputs become a High Z when disabled and the voltage level is determined by the termination circuitry.
6	Supply Voltage (V <sub>cc</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.

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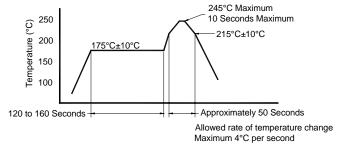
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Downloaded from Elcodis.com electronic components distributor



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### Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

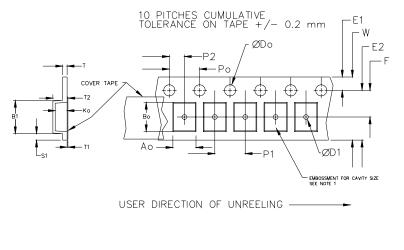
#### Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

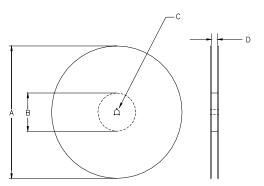
Constant Dimensions Table 1									
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max	
8mm		1.0			2.0				
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05				
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1	
24mm		1.5			<u>+</u> 0.1				

Variable Dimensions Table 2									
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko		
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1		

Note 1: Embossed cavity to conform to EIA-481-B Di







		REE			
A	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
в	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Width
с	mm	13	width		
D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0
	mm			24.4 +2.0 -0.0	24.0
	mm			32.4 +2.0 -0.0	32.0

#### Reel dimensions may vary from the above

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