

Vishay Semiconductors

6-Channel EMI-Filter with ESD-Protection

Features

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- Ultra compact LLP2513-DL package
- Low package profile of 0.6 mm
- 6-channel EMI-Filter
- · Low leakage current
- Line resistance $R_S = 100 \Omega$
- Typical cut off frequency f_{3dB} = 130 MHz
- ESD-protection to IEC 61000-4-2
 - ± 18 kV contact discharge
 - ± 25 kV air discharge
- Lead (Pb)-free component
- "Green" molding compound
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

Marking



DOT = Pin 1 marking 9S = Type code UO = Date code

Ordering Information

Device name Ordering code		Taped units per reel (8 mm tape on 7" reel)	Minimum order quantity		
VEMI65AB-HCI	VEMI65AB-HCI-GS08	3000	15000		

Package Data

Device name	Package name	Marking code	Weight	Molding compound flammability rating	Moisture sensitivity level	Soldering conditions
VEMI65AB-HCI	LLP2513-DL	9S	5.5 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

Absolute Maximum Ratings

Parameter	Test conditions	Symbol	Value	Unit	
Peak pulse current	all I/O pin to pin 13; acc. IEC 61000-4-5; t _p = 8/20 µs; single shot	I _{PPM}	4	A	
ESD immunity	contact discharge acc. IEC61000-4-2; 10 pulses	M	± 18	kV	
	air discharge acc. IEC61000-4-2; 10 pulses	V ESD	± 25		
Operating temperature	junction temperature	ТJ	- 40 to + 125	°C	
Storage temperature		T _{STG}	- 55 to + 150	°C	

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Application Note:

With the **VEMI65AB-HCI** 6 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (**BiAs**).







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The 6 independent EMI-Filter are placed between

pin 1 and pin 12, pin 2 and pin 11, pin 3 and pin 10, pin 4 and pin 9, pin 5 and pin 8 and pin 6 and pin 7.

They all are connected to a common ground pin 13 on the backside of the package.

The circuit diagram of one EMI-Filter-Channel shows two identical Z-Diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level (V_{BR}) and the diode capacitance (C_D). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance R_S between input and output the device works as a low pass filter. Low frequency signals (f < f_{3dB}) pass the filter while high frequency signals (f > f_{3dB}) will be shorted to ground through the diode capacitances C_D .



Each filter is symmetrical so that both ports can be used as input or output.

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2







Electrical Characteristics

Ratings at 25 °C, ambient temperature unless otherwise specified

VEMI65AB-HCI

All inputs (pin 1, 2, 3, 4, 5 and 6) to ground (pin 13)

Parameter	Test conditions/remarks	Symbol	Min.	Тур.	Max.	Unit
Filter channels	number of channels which can be protected	N _{channel}			6	channel
Reverse stand off voltage	at I _R = 1 μA	V _{RWM}	5			V
Reverse current	at V _R = V _{RWM}	I _R		0.25	1	μA
Reverse break down voltage	at I _R = 1 mA	V_{BR}	6			v
Pos. clamping voltage	at I _{PP} = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}			7	v
	at $I_{PP} = I_{PPM} = 4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}			8	V
Neg. clamping voltage	at I _{PP} = - 1 A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1			V
	at $I_{PP} = I_{PPM} = -4$ A applied at the input, measured at the output; acc. IEC 61000-4-5	V _{C-out}	- 1.2			v
Input capacitance	at V _R = 0 V; f = 1 MHz	C _{in}		40	45	pF
	at V _R = 2.5 V; f = 1 MHz	C _{in}		24	28	pF
ESD-clamping voltage	at ± 18 kV ESD-pulse acc. IEC 61000-4-2	V_{CESD}		7.5		V
Line resistance	measured between input and output; $I_S = 10 \text{ mA}$	R _S	90	100	110	Ω
Cut-off frequency	V_{IN} = 0 V; measured in a 50 Ω system	f _{3dB}		130		MHz

Typical Characteristics T_{amb} = 25 °C, unless otherwise specified



acc. IEC 61000-4-2 (330 Ω/150 pF)



Figure 2. 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

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Figure 5. Typical Peak Clamping Voltage V_C vs. Peak Pulse Current $\rm I_{PP}$



Figure 6. Typical Input Capacitance $\rm C_{IN}$ vs. Input Voltage $\rm V_{IN}$



Figure 7. Typical Small Signal Transmission (S21) at $\mbox{ } \mbox{Z}_{\mbox{O}}$ = 50 Ω



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Ozone Depleting Substances Policy Statement

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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