Vishay Siliconix

RoHS

COMPLIANT HALOGEN

FREE



G D S

Top View

Automotive N-Channel 100 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	100			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.025			
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.029			
I _D (A)	40			
Configuration	Single			
TO-252 Reverse Lead DPAK	D P			
Drain Connected to				

FEATURES

- · Reverse Lead DPAK for Top Side Cooling
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- · Package with Low Thermal Resistance
- AEC-Q101 Qualified^d
- Compliant to RoHS Directive 2002/95/EC
- · Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications

ORDERING INFORMATION	
Package	TO-252 Reverse Lead DPAK
Lead (Pb)-free and Halogen-free	SQR40N10-25-GE3

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	$T_{C} = 25 \ ^{\circ}C$, unle	ess otherwise note	ed		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V _{DS}	100	N/	
Gate-Source Voltage		V _{GS} ± 20		- V	
Continuous Drain Current ^a	T _C = 25 °C	1	40		
	T _C = 125 °C	I _D	23		
Continuous Source Current (Diode Conduction) ^a		I _S	40	A	
Pulsed Drain Current ^b		I _{DM}	70		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	80	mJ	
Single Pulse Avalanche Current		I _{AS}	40	А	
Maximum Power Dissipation ^b	T _C = 25 °C	P	136	W	
	T _A = 25 °C	P _D	3		
Operating Junction and Storage Temperature Rar	nge	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	50	°C/W	
Junction-to-Case (Drain)		R _{thJC}	1.1	C/ W	

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. When mounted on 1" square PCB (FR-4 material).

d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TES	vise noted TEST CONDITIONS		TYP.	MAX.	UNIT	
Static	•						•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		100	-	-	v	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		-	2.5		
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		$V_{GS} = 0 V$	V _{DS} = 100 V	-	-	1.0		
	I _{DSS}	$V_{GS} = 0 V$	$V_{DS} = 100 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	-	50	μΑ	
		$V_{GS} = 0 V$	$V_{DS} = 100 \text{ V}, \text{ T}_{J} = 175 ^{\circ}\text{C}$	-	-	250		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	70	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 40 A	-	0.022	0.025	Ω	
		V _{GS} = 10 V	I _D = 40 A, T _J = 125 °C	-	-	0.050		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 40 A, T _J = 175 °C	-	-	0.063		
		$V_{GS} = 4.5 V$	I _D = 20 A	-	0.024	0.029		
Forward Transconductance ^a	9 _{fs}	V _{DS}	= 15 V, I _D = 40 A	70	-	-	S	
Dynamic ^b	•					•	•	
Input Capacitance	C _{iss}		V V _{DS} = 25 V, f = 1 MHz	-	2400	-	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	290	-		
Reverse Transfer Capacitance	C _{rss}			-	120	-		
Total Gate Charge ^c	Qg		V _{DS} = 50 V, I _D = 40 A	-	40	60	nC	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	11	-		
Gate-Drain Charge ^c	Q _{gd}			-	9	-		
Turn-On Delay Time ^c	t _{d(on)}	$\label{eq:VDD} \begin{array}{l} V_{DD}=50~V,~R_L=1.25~\Omega\\ I_D\cong40~A,~V_{GEN}=10~V,~R_g=2.5~\Omega \end{array}$		-	8	13		
Rise Time ^c	tr			-	40	60	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	15	25		
Fall Time ^c	t _f			-	80	120		
Source-Drain Diode Ratings and Chara	acteristics $T_{C} = 2$	25 °C ^b			·			
Pulsed Current ^a	I _{SM}			-	-	70	Α	
Forward Voltage	V _{SD}	I _F = 40 A, V _{GS} = 0 V		-	1.0	1.5	V	
	•					•		

Notes

a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle ≤ 2 %.

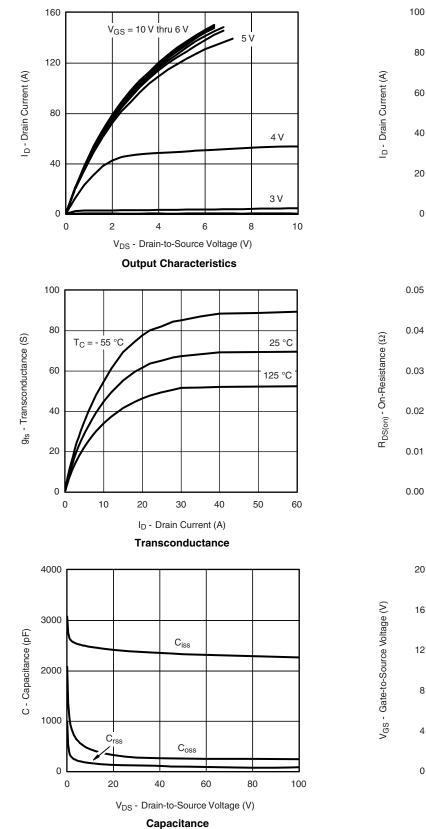
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

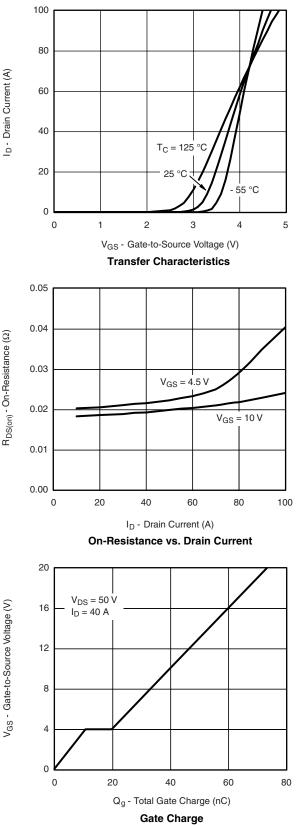


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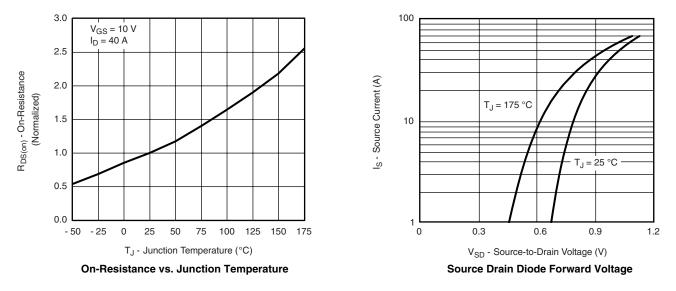
TYPICAL CHARACTERISTICS $T_A = 25$ °C, unless otherwise noted

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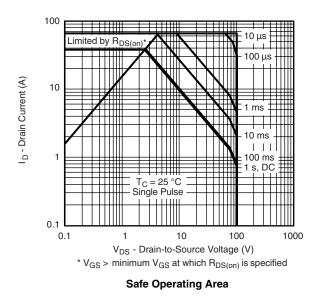
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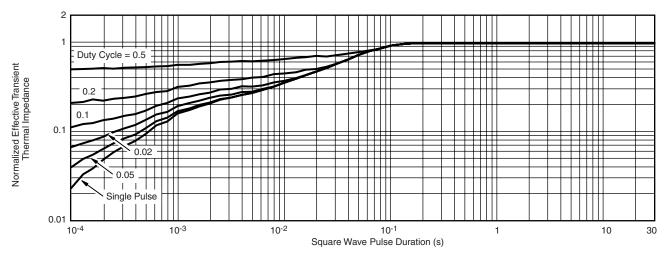
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Normalized Thermal Transient Impedance, Junction-to-Case

Note

The characteristics shown in the graph. Normalized Transient Thermal Impedance Junction to Case ($25 \,^{\circ}$ C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?69060.



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