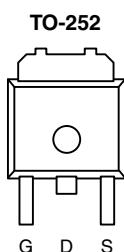
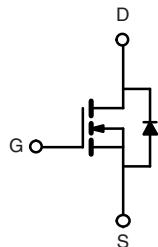


Automotive N-Channel 30 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	30
$R_{DS(on)}$ (Ω) at $V_{GS} = 10$ V	0.010
I_D (A)	62
Configuration	Single



Top View



N-Channel MOSFET

FEATURES

- Halogen-free
- TrenchFET® Power MOSFET
- Package with Low Thermal Resistance


RoHS
COMPLIANT

AEC-Q101 RELIABILITY

- Passed all AEC-Q101 Reliability Testing
- Characterization Ongoing

ORDERING INFORMATION

Package	TO-252
Lead (Pb)-free and Halogen-free	SQD50N03-09-GE3

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage	$T_C = 25$ °C	V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^a	$T_C = 100$ °C	I_D	62	A
			44	
Continuous Source Current (Diode Conduction) ^a		I_S	20	
Pulsed Drain Current ^b			I_{DM}	
Single Pulse Avalanche Energy	$L = 0.1$ mH	E_{AS}	-	mJ
Single Pulse Avalanche Current		I_{AS}	-	
Maximum Power Dissipation ^b	$T_C = 25$ °C	P_D	7.1	W
			8.3	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	50	°C/W
Junction-to-Case (Drain)		R_{thJC}	2.1	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR-4 material).

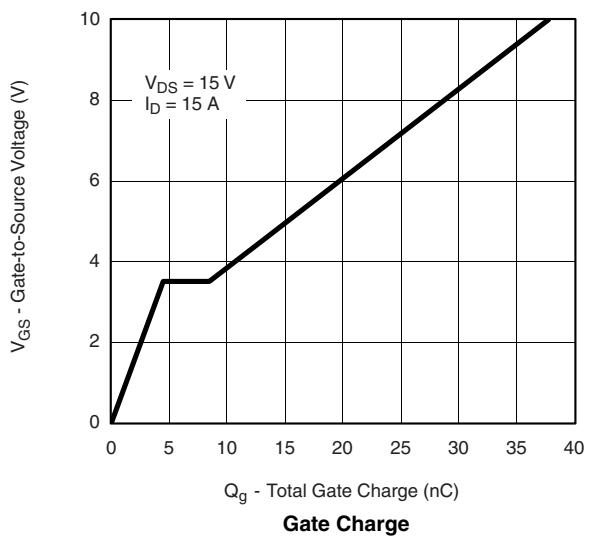
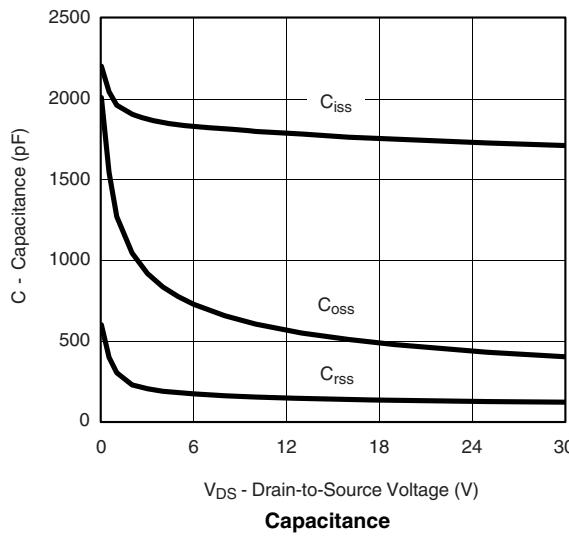
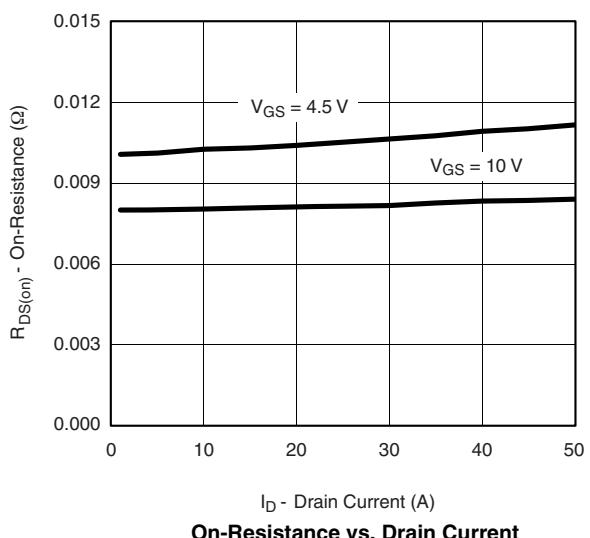
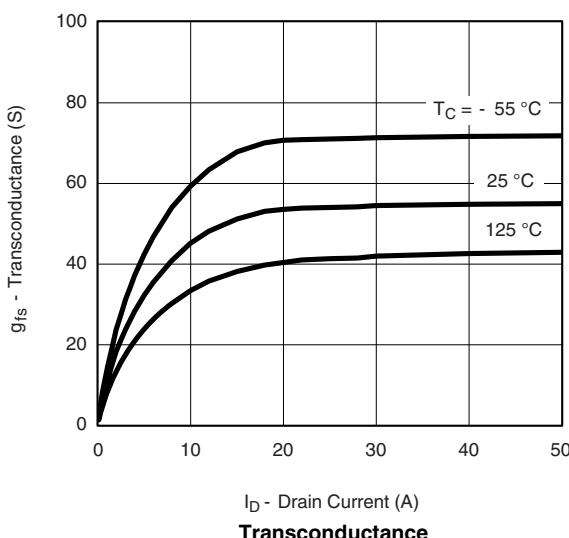
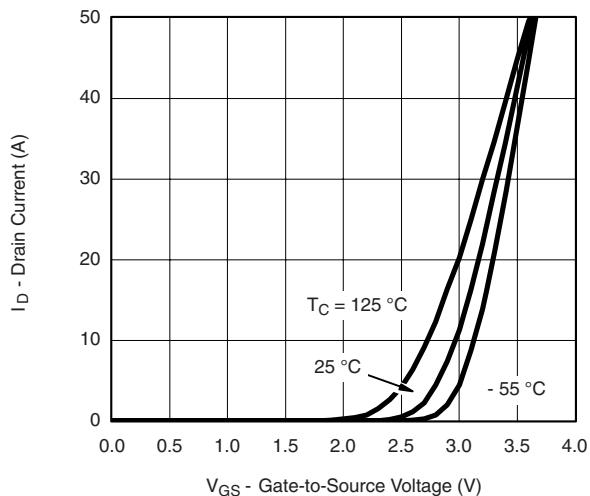
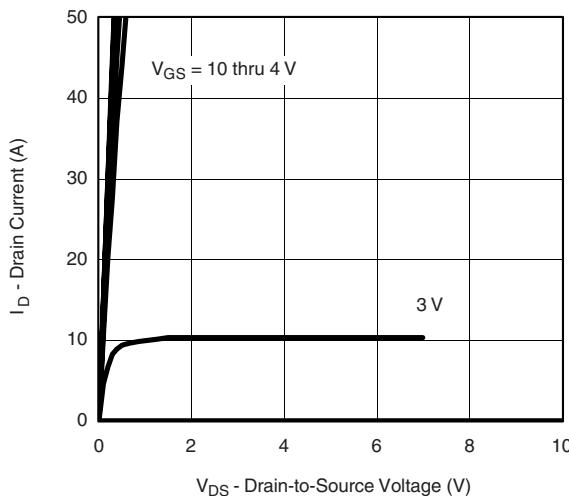
SPECIFICATIONS $T_C = 25^\circ\text{C}$, unless otherwise noted

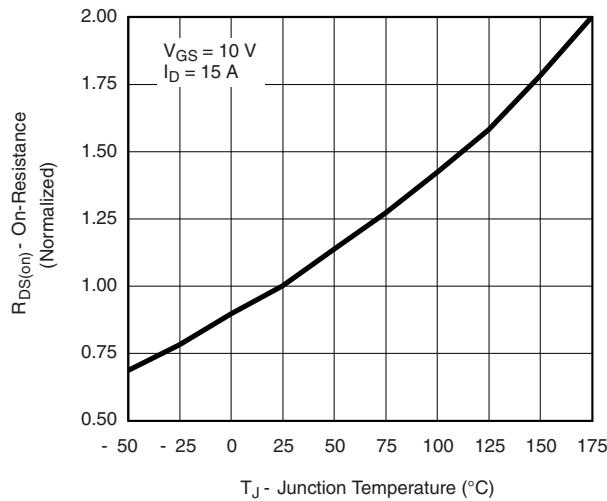
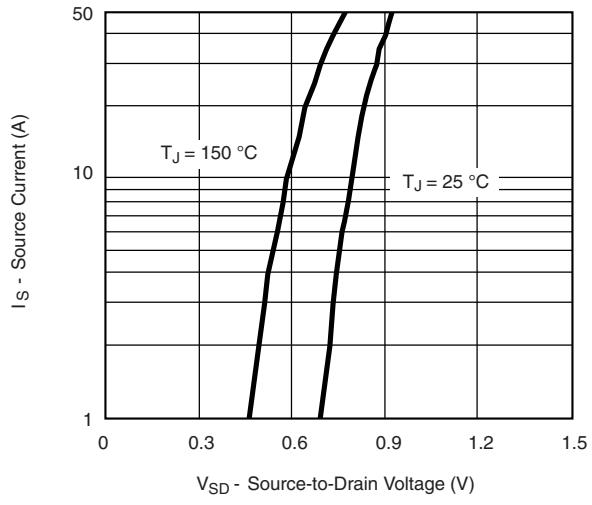
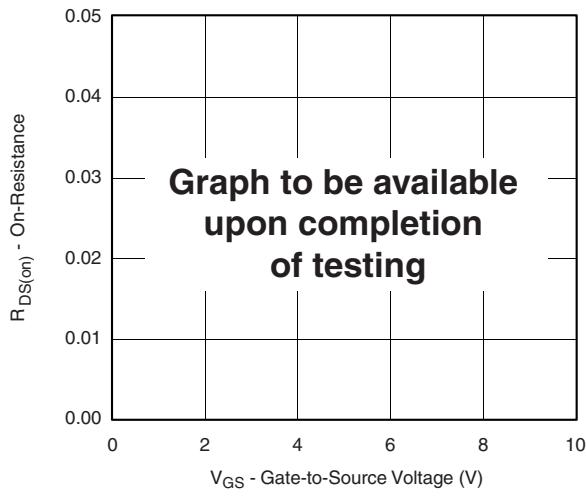
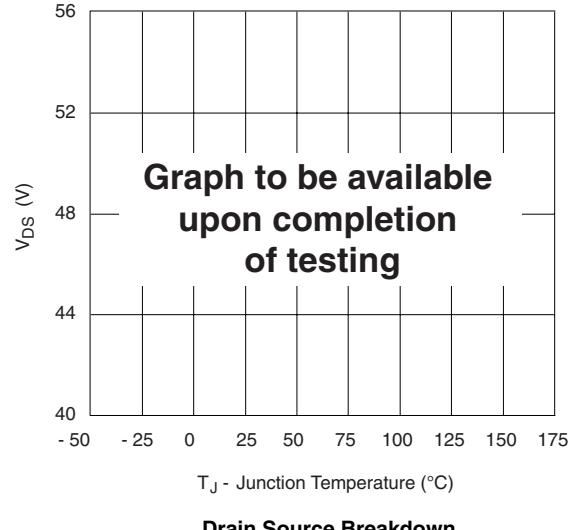
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static								
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0 \text{ V}$	$I_D = 250 \mu\text{A}$	30	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}$	$I_D = 250 \mu\text{A}$	1.0	-	-		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}$	$V_{GS} = \pm 20 \text{ V}$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 24 \text{ V}$	-	-	1.0	μA	
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 24 \text{ V}, T_J = 125^\circ\text{C}$	-	-	50		
		$V_{GS} = 0 \text{ V}$	$V_{DS} = 24 \text{ V}, T_J = 175^\circ\text{C}$	-	-	150		
On-State Drain Current ^a	$I_{D(\text{on})}$	$V_{GS} = 10 \text{ V}$	$V_{DS} \geq 5 \text{ V}$	50	-	-	A	
Drain-Source On-State Resistance ^a	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}$	$I_D = 15 \text{ A}$	-	0.008	0.010	Ω	
		$V_{GS} = 10 \text{ V}$	$I_D = 15 \text{ A}, T_J = 125^\circ\text{C}$	-	-	0.016		
		$V_{GS} = 10 \text{ V}$	$I_D = 15 \text{ A}, T_J = 175^\circ\text{C}$	-	-	0.020		
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15 \text{ V}, I_D = 15 \text{ A}$		20	60	-	S	
Dynamic^b								
Input Capacitance	C_{iss}	$V_{GS} = 0 \text{ V}$	$V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	-	1725	-	pF	
Output Capacitance	C_{oss}			-	425	-		
Reverse Transfer Capacitance	C_{rss}			-	120	-		
Total Gate Charge ^c	Q_g	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 15 \text{ V}, I_D = 62 \text{ A}$	-	13	-	nC	
Gate-Source Charge ^c	Q_{gs}			-	4.5	-		
Gate-Drain Charge ^c	Q_{gd}			-	4.0	-		
Turn-On Delay Time ^c	$t_{d(\text{on})}$	$V_{DD} = 15 \text{ V}, R_L = 1 \Omega$ $I_D \approx 62 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$	$V_{DS} = 15 \text{ V}, I_D = 62 \text{ A}$	-	10	-	ns	
Rise Time ^c	t_r			-	160	-		
Turn-Off Delay Time ^c	$t_{d(\text{off})}$			-	30	-		
Fall Time ^c	t_f			-	55	-		
Source-Drain Diode Ratings and Characteristics $T_C = 25^\circ\text{C}^b$								
Pulsed Current ^a	I_{SM}			-	-	100	A	
Forward Voltage	V_{SD}	$I_F = 15 \text{ A}, V_{GS} = 0 \text{ V}$		-	0.85	12	V	
Reverse Recovery Time	t_{rr}	$I_F = 62 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		-	80	110	ns	
Peak Reverse Recovery Current	$I_{RM(\text{REC})}$			-	-	-	A	
Reverse Recovery Charge	Q_{rr}			-	-	-	μC	

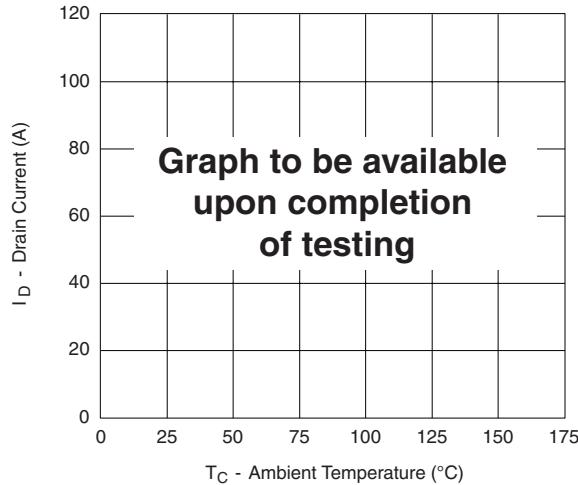
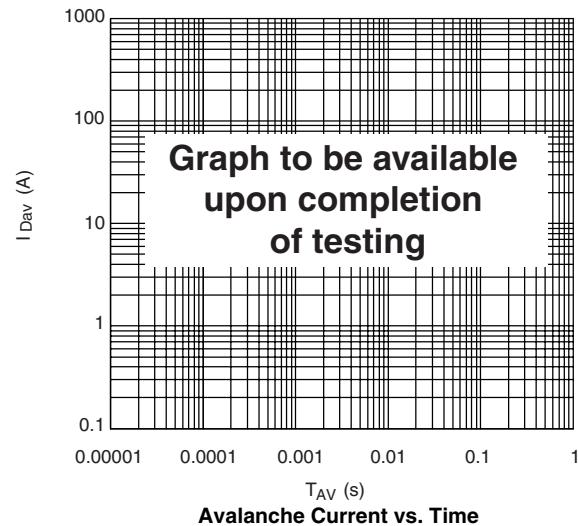
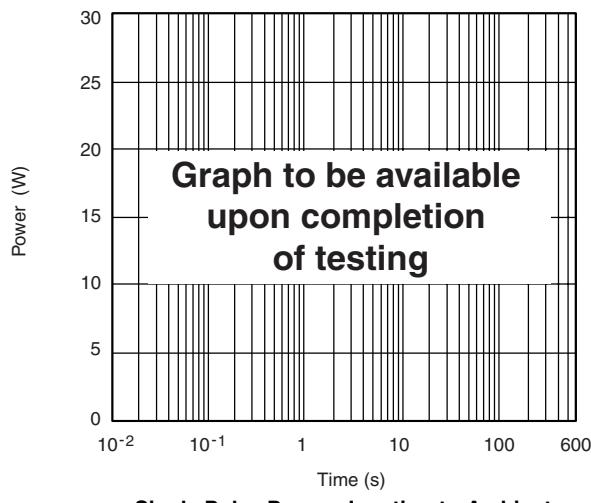
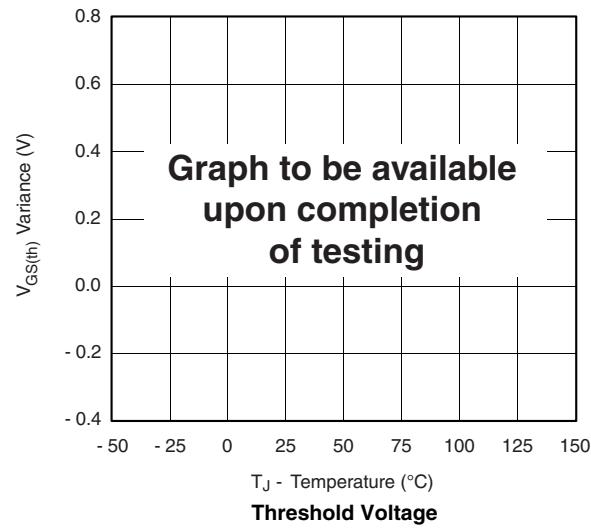
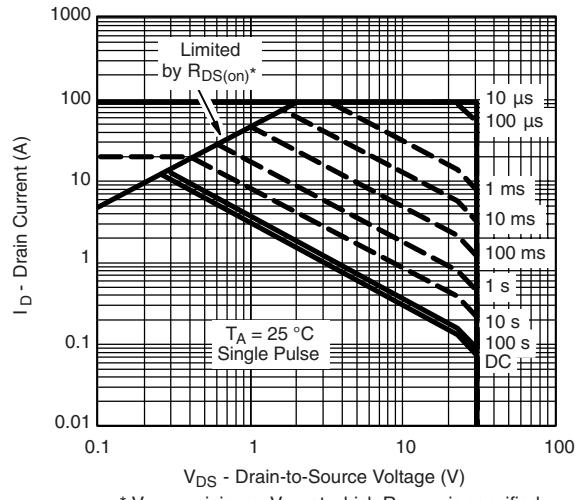
Notes

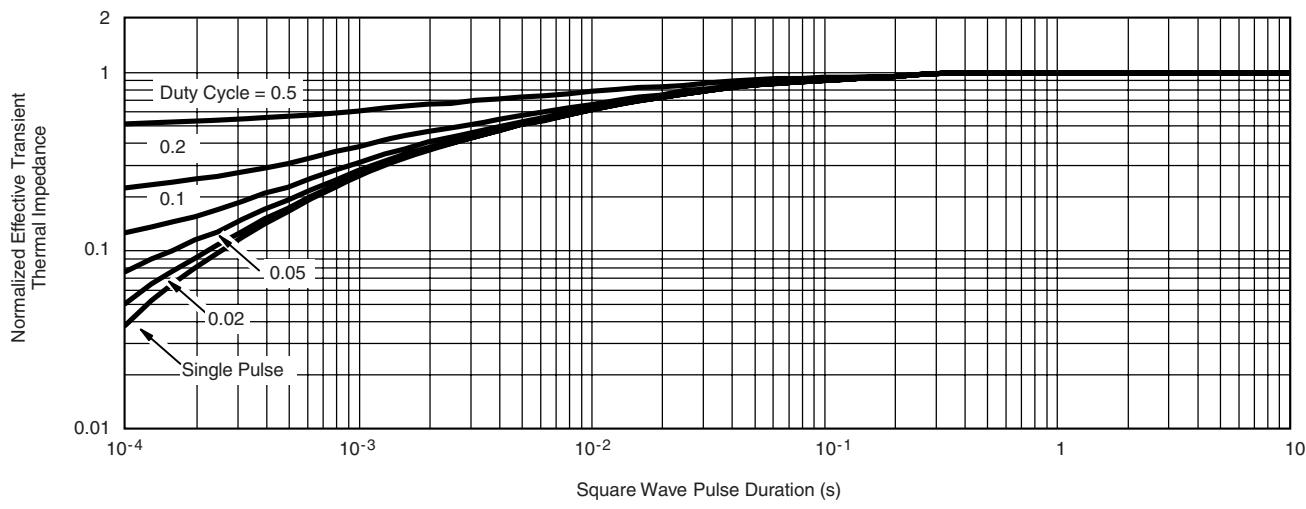
- a. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

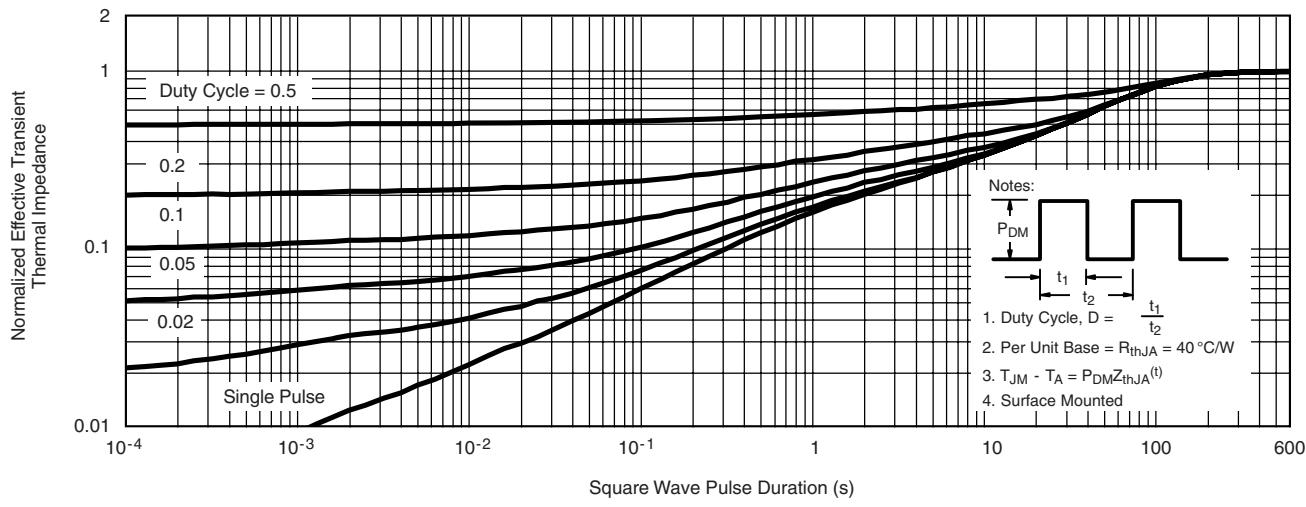
TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, unless otherwise noted


TYPICAL CHARACTERISTICS $T_A = 25^\circ\text{C}$, unless otherwise noted**On-Resistance vs. Junction Temperature****Source Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Drain Source Breakdown vs. Junction Temperature**

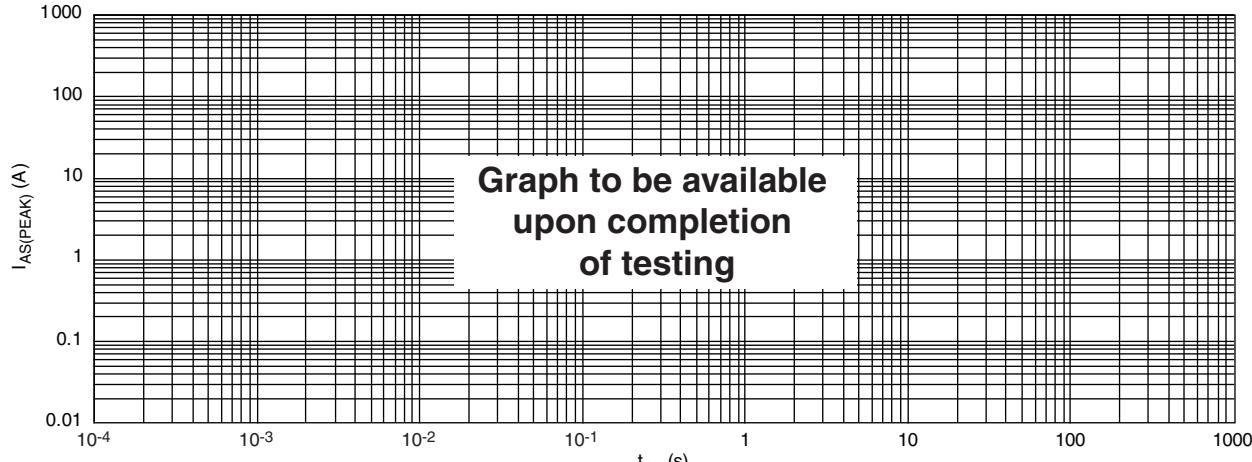
THERMAL RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Maximum Drain Current vs. Ambient Temperature

Avalanche Current vs. Time

Single Pulse Power, Junction-to-Ambient

Threshold Voltage

Safe Operating Area

THERMAL RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted

Normalized Thermal Transient Impedance, Junction-to-Case

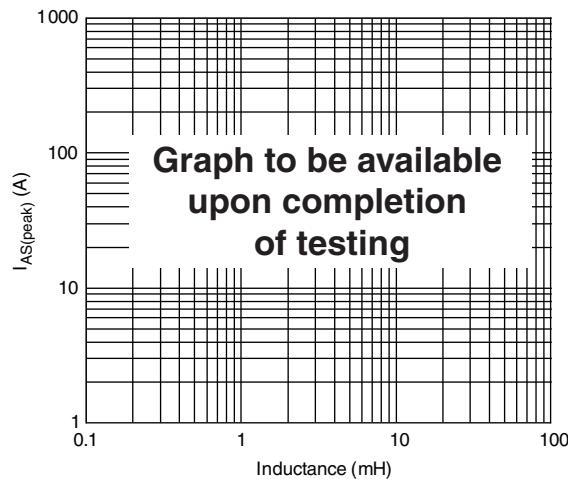


Normalized Thermal Transient Impedance, Junction-to-Ambient

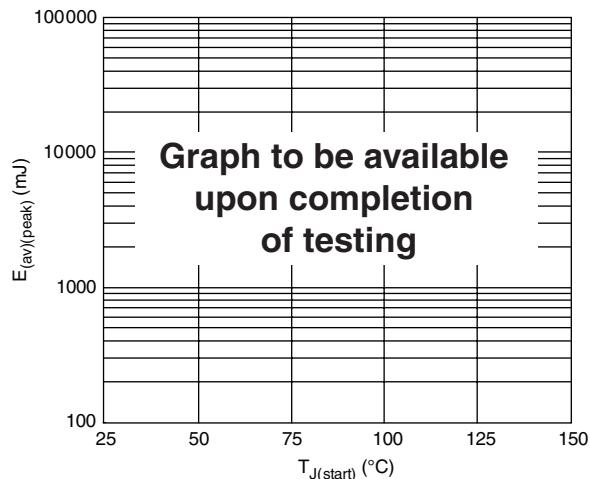


Single Pulse Avalanche Current (Peak) vs. Time in Avalanche

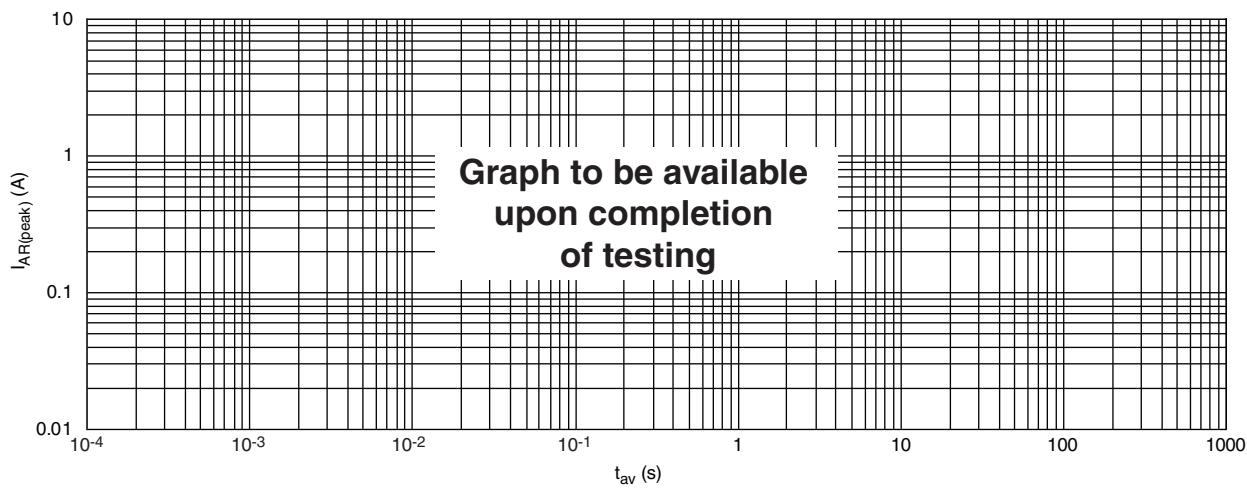
THERMAL RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted



Single Pulse Avalanche Current (Peak) vs. Inductance

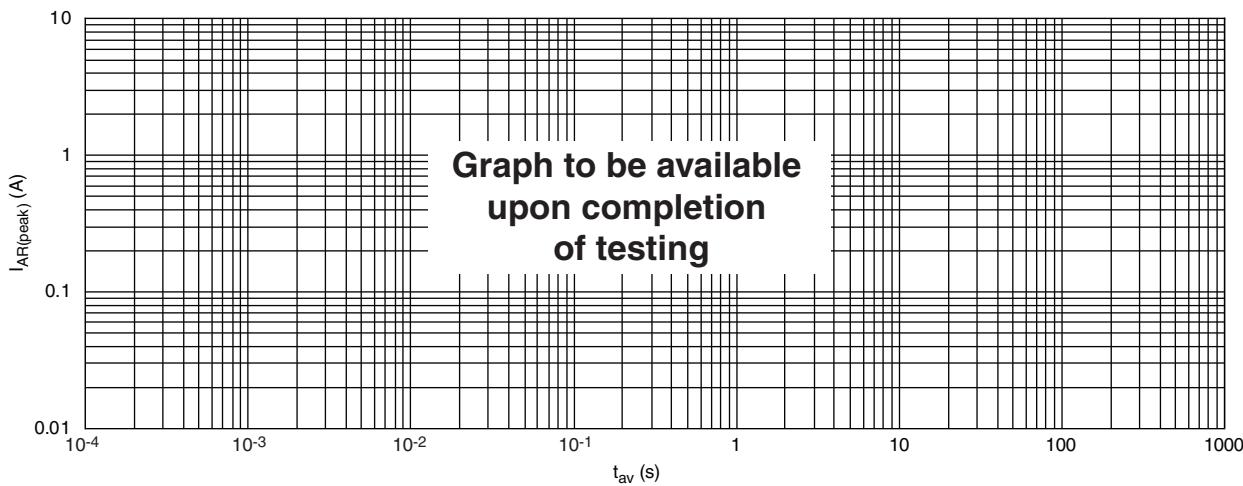


Single Pulse Avalanche Energy (Peak) vs. $T_{J(start)}$



Repetitive Avalanche Current (Peak) vs. Time in Avalanche at $T_A = 25^\circ\text{C}$

THERMAL RATINGS $T_A = 25^\circ\text{C}$, unless otherwise noted



Repetitive Avalanche Current (Peak) vs. Time in Avalanche at $T_A = 150^\circ\text{C}$

Note

The characteristics shown in the six graphs

- Normalized Transient Thermal Impedance Junction to Ambient (25°C)
- Single Pulse Avalanche Current (Peak) vs. Time in Avalanche
- Single Pulse Avalanche Current (Peak) vs. Inductance
- Single Pulse Avalanche Energy (Peak) vs. T_J (start)
- Repetitive Avalanche Current (Peak) vs. Time in Avalanche at $T_A = 25^\circ\text{C}$
- Repetitive Avalanche Current (Peak) vs. Time in Avalanche at $T_A = 150^\circ\text{C}$

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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