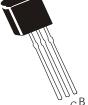




An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

PNP SILICON PLANAR EPITAXIAL TRANSISTORS

CSB621, CSB621A



TO-92 Plastic Package

AF Output Amplifier

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

DESCRIPTION	SYMBOL	CSB621	CSB621A	UNITS		
Collector Emitter Voltage	V_{CEO}	25	50	V		
Collector Base Voltage	V_{CBO}	30	60	V		
Emitter Base Voltage	V_{EBO}		V			
Collector Current Peak	I _{CP}		Α			
Collector Current	I _C		Α			
Power Dissipation @ T _a =25°C	*P _C		mW			
Junction Temperature	Tj	150				
Storage Temperature Range	T _{stg}	- 55 to +150				

^{*}P_C=600mW/Potting type: P_C=600mW

ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

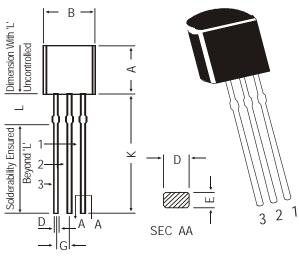
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Cut off Current	I _{CBO}	$V_{CB} = 20V, I_{E} = 0$			0.1	μΑ
Collector Base Voltage	V_{CBO}	$I_{C}=10\mu A, I_{E}=0$				
		CSB621	30			V
		CSB621A	60			V
Collector Emitter Voltage	V_{CEO}	$I_C=2mA$, $I_B=0$				
		CSB621	25			V
		CSB621A	50			V
Emitter Base Voltage	V_{EBO}	$I_{E}=10\mu A, I_{C}=0$	5			V
DC Current Gain	*h _{FE}	V_{CE} =10V, I_{C} =500mA	85		340	
	h _{FE}	V_{CE} =5V, I_{C} =1A	50			
Collector Emitter Saturation Voltage	$V_{CE(sat)}$	I_C =500mA, I_B =50mA			0.4	V
Base Emitter Saturation Voltage	$V_{BE(sat)}$	I _C =500mA, I _B =50mA			1.2	V
Transition Frequency	f _T	$I_C=50$ mA, $V_{CE}=10$ V		200		MHz
Output Capacitance	C _{ob}	I _E =0, V _{CB} =10V,f=1MHz			30	pF

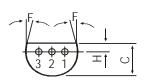
*h_{FE} Classifications Q: 85 - 170 R: 120 - 240 S: 170 - 340

TO-92 Plastic Package

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TO-92 Transistors on Tape and Ammo Pack



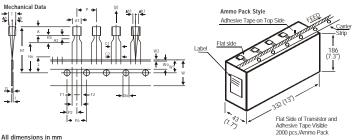


PIN CONFIGURATION

- 1. BASE
- 2. COLLECTOR
- 3. EMITTER

DIM	MIN.	MAX.					
Α	4.32	5.33					
В	4.45	5.20					
С	3.18	4.19					
D	0.41	0.55					
Е	0.35	0.50					
F	5 DEG 1.14 1.40						
G							
Н	1.14	1.53					
K	12.70	_					
L	1.982	2.082					
All diminsions in mm							

All diminsions in mm.



All	dimen	sions	in	mı

			SPECIFICATION		ON		
	ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS
	BODY WIDTH	A1	4.0		4.8		
	BODY HEIGHT	A	4.8		5.2		
	BODY THICKNESS	T	3.9		4.2		
	PITCH OF COMPONENT	P		12.7		± 1.0	
	FEED HOLE PITCH	Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
	FEED HOLE CENTRE TO						
	COMPONENT CENTRE	P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH
	DISTANCE BETWEEN OUTER					+ 0.6	
	LEADS	F		5.08		- 0.0	
	COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		AT TOP OF BODY
	COMPONENT ALIGNMENT FRONT VIEW	Δh1		0	1.3		AT TOP OF BODY
	TAPE WIDTH	W		18		± 0.5	
	HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	
	HOLE POSITION	W1		9		+ 0.7	
						- 0.5	
	HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	
	LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5	
	COMPONENT HEIGHT	H1			23.25		
	LENGTH OF SNIPPED LEADS	L			11.0		
	FEED HOLE DIAMETER	Do		4		± 0.2	
	TOTAL TAPE THICKNESS	t			1.2		t1 0.3-0.6
	LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4	
	STAND OFF	H2	0.45		1.45	- 0.1	
	CLINCH HEIGHT	H3	0.45		3.0		
	LEAD PARALLELISM				0.22		
		C1 - C2	6N		0.22		
Į	PULL - OUT FORCE	(P)	OIA				

- NOTES

 1. Maximum alignment deviation between leads will not to be greater than 0.2mm.

 2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.

 3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.

 4. There will be no more than three (3) consecutive missing components in a tape.

 5. A tape trailer, having at least three feed holes are provided after the last component in a tape.

 6. Spilces should not interfere with the sprocket feed holes.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details Net Weight / Oty		Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes CSB621, CSB621A

TO-92 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



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Continental Device India Limited Data Sheet Page 3 of 3