HVC417C

Variable Capacitance Diode for tuner

REJ03G0518-0100
(Previous: ADE-208-1433)
Rev.1.00
Feb 17, 2005

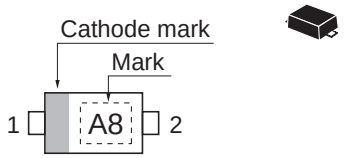
Features

- High capacitance ratio. (n = 13.00 min)
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Renesas Code	Previous Code
HVC417C	A8	PWSF0002ZA-A	UFP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V_R	30	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	V_R = 25 V
	I_{R2}	—	—	100		V_R = 25 V, Ta = 60°C
Capacitance	C_1	7.80	—	9.40	pF	V_R = 1 V, f = 1 MHz
	C_{25}	0.50	—	0.60		V_R = 25V, f = 1 MHz
Capacitance ratio	n	13.00	—	—	—	C_1 / C_{25}
Series resistance	r_S	—	—	1.50	Ω	V_R = 5 V, f = 470 MHz
Matching error	ΔC/C *[1]	—	—	6.00	%	V_R = 1 to 25 V, f = 1 MHz

Note: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ΔC/C continuous in a reel, expect extention to another group.
Calculate Matching Error,

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100\ (\%)$$

Main Characteristic

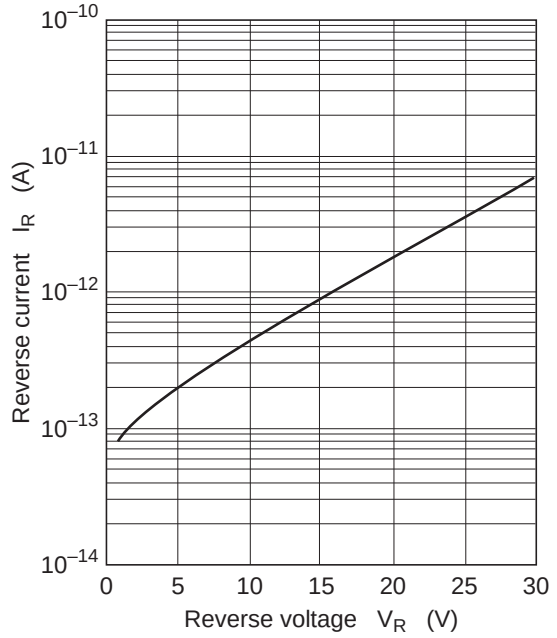


Fig.1 Reverse current vs. Reverse voltage

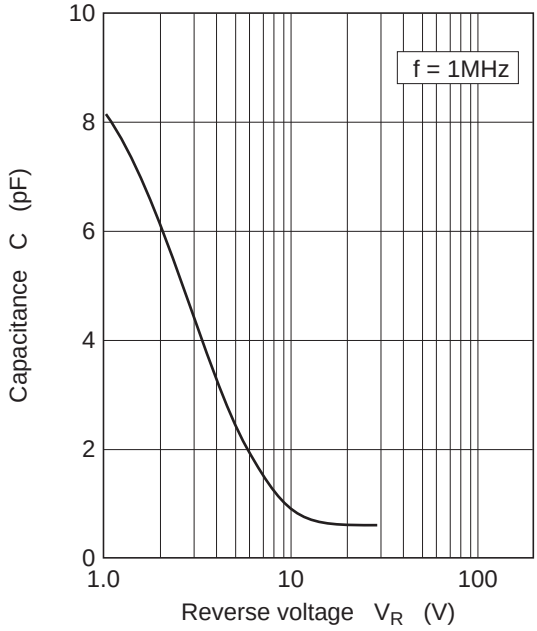


Fig.2 Capacitance vs. Reverse voltage

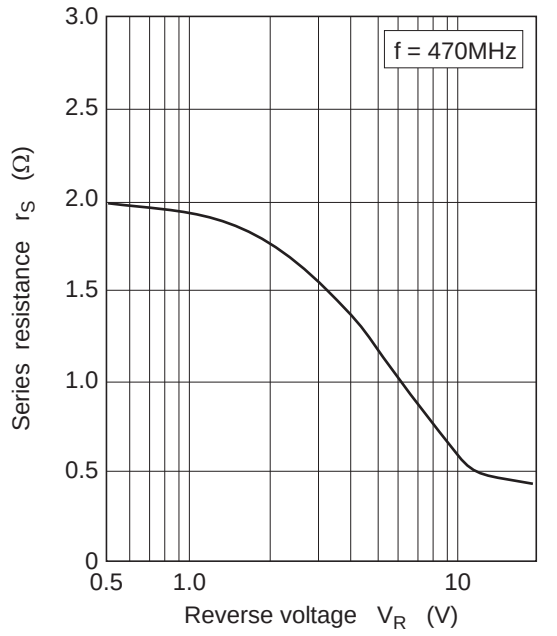


Fig.3 Series resistance vs. Reverse voltage

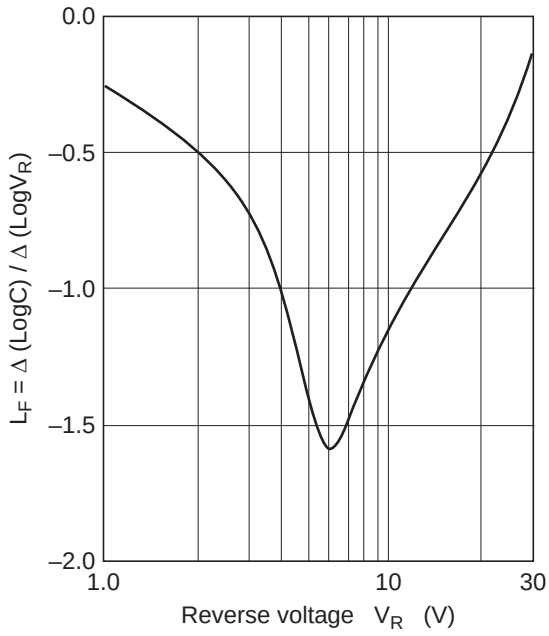
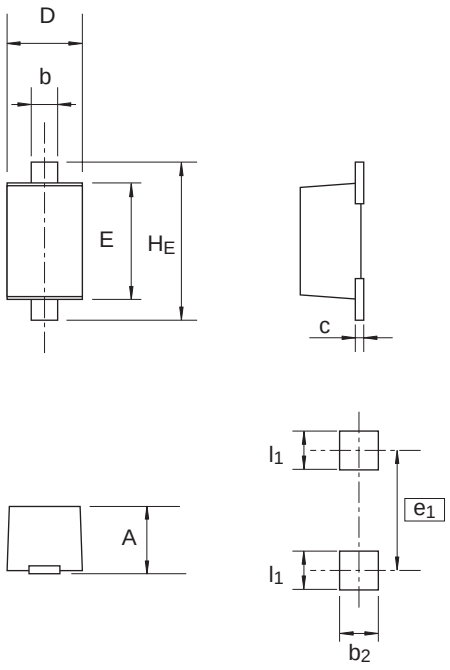


Fig.4 Linearity factor vs. Reverse voltage

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
SC-79	PWSF0002ZA-A	UFP / UFPV	0.0016g



Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	0.60	0.70
b	0.25	0.30	0.35
c	0.08	—	0.18
D	0.70	0.80	0.90
E	1.10	1.20	1.30
H_E	1.50	1.60	1.70
b_2	—	0.80	—
e_1	—	1.70	—
l_1	—	0.60	—

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http://www.renesas.com

Refer to "**http://www.renesas.com/en/network**" for the latest and detailed information.

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Colophon 2.0