To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HVC300C

Variable Capacitance Diode for VHF tuner

REJ03G0513-0100
(Previous: ADE-208-1611)
Rev.1.00
Feb 04, 2005

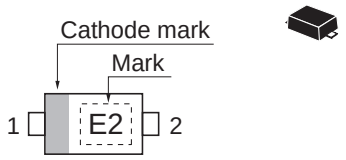
Features

- High capacitance ratio (n = 14.5 min) and suitable for wide band tuner.
- Low series resistance and good C-V linearity.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Renesas Code	Previous Code
HVC300C	E2	PWSF0002ZA-A	UFP

Pin Arrangement



1. Cathode
2. Anode

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM} *1	35	V
Reverse voltage	V_R	34	V
Junction temperature	Tj	150	°C
Storage temperature	Tstg	–55 to +150	°C

Note: 1. R_L = 10 kΩ

Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I_{R1}	—	—	10	nA	V_R = 32 V
	I_{R2}	—	—	100		V_R = 32 V, Ta = 60°C
Capacitance	C_2	39.5	—	47.0	pF	V_R = 2 V, f = 1 MHz
	C_{25}	2.6	—	3.0		V_R = 25V, f = 1 MHz
Capacitance ratio	n	14.5	—	—	—	C_2 / C_{25}
Series resistance	r_S	—	—	1.1	Ω	V_R = 5 V, f = 470 MHz
Matching error	ΔC/C *1	—	—	2.0	%	V_R = 2 to 25 V, f = 1 MHz

Note: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ΔC/C continuous in a reel , expect extention to another group.
Calculate Matching Error,

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100\ (\%)$$

Main Characteristic

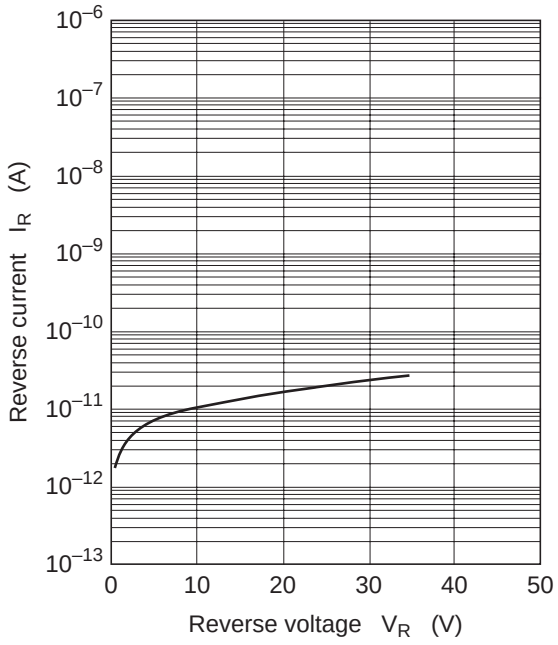


Fig.1 Reverse current vs. Reverse voltage

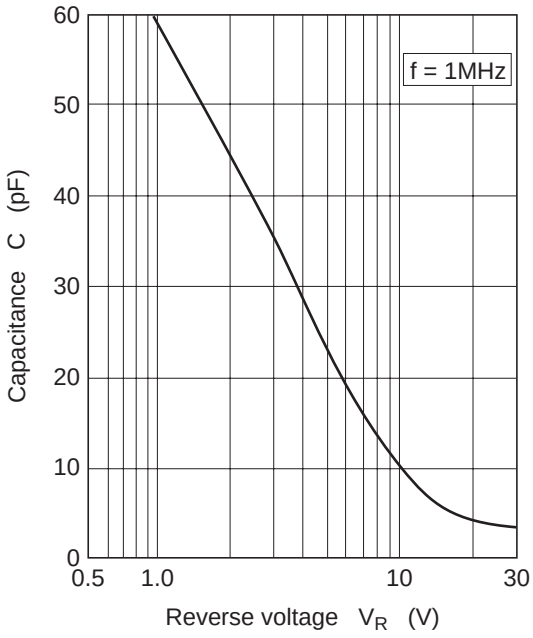


Fig.2 Capacitance vs. Reverse voltage

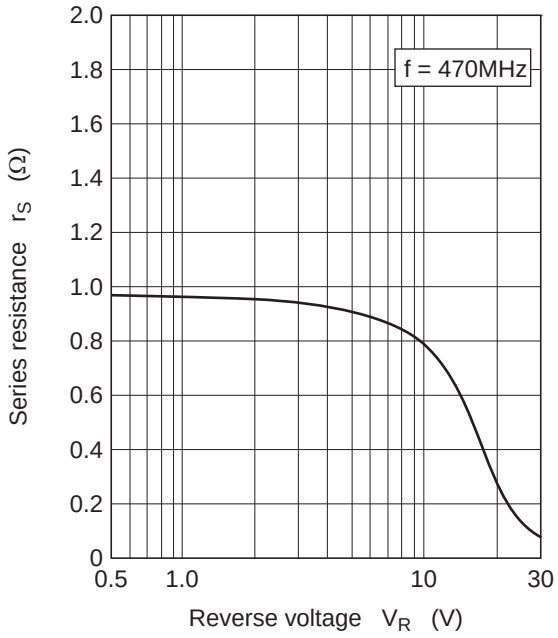


Fig.3 Series resistance vs. Reverse voltage

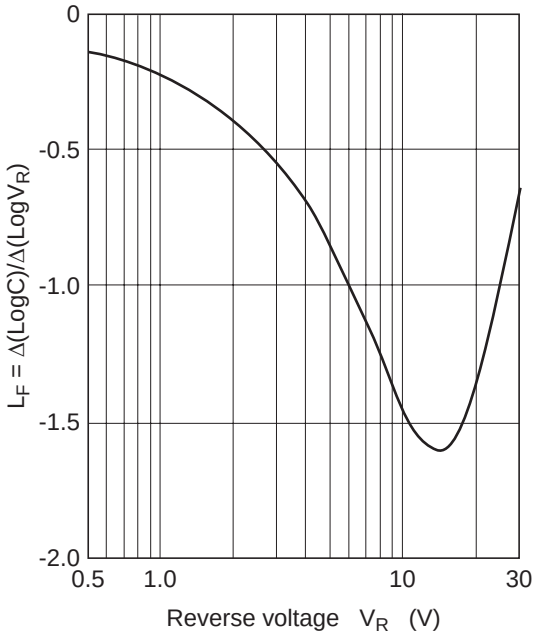
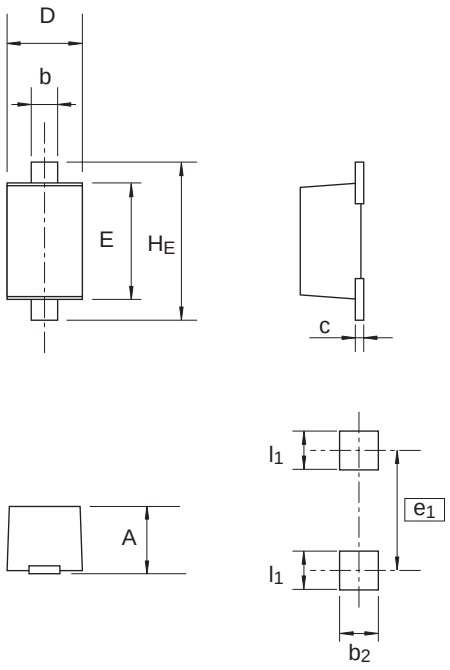


Fig.4 Linearity factor vs. Reverse voltage

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
SC-79	PWSF0002ZA-A	UFP / UFPV	0.0016g



Pattern of terminal position areas

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	0.50	0.60	0.70
b	0.25	0.30	0.35
c	0.08	—	0.18
D	0.70	0.80	0.90
E	1.10	1.20	1.30
H_E	1.50	1.60	1.70
b_2	—	0.80	—
e_1	—	1.70	—
l_1	—	0.60	—

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

RENESAS SALES OFFICES

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd.
Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China
Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

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