TOSHIBA Intelligent Power Device Silicon Monolithic Power MOS Integrated Circuit

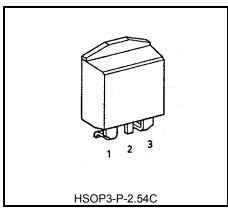
TPD1031AF

Low-Side Power Switch for Motor, Solenoid and Lamp Drivers

The TPD1031AF is a monolithic power IC intended for low-side load switching applications. The output has a vertical MOSFET, and the input can be directly driven from CMOS or TTL logic (e.g., an MPU). The TPD1031AF provides intelligent protection functions.

Features

- A structure that incorporates control circuitry and a vertical power MOSFET on a single chip.
- Can be directly driven from a microprocessor, a CMOS logic IC, etc.
- Overvoltage, overtemperature and overcurrent protections
- Low ON-resistance: R_{DS} (ON) = 65m Ω (max) (@V_{IN} = 5 V, I_D = 4 A, T_{ch} = 25°C)
- Low leakage current: IDSS = 100 μA (max) (@VIN = 0 V, VDS = 40 V, Tch = 25°C)
- Housed in "TO-220SM" package and supplied in embossed carrier tape.

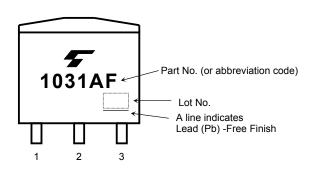


Weight: 1.5 g (typ.)

Pin Assignment

marking 1 2 3 IN DRAIN SOURCE (heat sink)

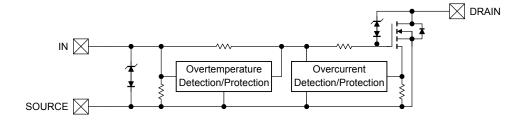
Marking



Note: This product has a MOS structure and is sensitive to electrostatic discharge.



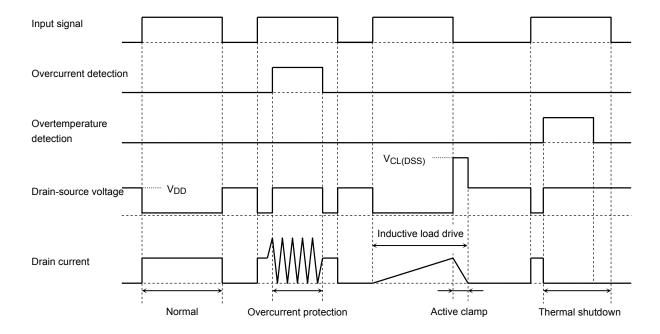
Block Diagram



Pin Description

Pin No.	Symbol	Pin Description
1	IN	Input pin. This pin is connected to a pull-down resistor internally, so that even if the input is open-circuited, the output never turns on inadvertently.
2	DRAIN	Drain pin. When a load short-circuit causes an overcurrent (8 A min) to flow into a device, the output automatically turns on and off repeatedly.
3	SOURCE	Source (ground) pin.

Timing Chart



Truth Table

V _{IN}	V _{DS}	Output State	Operating State		
L	Н	Off	Normal		
Н	L	On	Noma		
L	Н	Off			
Н	Н	Current limiting (switching)	Load short-circuited		
L	Н	Off	Overtemperature		
Н	Н	Off	Overlemperature		

Absolute Maximum Ratings (Ta = 25°C)

Characterist	ics	Symbol	Rating	Unit	
Drain-source voltage		V_{DS}	50	V	
Drain current		I _D	Internally limited	Α	
Input voltage		V _{IN}	−0.3 to 7	V	
B " : "	Tc = 25°C	P _{D(1)}	50	W	
Power dissipation	Ta = 25°C	P _{D(2)}	1.5	VV	
Single pulse active clam (Note 1)	p capability	EAS	1110	mJ	
Active clamp current		I _{AR}	8	Α	
Repetitive active clamp (Note 2)	capability	E _{AR}	5	mJ	
Operating temperature		T _{opr}	-40 to 110	°C	
Channel temperature		T _{ch}	150	°C	
Storage temperature		T _{stg}	−55 to 150	°C	

- Note 1: Active clamp capability (single pulse) test condition $V_{DD}=25~V,~T_{Ch}=25^{\circ}C(initial),~L=20~mH,~I_{AR}=8~A,~R_{G}=25~\Omega$
- Note 2: Repetitive rating: Pulse width limited by maximum channel temperature
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	max	Unit
Thermal resistance, channel to case	R _{th(ch-c)}	2.5	°C/W
Thermal resistance, channel to ambient	R _{th(ch-a)}	83.3	°C/W



Electrical Characteristics (T_{ch} = 25°C)

Characteristics		Symbol	Test circuit	Test condition	Min	Тур.	Max	Unit
Operating supply voltage range		V _{DD(opr)}	-	-	1	-	18	V
High-level input	current	I _{IH}	-	$V_{IN} = 5 \text{ V}, V_{DS} = 0 \text{ V}$	ı	-	500	μА
Drain cut-off current		I _{DSS}	-	$V_{DS} = 40 \text{ V}, V_{IN} = 0 \text{ V}$	-	-	100	μΑ
Drain-source clamp voltage		V _{(CL)DSS}	-	$I_D=10\ mA,\ V_{IN}=0\ V$	50	-	1	V
Drain-source ON-resistance		R _{DS(ON)}	-	$V_{IN} = 5 \text{ V}, I_D = 4 \text{ A}$	ı	0.045	0.065	Ω
	Rise time	t _r	1	V_{IN} = +5 V/-0 V V_{DD} = 12 V R_L = 10 Ω	ı	70	1	μs
Cusitabina timas	Turn on time	ton			1	100	250	
Switching times	Fall time	t _f			ı	120	1	
	Turn off time	t _{off}			ı	300	450	
Input threshold voltage		V _{th}	-	$V_{DS} = 12 \text{ V}, I_D = 1 \text{ mA}$	2	3	3.5	V
Protective circuit operation input voltage range		V _{IN(P)}	-	-	4	-	-	V
Overcurrent detection		loc	-	V _{IN} = 5 V	8	15	-	Α
Overtemperature detection		T _{OT}	-	$V_{IN} = 5 V$	1	160	-	°C
Drain-source diode forward voltage		V _{DSF}	-	$I_{DR} = 8 A, V_{IN} = 0 V$	-	-	1.8	V

Electrical Characteristics (T_{ch} = -40 to 110°C)

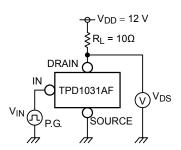
Characteristics		Symbol	Test circuit	Test condition	Min	Тур.	Max	Unit
Operating supply voltage range		V _{DD(opr)}	-	-	-	-	18	V
High-level input current		I _{IH}	-	$V_{IN} = 5 \text{ V}, V_{DS} = 0 \text{ V}$	-	ı	750	μА
Drain cut-off current		I _{DSS}	-	$V_{DS} = 40 \text{ V}, V_{IN} = 0 \text{ V}$	-	ı	100	μА
Drain-source cla	Drain-source clamp voltage		-	$I_D=10~mA,~V_{IN}=0~V$	48	ı	-	V
Drain-source ON-resistance		R _{DS(ON)}	-	$V_{IN} = 5 \text{ V}, I_D = 4 \text{ A}$	-	0.045	0.095	Ω
	Rise time	t _r	1	V_{IN} = +5 V/-0 V V_{DD} = 12 V R_L = 10 Ω	-	70	-	μs
O	Turn on time	t _{on}			-	100	400	
Switching times	Fall time	t _f				120	-	
	Turn off time	t _{off}			-	300	700	
Input threshold voltage		V _{th}	-	$V_{DS} = 12V, I_D = 1 \text{ mA}$	1.5	3	4	V
Protective circuit operation input voltage range		V _{IN(P)}	-	-	4	1	-	V
Overcurrent detection		loc	-	$V_{IN} = 5V$	8	15	-	Α
Overtemperature detection		T _{OT}	-	$V_{IN} = 5V$	-	160	-	°C

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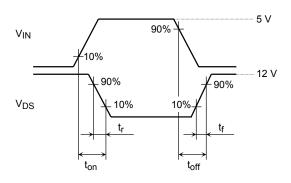
Test Circuit 1

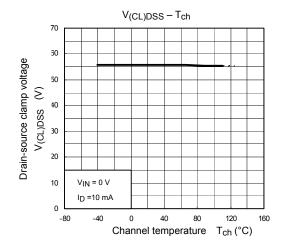
Switching times measuring circuit

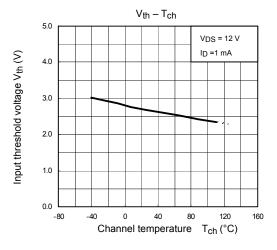
Test circuit

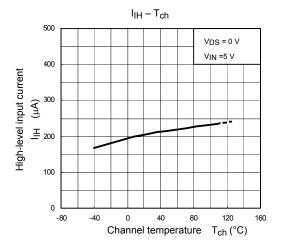


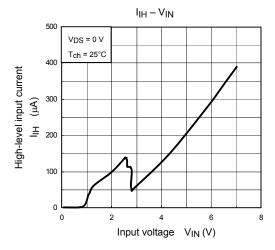
Measured waveforms

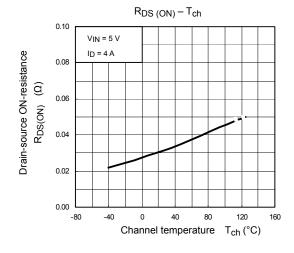


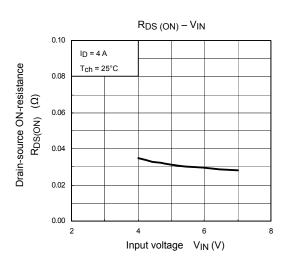


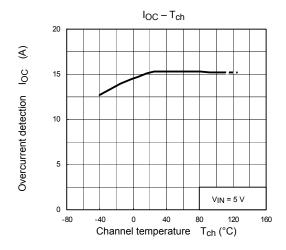


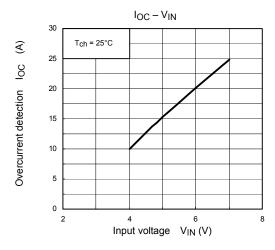


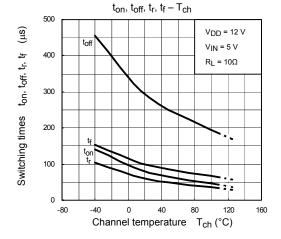


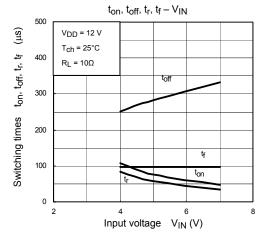


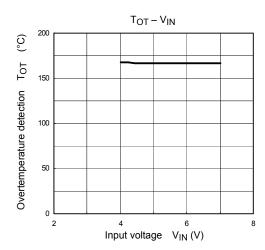


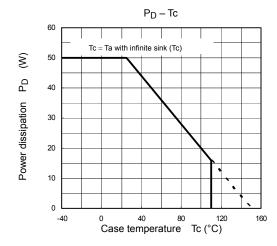


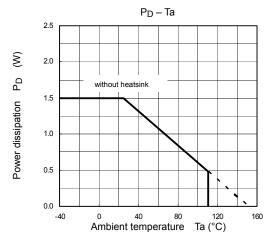


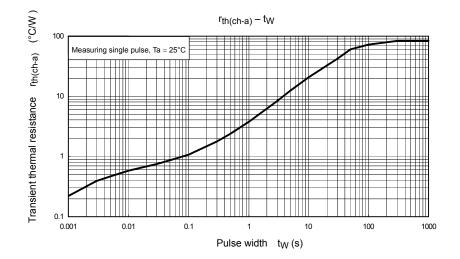






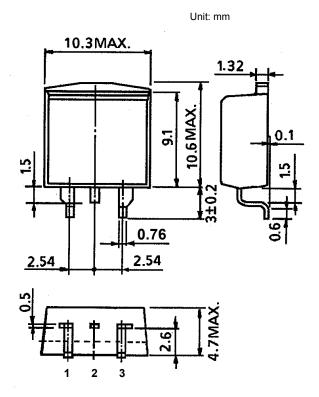






Package Dimensions

HSOP3-P-2.54C



Weight: 1.5 g (typ.)

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20070701-EN GENERAL

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