# FAIRCHILD

SEMICONDUCTOR

# **KSA642**

### Low Frequency Power Amplifier

- Complement to KSD227
- Collector Power Dissipation : P<sub>C</sub> = 400mW
  Suffix "-C" means Center Collector (1. Emitter 2. Collector 3. Base)



## **PNP Epitaxial Silicon Transistor**

### Absolute Maximum Ratings T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
V <sub>CBO</sub>	Collector-Base Voltage	-30	V	
V <sub>CEO</sub>	Collector-Emitter Voltage	-25	V	
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V	
I <sub>C</sub>	Collector Current (DC)	-300	mA	
I <sub>CP</sub>	* Collector Current (Pulse)	-500	mA	
P <sub>C</sub>	Collector Power Dissipation	400	mW	
TJ	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	-55 ~ 150	°C	

\* PW≤10ms, Duty cycle≤50%

### Electrical Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100μA, I <sub>E</sub> =0	-30			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA. I <sub>B</sub> =0	-25			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -10μΑ. I <sub>C</sub> =0	- 5			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> = -25V, I <sub>E</sub> =0			-100	nA
I <sub>EBO</sub>	Emitter Cut-off Current	V <sub>EB</sub> = -3V, I <sub>C</sub> =0			-100	nA
h <sub>FE</sub>	* DC Current Gain	V <sub>CE</sub> = -1V, I <sub>C</sub> = -50mA	70		400	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = -300mA, I <sub>B</sub> = -30mA		-0.35	-0.6	V

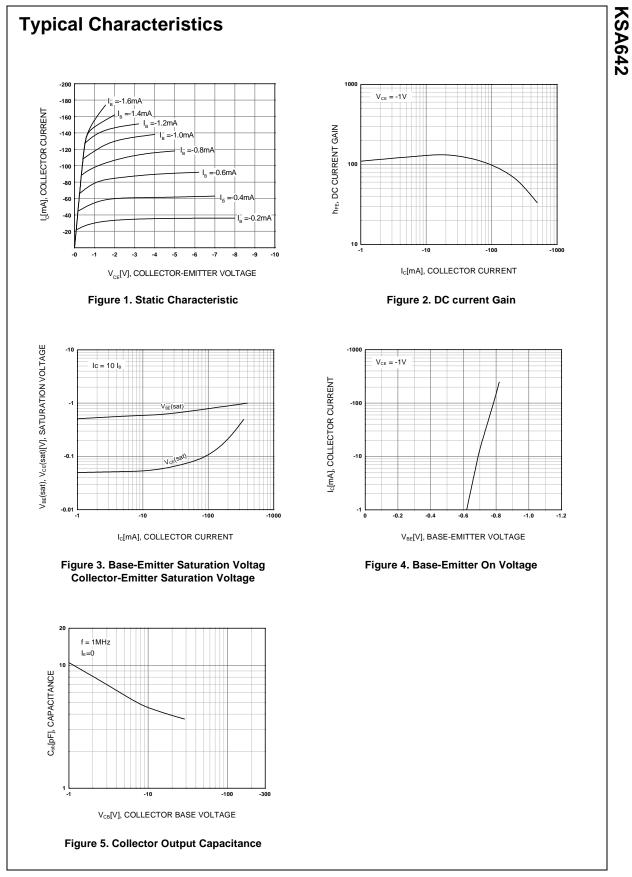
\* Pulse Test: PW≤350µs, Duty cycle≤2%

### h<sub>FE</sub> Classification

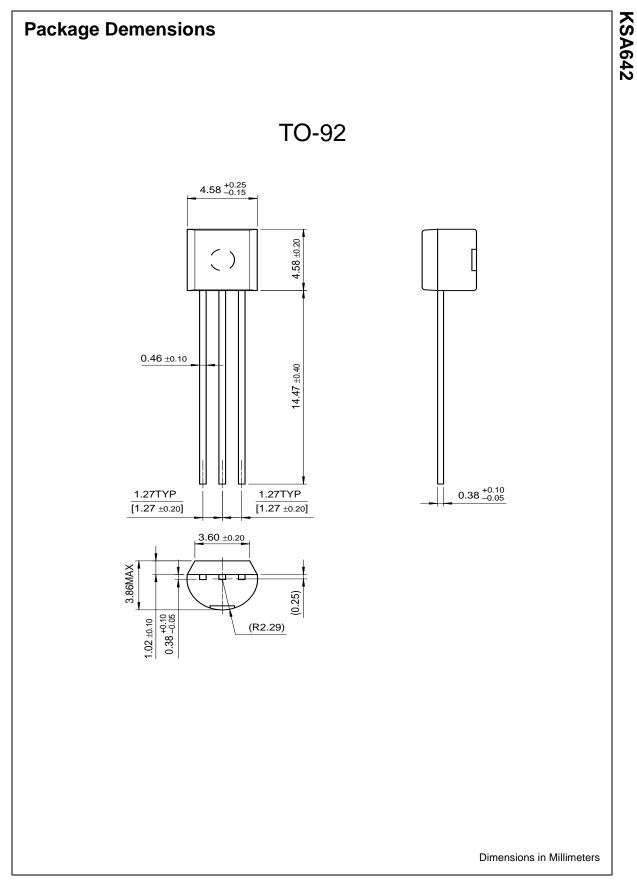
Classification	0	Y	G
h <sub>FE</sub>	70 ~ 140	120 ~ 240	200 ~ 400

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Rev. A1, June 2001

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