





NPN SURFACE MOUNT TRANSISTOR

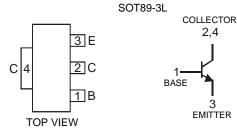
Features

- **Epitaxial Planar Die Construction**
- Complementary PNP Type Available (DXT5401)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)

Mechanical Data

- Case: SOT89-3L
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.072 grams (approximate)





Schematic and Pin Configuration

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	180	V
Collector-Emitter Voltage	V_{CEO}	160	V
Emitter-Base Voltage	V _{EBO}	6.0	V
Collector Current	Ic	600	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation @T _A = 25°C (Note 3)	P _D	1	W
Thermal Resistance, Junction to Ambient @T _A = 25°C (Note 3)	$R_{ hetaJA}$	125	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 4)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	180	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	160	_	V	$I_C = 1.0 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6.0	_	V	$I_E = 10\mu A, I_C = 0$
Collector Cutoff Current		_	50	nA	$V_{CB} = 120V, I_{E} = 0$
	Ісво			μΑ	$V_{CB} = 120V$, $I_E = 0$, $T_A = 100$ °C
Emitter Cutoff Current	I _{EBO}	_	50	nA	$V_{EB} = 4.0V, I_{C} = 0$
ON CHARACTERISTICS (Note 4)					
DC Current Gain		80	_		$V_{CE} = 5.0V, I_{C} = 1.0mA$
	h _{FE}	80	250	_	$V_{CE} = 5.0V, I_{C} = 10mA$
		30	_		$V_{CE} = 5.0V, I_{C} = 50mA$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		0.15 0.20	V	$I_C = 10 \text{mA}, I_B = 1.0 \text{mA}$
					$I_C = 50 \text{mA}, I_B = 5.0 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	_	1.0	٧	$I_C = 10mA$, $I_B = 1.0mA$
					$I_C = 50 \text{mA}, I_B = 5.0 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	$C_{ m obo}$	_	6.0	pF	$V_{CB} = 10V, f = 1.0MHz, I_{E} = 0$
Small Signal Current Gain	h _{fe}	50	200	_	$V_{CE} = 10V, I_{C} = 1.0mA, f = 1.0kHz$
Current Gain-Bandwidth Product	f _T	100	300	MHz	V _{CE} = 10V, I _C = 10mA, f = 100MHz
Noise Figure	NF	_	8.0	dB	$V_{CE} = 5.0V$, $I_C = 200\mu A$, $R_S = 1.0k\Omega$, $f = 1.0kHz$

Notes:

- 1. No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

 Device mounted on FR-4 PCB, pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
- Measured under pulsed conditions. Pulse width = 300µs. Duty cycle ≤ 2%.



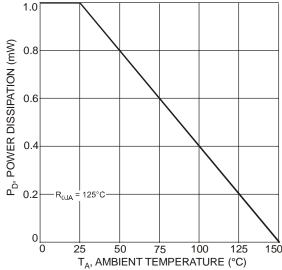


Fig. 1 Max Power Dissipation vs. Ambient Temperature

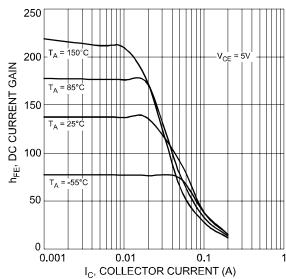
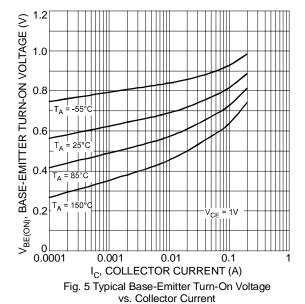


Fig. 3 Typical DC Current Gain vs. Collector Current



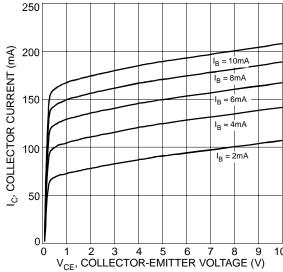


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

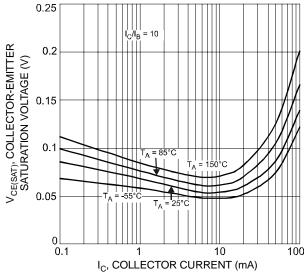


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

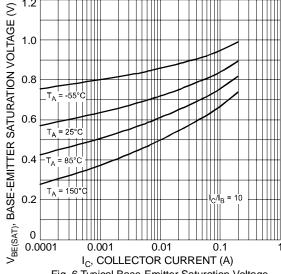
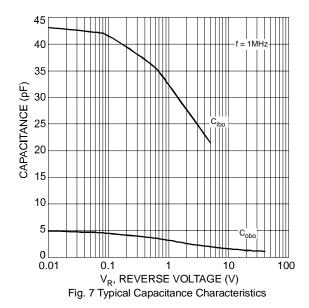


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current





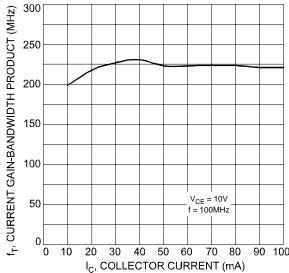


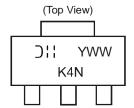
Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

Ordering Information (Note 5)

Device	Packaging	Shipping
DXT5551-13	SOT89-3L	2500/Tape & Reel

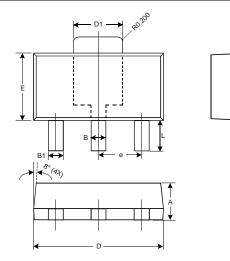
Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

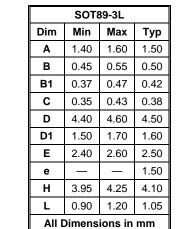
Marking Information



OH = Manufacturer's code marking K4N = Product type marking code YWW = Date code marking Y = Last digit of year ex: 7 = 2007 WW = Week code 01 - 52

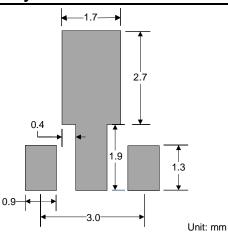
Package Outline Dimensions







Suggested Pad Layout



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