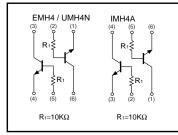
# General purpose (dual digital transistors) EMH4 / UMH4N / IMH4A

#### Features

1) Two DTC114T chips in a EMT or UMT or SMT package.

## Equivalent circuits



## •Package, marking, and packaging specifications

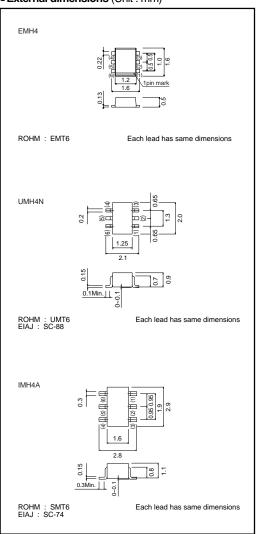
Туре	EMH4	UMH4N	IMH4A
Package	EMT5	UMT6	SMT6
Marking	H4	H4	H4
Code	T2R	TN	T110
Basic ordering unit (pieces)	8000	3000	3000

## Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Collector-base voltage		Vсво	50	V	
Collector-emitter voltage		VCEO	50	V	
Emitter-base voltage		VEBO	5	V	
Collector current		lc	100	mA	
Power dissipation	EMH4 / UMH4N	Pd	150(TOTAL)	mW *1 *2	
	IMH4A	Fu Fu	300(TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	
*1 120mW per element must not be e	xceeded	•			

\*1 120mW per element must not be exceeded.
\*2 200mW per element must not be exceeded.

#### •External dimensions (Unit : mm)

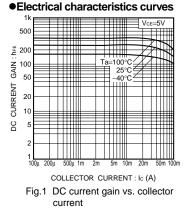


# Transistors

#### •Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	50	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVCEO	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВУево	5	-	-	V	Ιε=50μΑ
Collector cutoff current	Ісво	-	-	0.5	μA	Vcb=50V
Emitter cutoff current	Іево	-	-	0.5	μA	VEB=4V
Collector-emitter saturation voltage	VCE(sat)	-	-	0.3	V	Ic/IB=10mA/1mA
DC current transfer ratio	hfe	100	250	600	-	Vce=5V, Ic=1mA
Transition frequency	fr	-	250	-	MHz	Vce=10V, Ie= -5mA, f=100MHz *
Input resistance	R1	7	10	13	kΩ	_

\*Transition frequency of the device.



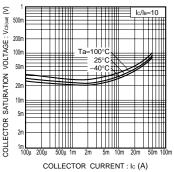


Fig.2 Collector-emitter saturation voltage vs. collector current

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