# General purpose (dual digital transistors) EMH9 / UMH9N / IMH9A

#### Features

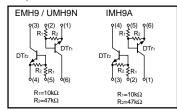
- 1) Two DTC114Ys chips in a EMT or UMT or SMT package.
- Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

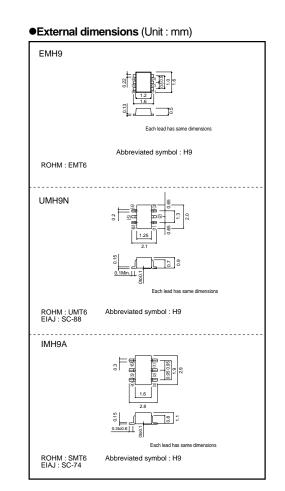
#### Structure

Epitaxial planar type NPN silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

#### Equivalent circuit





#### Packaging specifications

	Package Taping			
	Code	T2R	TN	T110
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMH9		0	-	-
UMH9N		-	0	-
ІМН9А		-	-	0

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### Transistors

#### Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIIV	-6		
Output current		lo	70	mA	
		IC (Max.)	100	IIIA	
Power dissipation	EMH9,UMH9N	Pd	150 (TOTAL)	*1 mW	
	IMH9A	Fu	300 (TOTAL)	*2	
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

\*1 120mW per element must not be exceeded.

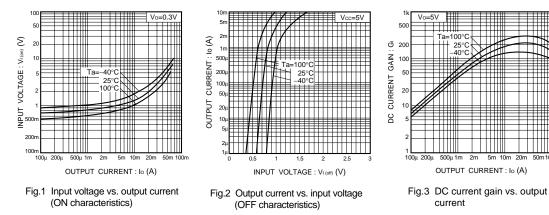
\*2 200mW per element must not be exceeded.

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltogo	VI (off)	-	-	0.3	V	Vcc=5V, Io=100μA	
Input voltage	VI (on)	1.4	-	-	V	Vo=0.3V, Io=1mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo/l=5mA/0.25mA	
Input current	h	-	-	0.88	mA	Vi=5V	
Output current	IO (off)	-	-	0.5	μΑ	Vcc=50V, VI=0V	
DC current gain	G	68	-	-	-	Vo=5V, Io=5mA	
Transition frequency	f⊤	-	250	-	MHz	Vce=10V, Ie= -5mA, f=100MHz *	
Input resistance	R1	7	10	13	kΩ	_	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	3.7	4.7	5.7	-	_	

#### •Electrical characteristics (Ta = 25°C)

\* Transition frequency of the device

#### •Electrical characteristic curves



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## Transistors

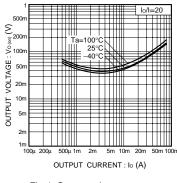


Fig.4 Output voltage vs. output current

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