

NPN SILICON TRANSISTOR

NE856M23

FEATURES

NEW MINIATURE M23 PACKAGE:

- World's smallest transistor package footprint leads are completely underneath package body
- Low profile/0.55 mm package height
- Ceramic substrate for better RF performance
- LOW NOISE FIGURE:

NF = 1.4 dB at 1 GHz

• HIGH COLLECTOR CURRENT:

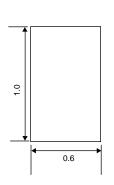
Ic MAX = 100 mA

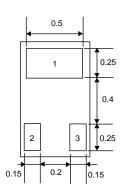
DESCRIPTION

The NE856M23 transistor is designed for low cost amplifier and oscillator applications. Low noise figure, high gain and high current capability equate to wide dynamic range and excellent linearity. NEC's new low profile/ceramic substrate style "M23" package is ideal for today's portable wireless applications. The NE856 is also available in chip, Micro-x, and eight different low cost plastic surface mount package styles.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE M23







BOTTOM VIEW

PIN CONNECTIONS

- 1. Collector
- 2. Emitter
- 3. Base

ELECTRICAL CHARACTERISTICS (TA = 25°C)

PART NUMBER EIAJ¹ REGISTERED NUMBER PACKAGE OUTLINE			NE856M23 2SC5649 M23			
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	
fτ	Gain Bandwidth at Vce = 3 V, Ic = 7 mA, f = 1 GHz	GHz	3	4.5		
NF	Noise Figure at VcE = 3 V, Ic = 7 mA, f = 1 GHz	dB		1.4	2.5	
S21E ²	Insertion Power Gain at VcE = 3 V, Ic = 7 mA, f = 1 GHz	dB	7	10.0		
hFE ²	Forward Current Gain at VcE = 3 V, Ic = 7 mA		80		145	
Ісво	Collector Cutoff Current at VcB = 10 V, IE = 0	μΑ			1	
ІЕВО	Emitter Cutoff Current at VEB = 1 V, IC = 0	μΑ			1	
CRE ³	Feedback Capacitance at VcB = 3 V, IE = 0, f = 1 MHz	pF		0.7	1.5	

Notes:

- 1. Electronic Industrial Association of Japan.
- 2. Pulsed measurement, pulse width \leq 350 µs, duty cycle \leq 2 %.
- 3. Capacitance is measured with emitter and case connected to the guard terminal at the bridge.

-California Eastern Laboratories

ABSOLUTE MAXIMUM RATINGS¹ (TA = 25°C)

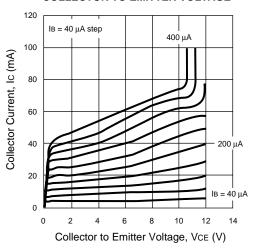
SYMBOLS	PARAMETERS	UNITS	RATINGS				
Vсво	Collector to Base Voltage	V	20				
VCEO	Collector to Emitter Voltage	V	12				
VEBO	Emitter to Base Voltage	V	3				
Ic	Collector Current	mA	100				
Рт	Total Power Dissipation	mW	TBD				
TJ	Junction Temperature	°C	150				
Tstg	Storage Temperature	°C	-65 to +150				

Note:

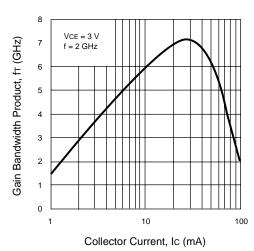
1. Operation in excess of any one of these parameters may result in permanent damage.

TYPICAL PERFORMANCE CURVES (TA = 25°C)

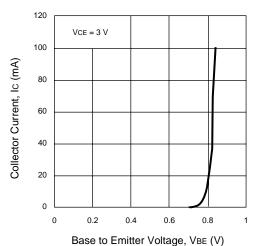
COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



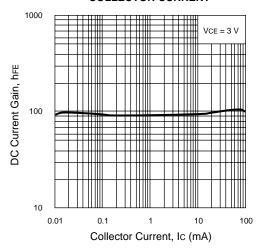
GAIN BANDWIDTH PRODUCT vs. COLLECTOR CURRENT



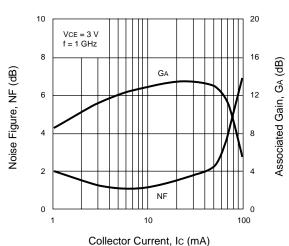
COLLECTOR CURRENT vs. BASE TO EMITTER VOLTAGE



DC CURRENT GAIN vs. COLLECTOR CURRENT



NOISE FIGURE/ASSOCIATED GAIN vs. COLLECTOR CURRENT



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02/10/2000