-500mA / -50V Digital transistors (with built-in resistors)

DTB114GK

Applications

Inverter, Interface, Driver

●Feature

- The built-in bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- Only the on / off conditions need to be set for operation, making the device design easy.
- 3) Higher mounting densities can be achieved.

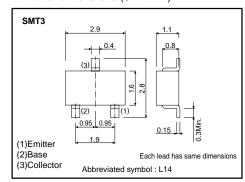
●Structure

PNP epitaxial planar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	SMT3
	Packaging type	Taping
	Code	T146
Part No.	Basic ordering unit (pieces)	3000
DTB114GK	0	

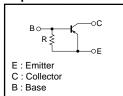
●External dimensions (Unit : mm)



●Equivalent circuit

R=10k Ω (typ.)

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●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Collector-base voltage	Vсво	-50	V	
Collector-emitter voltage	VCEO	-50	V	
Emitter-base voltage	Vево	-5	V	
Collector current	Ic	-500	mA	
Collector power dissipation	Pd *	200	mW	
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

^{*} Each pin mounted on the recommended land

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●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	-	-	V	Ic= -50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	-	-	V	I _E = -720μA
Collector cutoff current	Ісво	-	-	-0.5	μΑ	Vcb=-50V
Emitter cutoff curren	ІЕВО	-	-	-580	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.3	V	Ic/I _B = -50mA/-2.5mA
DC current transfer ratio	hfe	56	-	-	-	Ic=-50mA , Vc=-5V
Input resistance	R	7	10	13	kΩ	-
Transition frequency	f⊤ ∗	-	200	-	MHz	Vc=-10V , I=50mA , f=100MHz

^{*}Characteristics of built-in transistor

•Electrical characteristics curves

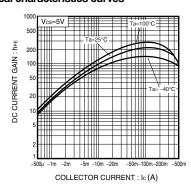


Fig.1 DC current transfer ratio vs. Collector current

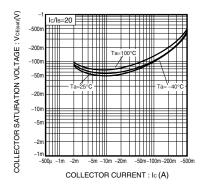


Fig.2 Collector-Emitter saturation voltage vs. Collector current

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Appendix1-Rev1.1