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## DTC144TE

## **Features**

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

### **Absolute Maximum Ratings**

Parameter	Symbol	Value	Unit			
Collector-Base Voltage	$V_{CBO}$	50	V			
Collector-Emitter Voltage	$V_{CEO}$	50	V			
Emitter-Base voltage	$V_{EBO}$	5	V			
Collector Current-Continuous	Ic	100	mA			
Collector Dissipation	Pc	150	mW			
Junction Temperature	TJ	150	°C			
Storage Temperature Range	T <sub>STG</sub>	-55~150	°C			

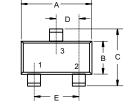
 Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0 and MSL Rating 1

### **Electrical Characteristics**

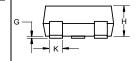
Sym	Parameter	Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage (I <sub>C</sub> =50uA, I <sub>E</sub> =0)	50			>
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage (I <sub>C</sub> =1mA, I <sub>B</sub> =0)	50			>
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage (I <sub>E</sub> =50uA, I <sub>C</sub> =0)	5			>
I <sub>CBO</sub>	Collector Cut-off Current (V <sub>CB</sub> =50V, I <sub>E</sub> =0)			0.5	uA
I <sub>EBO</sub>	Emitter Cut-off Current (V <sub>EB</sub> =4V, I <sub>C</sub> =0)			0.5	uA
h <sub>FE</sub>	DC Current Gain (V <sub>CE</sub> =5V, I <sub>C</sub> =1mA)	100	300	600	
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage (I <sub>C</sub> =10mA, I <sub>B</sub> =1mA)			0.3	>
R <sub>1</sub>	Input resistance	32.9	47	61.1	KΩ
f⊤	Transition Frequency (V <sub>CE</sub> =10V, I <sub>C</sub> =-5mA, f=100MHz)		250		MHz

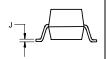
# **NPN Digital Transistor**





Base
 Emitter
 Collector





DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.059	.067	1.50	1.70	
В	.030	.033	0.75	0.85	
С	.057	.069	1.45	1.75	
D	.020 No	ominal	0.50Nom	inal	
Е	.035	.043	0.90	1.10	
G	.000	.004	.000	.100	
Н	.028	.031	.70	0.80	
J	.004	.008	.100	.200	
K	.010	.014	.25	.35	

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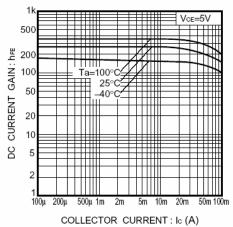


Fig.1 DC current gain vs. collector current

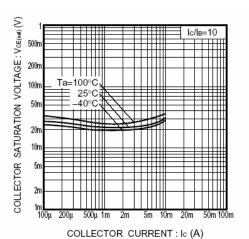
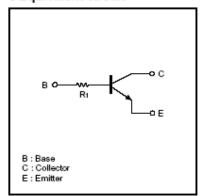


Fig.2 Collector-emitter saturation voltage vs. collector current

## ●Equivalent circuit





## **Ordering Information**

Device	Packing
(Part Number)-TP	Tape&Reel3Kpcs/Reel

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