500mA / 50V Digital transistors (with built-in resistors)

DTD113EK / DTD113ES

Applications

Inverter, Interface, Driver

Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

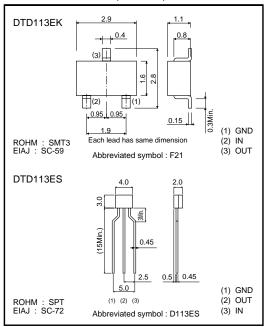
●Structure

NPN epitaxial planar silicon transistor (Resistor built-in type)

Package specifications

| | Package | SMT3 | SPT | | | | |
|----------|------------------------------|--------|--------|--|--|--|--|
| | Packaging type | Taping | Taping | | | | |
| | Code | T146 | TP | | | | |
| Part No. | Basic ordering unit (pieces) | 3000 | 5000 | | | | |
| DTD113EK | | 0 | - | | | | |
| DTD113E | - | 0 | | | | | |

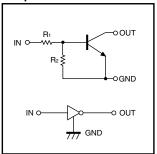
●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

| Parameter | Symbol | Limits | | - Unit |
|----------------------|--------|-------------------|-----|--------|
| raiametei | Symbol | DTD113EK DTD113ES | | |
| Supply voltage | Vcc | 5 | V | |
| Input voltage | Vin | -10 to +10 | | V |
| Output current | lc | 500 | | mA |
| Power dissipation | Pd | 200 | 300 | mW |
| Junction temperature | Tj | 150 | | ొ |
| Storage temperature | Tstg | -55 to +150 | | ာ |

●Equivalent circuit



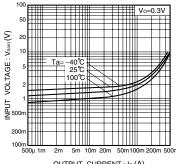
R₁=R₂=1.0kΩ

●Electrical characteristics (Ta=25°C)

| Parameter | Symbol | Min. | Тур. | Max. | Unit | Conditions |
|----------------------|--------------------|------|------|------|------|---------------------------|
| Input voltage | VI(off) | _ | - | 0.5 | v | Vcc=5V, Io=100μA |
| Input voltage | VI(on) | 3 | - | - | V | Vo=0.3V, Io=20mA |
| Output voltage | V _{O(on)} | _ | 0.1 | 0.3 | V | lo/l=50mA/2.5mA |
| Input current | lı | - | - | 7.2 | mA | V _I =5V |
| Output current | IO(off) | - | - | 0.5 | μΑ | Vcc=50V, Vi=0V |
| DC current gain | Gı | 33 | - | - | - | Vo=5V, Io=50mA |
| Input resistance | R ₁ | 0.7 | 1 | 1.3 | kΩ | _ |
| Resistance ratio | R2/R1 | 0.8 | 1 | 1.2 | - | - |
| Transition frequency | f ⊤ * | ı | 200 | _ | MHz | Vc=10V, I=-50mA, f=100MHz |

^{*} Characteristics of built-in transistor

•Electrical characteristics curves



OUTPUT CURRENT: Io (A)
Fig.1 Input voltage vs. output current
(ON characteristics)

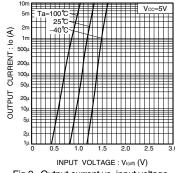
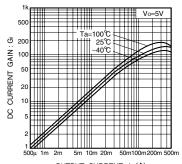
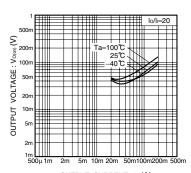


Fig.2 Output current vs. input voltage (OFF characteristics)



OUTPUT CURRENT: Io (A)
Fig.3 DC current gain
vs. output current



OUTPUT CURRENT: lo(A) Fig.4 Output voltage vs. output current

Notes

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Appendix1-Rev1.1