Power MOSFET and Schottky Diode

20 V, 2.5 A, P-Channel with Schottky Barrier Diode, TSOP-6 Dual

Features

- FETKY® P-Channel and Schottky Diode
- Small Size (3 x 3 mm) Dual TSOP-6 Package
- Leading Edge Trench Technology for Low On Resistance
- Low VF Schottky Diode
- Common Drain/Cathode for Ease of Board Layout
- This is a Pb-Free Device

Applications

- DC-DC Converters; Configured as Asynchronous Buck
- Portable Devices like PDA's, Cellular Phones, and Hard Drives

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Param	Symbol	Value	Unit						
Drain-to-Source Voltag	V _{DSS}	20	V						
Gate-to-Source Voltage	Э		V_{GS}	±12	V				
Continuous Drain	Steady	T _A = 25°C	I _D	2.3	Α				
Current (Note 1)	State	T _A = 85°C		1.6					
	t ≤ 5 s	T _A = 25°C		2.5					
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	1.1	W				
	t ≤ 5 s			1.3					
Continuous Drain		T _A = 25°C	I _D	1.7	Α				
Current (Note 2)	Steady					T _A = 85°C		1.2	
Power Dissipation (Note 2)	State	T _A = 25°C	P _D	0.56	W				
Pulsed Drain Current	t _p =	10 μs	I_{DM}	6.9	Α				
Operating Junction and	T _J , T _{STG}	-25 to 150	°C						
Source Current (Body D	I _S	0.9	Α						
Lead Temperature for S (1/8" from case for 10 s		urposes	TL	260	°C				

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces). Both die on.
- 2. Surface Mounted on FR4 Board using the minimum recommended pad size (Cu area = 0.0465 in sq [2 oz] including traces). Both die on.



ON Semiconductor®

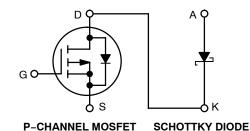
http://onsemi.com

MOSFET

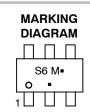
V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX (Note 1)
20 V	145 mΩ @ 4.5 V	2.5 A
	200 mΩ @ 2.5 V	2.5 A

SCHOTTKY DIODE

V _R MAX	V _F TYP	I _F MAX
20 V	0.40 V	1.0 A







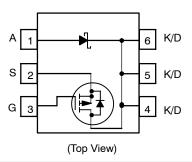
S6 = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTION



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 6 of this data sheet.

SCHOTTKY DIODE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	20	V
DC Blocking Voltage	V _R	20	V
Average Rectified Forward Current	I _F	1.0	Α

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
BOTH DIE ON	<u>.</u>		
Junction-to-Ambient - Steady State (Note 3)	$R_{ hetaJA}$	115	
Junction-to-Ambient $-t \le 5$ s (Note 3)	$R_{ heta JA}$	95	°C/W
Junction-to-Ambient - Steady State (Note 4)	$R_{ heta JA}$	225	
ONE DIE ON			
Junction-to-Ambient - Steady State (Note 3)	$R_{ heta JA}$	225	
Junction-to-Ambient $-t \le 5$ s (Note 3)	$R_{ heta JA}$	125	°C/W
Junction-to-Ambient - Steady State (Note 4)	$R_{ hetaJA}$	305	

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
 Surface Mounted on FR4 Board using the minimum recommended pad size (Cu area = 30 mm sq [2 oz] including traces).

$\textbf{MOSFET ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}\text{C unless otherwise noted})$

Parameter	Symbol	Test Condition	ns	Min	Тур	Max	Unit
OFF CHARACTERISTICS							_
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 25$	50 μA	20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				14.4		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V 40VV 0V	T _J = 25°C			1.0	μΑ
		$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$	T _J = 125°C			10	
Gate-to-Source Leakage Current	I _{GSS}	V _{DS} = 0 V, V _{GS} = 1	±12 V			100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 250 \mu A$		0.6	1.2	1.4	V
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				3.2		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	$V_{GS} = 4.5$, $I_D = 2.0$ A $V_{GS} = 2.5$, $I_D = 1.7$ A			95	145	mΩ
					150	200	
Forward Transconductance	9FS	V _{DS} = -5.0 V, I _D = -2.5 A			4.0		S
CHARGES, CAPACITANCES AND GA	ATE RESISTAN	CE				-	
Input Capacitance	C _{ISS}				390		pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = -10 \text{ V}$			75		
Reverse Transfer Capacitance	C _{RSS}				37		
Total Gate Charge	Q _{G(TOT)}				3.7	5.5	nC
Threshold Gate Charge	Q _{G(TH)}	$V_{GS} = -4.5 \text{ V}, V_{DS} = -10 \text{ V},$			0.7		
Gate-to-Source Charge	Q _{GS}	$I_D = -2.2 \text{ A}$,		1.1		
Gate-to-Drain Charge	Q_{GD}				1.2		

5. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

$\textbf{MOSFET ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}C \ unless \ otherwise \ noted)$

Parameter	Symbol	Test Condition	ns	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS	S (Note 6)	•	•		•		•
Turn-On Delay Time	t _{d(ON)}				6.7		ns
Rise Time	t _r	V _{GS} = 4.5 V, V _{DD} =	10 V,		12.7		
Turn-Off Delay Time	t _{d(OFF)}	V_{GS} = 4.5 V, V_{DD} = 10 V, I_{D} = 1.0 A, R_{G} = 6.0 Ω			13.2		
Fall Time	t _f			11			
DRAIN-SOURCE DIODE CHARA	ACTERISTICS						
Forward Recovery Voltage	V _{SD}	V 0V 10 00A	T _J = 25°C		-0.8	-1.2	.,
		$V_{GS} = 0 \text{ V, IS} = -0.8 \text{ A}$	T _J = 125°C		-0.6		V
Reverse Recovery Time	t _{RR}		•		7.4		
Charge Time	t _a	V_{GS} = 0 V, d_{ISD}/d_t = 100 A/ μ s, I_S = -1.0 A			4.8		ns
Discharge Time	t _b				2.6		
Reverse Recovery Time	Q_{RR}				2.4		nC

$\textbf{SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}C \ unless \ otherwise \ noted)$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.5 A		0.35	0.4	V
Forward Voltage		I _F = 1.0 A		0.4	0.45	
Maximum Instantaneous	I _R	V _R = 10 V		15.7	200	μΑ
Reverse Current		V _R = 20 V		29.6	400	

P-CHANNEL

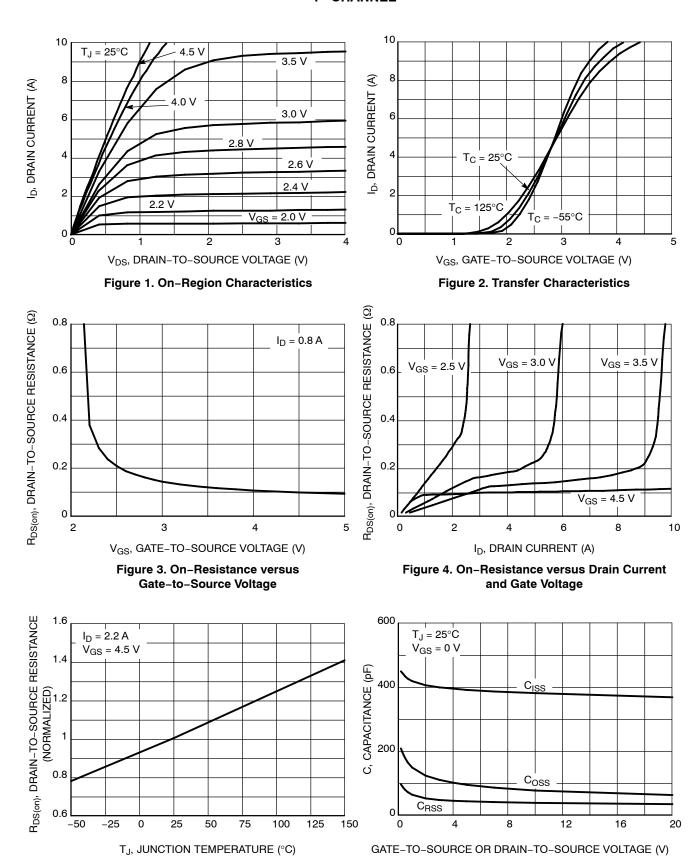


Figure 6. Capacitance Variation

Figure 5. On-Resistance Variation with

Temperature

P-CHANNEL

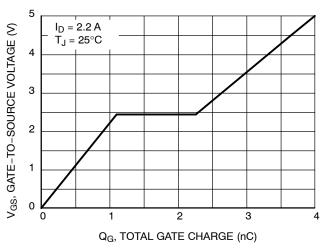


Figure 7. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

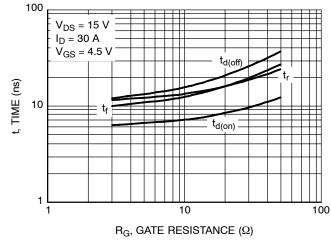


Figure 8. Resistive Switching Time Variation versus Gate Resistance

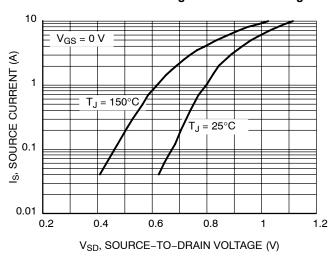


Figure 9. Diode Forward Voltage versus Current

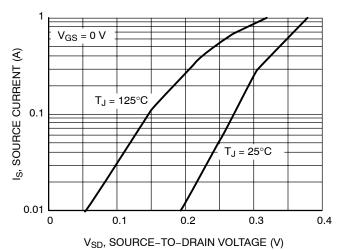


Figure 10. Schottky Diode Forward Voltage versus Current

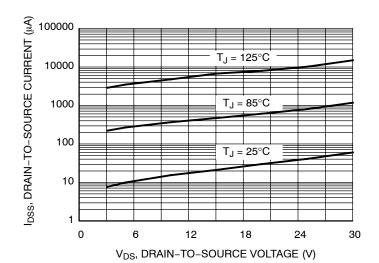


Figure 11. Schottky Diode Reverse Current

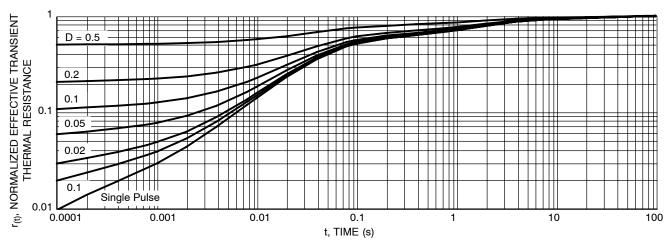


Figure 12. Thermal Response

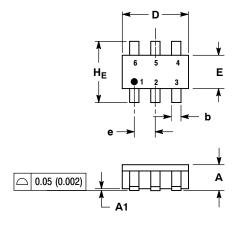
ORDERING INFORMATION

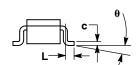
Device	Package	Shipping [†]
NTGF3123PT1G	TSOP6 (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

TSOP-6 CASE 318G-02 **ISSUE S**



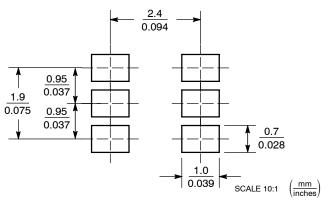


NOTES:

- DIMENSIONING AND TOLERANCING PER
- DIMENSIONING AND TOLEHANCING PEH ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
 DIMENSIONS A AND B DO NOT INCLUDE
- MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	М	ILLIMETE	RS		INCHES	
DIM	MIN	NOM	MAX	MIN	MON	MAX
Α	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.38	0.50	0.010	0.014	0.020
С	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	ı	10°	0°	-	10°

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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