

# **HAT1035R**

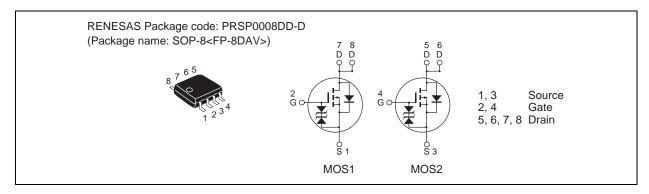
# Silicon P Channel Power MOS FET High Speed Power Switching

REJ03G0845-0100 Rev.1.00 Apr.22,2005

#### **Features**

- Low on-resistance
- Capable of -4 V gate drive
- Low drive current
- High density mounting

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to Source voltage	V <sub>DSS</sub>	-150	V
Gate to Source voltage	V <sub>GSS</sub>	±15	V
Drain current	I <sub>D</sub>	-0.25	A
Drain peak current	I <sub>D(pulse)</sub> Note1	-1	А
Body-Drain diode reverse Drain current	I <sub>DR</sub>	-0.25	А
Channel dissipation	P <sub>ch</sub> Note2	1	W
Channel dissipation	P <sub>ch</sub> Note3	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

- 2. 1 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)
- 3. 2 Drive operation: When using the glass epoxy board (FR4 40 x 40 x 1.6 mm)

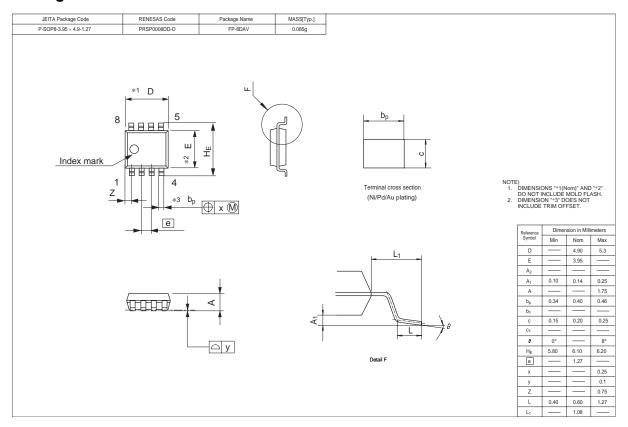
# **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to Source breakdown	V <sub>(BR)DSS</sub>	-150	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$	
voltage							
Gate to Source breakdown voltage	$V_{(BR)GSS}$	±15	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$	
Gate to Source leak current	$I_{GSS}$	_	_	±10	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$	
Zero Gate voltage Drain current	I <sub>DSS</sub>	_	_	<b>-</b> 5	μΑ	$V_{DS} = -150 \text{ V}, V_{GS} = 0$	
Gate to Source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	
Static Drain to Source on state	R <sub>DS(on)</sub>	_	5.0	6.2	Ω	$I_D = -0.25 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note4}}$	
resistance	R <sub>DS(on)</sub>	_	6.0	7.5	Ω	$I_D = -0.25 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note4}}$	
	R <sub>DS(on)</sub>	_	7.0	10.0	Ω	$I_D = -1 \text{ A}, V_{GS} = -5 \text{ V}^{\text{Note4}}$	
Forward transfer admittance	y <sub>fs</sub>	0.29	0.45	_	S	$I_D = -0.25 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss	_	92	_	pF	V <sub>DS</sub> = -10 V	
Output capacitance	Coss	_	37	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	10	_	pF	f = 1 MHz	
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$V_{GS} = -5 \text{ V}, I_D = -0.25 \text{ A},$	
Rise time	t <sub>r</sub>	_	13	_	ns	$V_{DD} \cong -30 \text{ V}$	
Turn-off delay time	t <sub>d(off)</sub>	_	22	_	ns		
Fall time	t <sub>f</sub>	_	15	_	ns		
Body-Drain diode forward voltage	$V_{DF}$	_	-0.9	-1.4	V	$IF = -0.25 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-Drain diode reverse	t <sub>rr</sub>	_	80	_	ns	$IF = -0.25 \text{ A}, V_{GS} = 0$	
recovery time						$diF/dt = 50 A/\mu s$	

Notes: 4. Pulse test

# **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1035R-EL-E	2500 pcs.	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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