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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

HAT2215R, HAT2215RJ

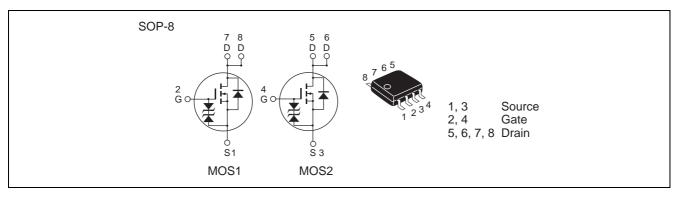
Silicon N Channel Power MOS FET High Speed Power Switching

> REJ03G0486-0300 Rev.3.00 Dec.22.2004

Features

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting

Outline



Absolute Maximum Ratings

				(Ta = 25°C)
Item	Symbol	Rat	11	
		HAT2215R	HAT2215RJ	Unit
Drain to source voltage	V _{DSS}	80	80	V
Gate to source voltage	V _{GSS}	±20	±20	V
Drain current	I _D	3.4	3.4	A
Drain peak current	I _{D(pulse)} Note1	20.4	20.4	A
Reverse drain current	I _{DR}	3.4	3.4	A
Avalanche current	I _{AP} Note 2	_	3.4	A
Avalanche energy	E _{AR} Note 2	_	1.54	mJ
Channel dissipation	Pch Note3	1.5	1.5	W
Channel dissipation	Pch Note4	2.2	2.2	W
Channel temperature	Tch	150 150		°C
Storage temperature	Tstg	-55 to +150	-55 to +150	°C

Notes: 1. PW \leq 10 $\mu s,\,duty\,cycle \leq$ 1 %

2. Value at Tch = 25° C, Rg $\geq 50 \Omega$

3. 1 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s

4. 2 Drive operation; When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s



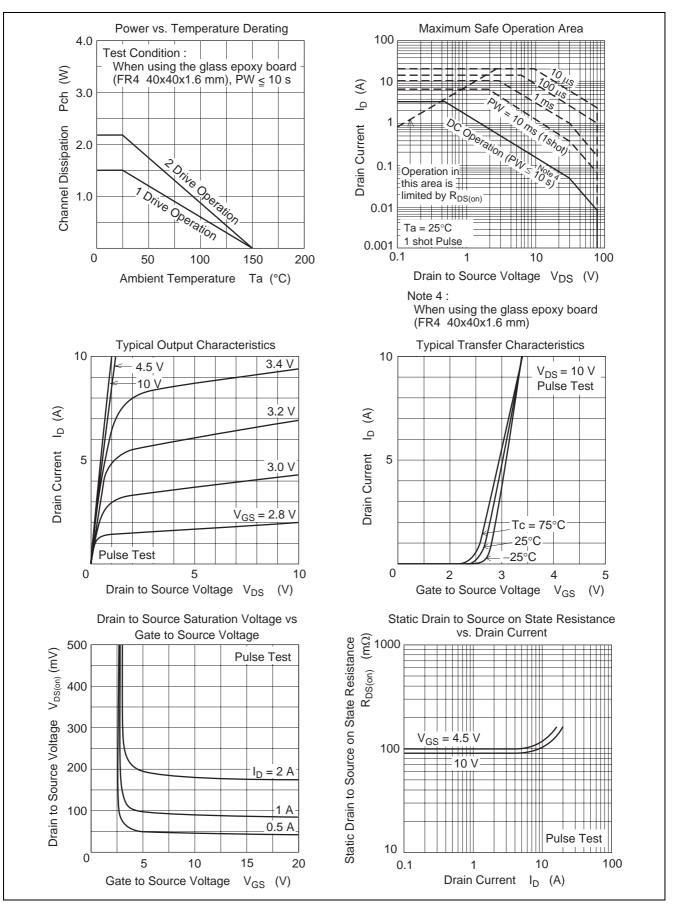
Electrical Characteristics

Item		Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage		V _{(BR)DSS}	80	_		V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage		V _{(BR)GSS}	±20	_		V	$I_{G} = \pm 100 \ \mu A, V_{GS} = 0$
Gate to source leak current		I _{GSS}		_	±10	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current		I _{DSS}	_	—	1	μΑ	$V_{DS} = 80 V, V_{GS} = 0$
Zero gate voltage drain current	HAT2215R	I _{DSS}		_		μΑ	V _{DS} = 64 V, V _{GS} = 0 Ta = 125°C
	HAT2215RJ	I _{DSS}	_	—	10	μΑ	
Gate to source cutoff voltage		V _{GS(off)}	1.0	—	2.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$
Static drain to source on state		R _{DS(on)}	_	88	115	mΩ	$I_D = 1.7 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note5}}$
resistance		R _{DS(on)}	_	100	145	mΩ	$I_D = 1.7 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note5}}$
Forward transfer admittance		y _{fs}	4.2	7.0	_	S	$I_D = 1.7 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note5}}$
Input capacitance		Ciss	_	400		pF	V _{DS} = 10 V
Output capacitance		Coss	_	57		pF	$V_{GS} = 0$
Reverse transfer capacitance		Crss	_	24		pF	f = 1MHz
Total gate charge		Qg	_	7.3		nC	V _{DD} = 25 V
Gate to source charge		Qgs	_	1.1		nC	V _{GS} = 10 V I _D = 3.4 A
Gate to drain charge		Qgd	_	1.3		nC	
Turn-on delay time		t _{d(on)}	_	6.0		ns	V _{GS} =10 V, I _D = 1.7 A
Rise time		tr	_	4.0		ns	$V_{DD} \approx 30 \text{ V}$ $R_L = 17.6 \Omega$ $R_g = 4.7 \Omega$
Turn-off delay time		t _{d(off)}		39	_	ns	
Fall time		t _f		3.5	_	ns	
Body-drain diode forward voltage		V_{DF}		0.83	1.08	V	$IF = 3.4 \text{ A}, V_{GS} = 0^{Note5}$
Body–drain diode reverse		t _{rr}		30	_	ns	IF =3.4 A, V _{GS} = 0
recovery time							diF/ dt = 100 A/µs

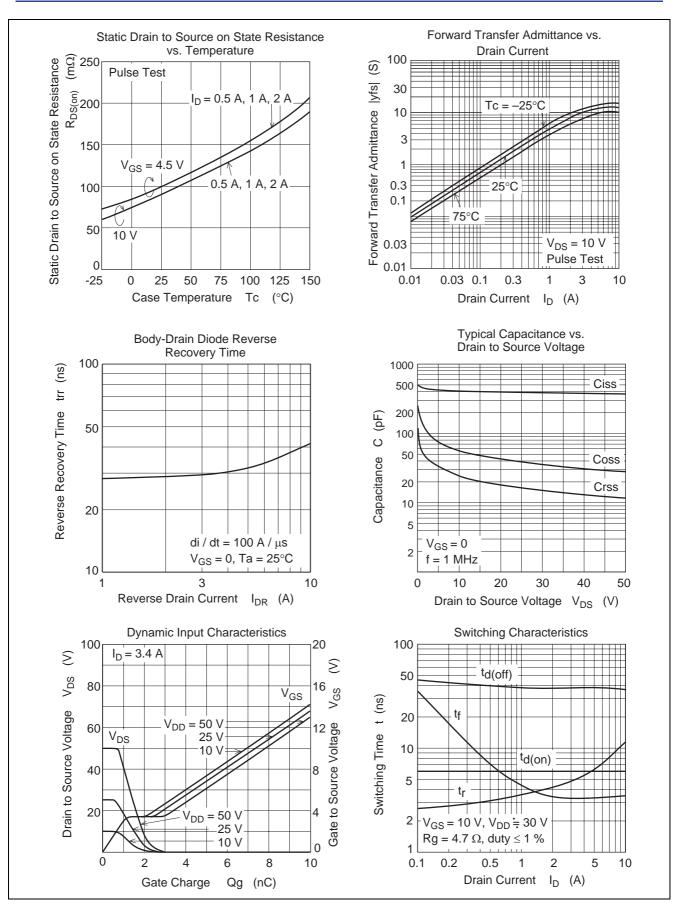
Notes: 5. Pulse test



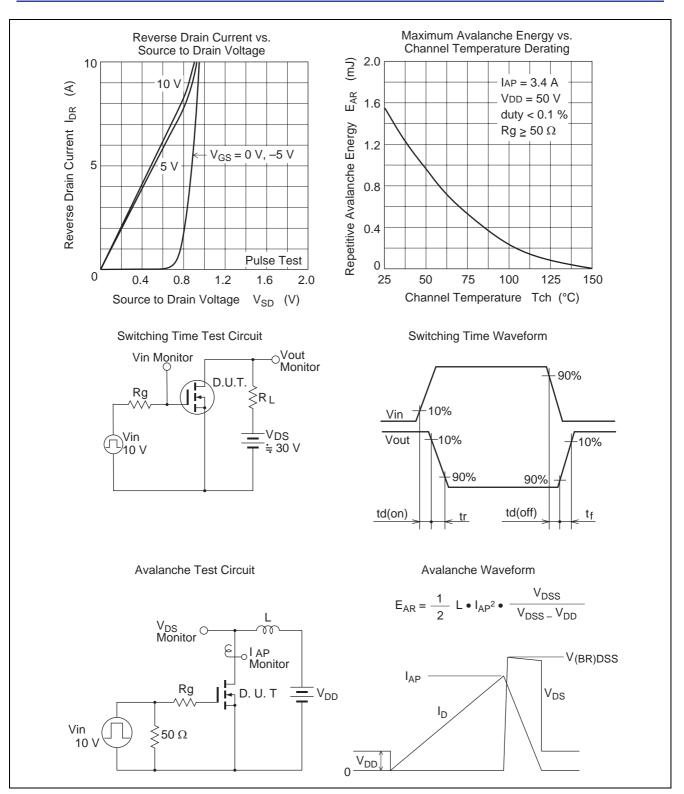
Main Characteristics

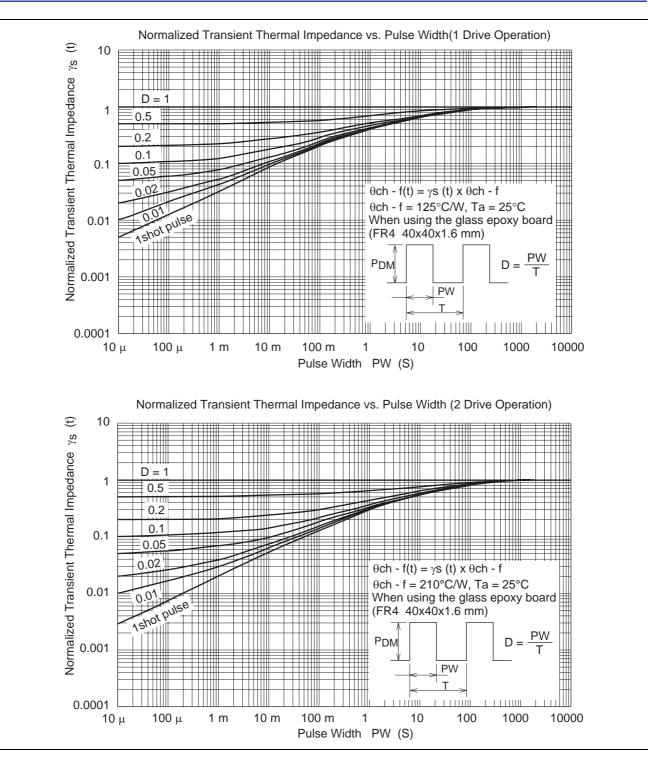






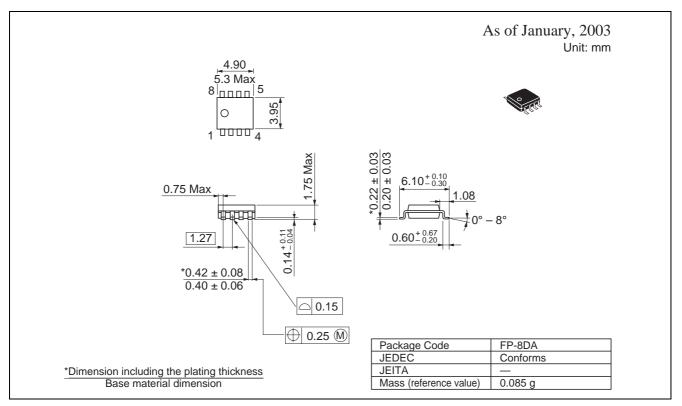








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2215R-EL-E	2500 pcs	Taping
HAT2215RJ-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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