

Low Voltage CMOS Driver Circuit

Features

- Four low resistance output drivers for bipolar or unipolar watch stepping motors.
- Low transversal transition current.
- Very low current consumption: 0.1 µA at 25°C.
- Two different output resistances programmable by metal mask.
- Wide power supply voltage range: 1.1 to 3.5 V.
- Tristate input for applications as fast bus driver.
- ESD and latch-up protections on input and output pads.

Description

The EM5060 (previously named H5060) is a low power integrated circuit in HCMOS Silicon Gate Technology designed to drive bipolar or unipolar stepping motors.

This device contains four identical and independent noninverting circuits which can be connected by metal mask programation so as to obtain two identical non-inverting circuits with a lower resistance output.

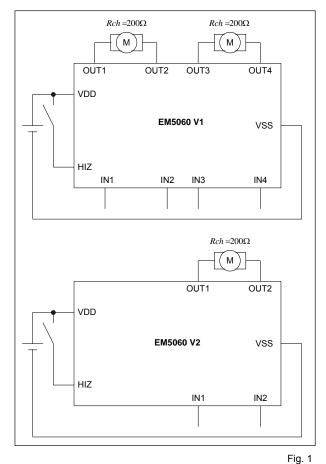
Each buffer is driven by a special cell which dephases the P and N transistor signal input, for a minimization of the transversal transition current.

A tristate input HIZ, with internal pulldown resistor provides the high impedance state of the four outputs.

Application

- Motor driver for watch/clock application
- Bus drivers
- LED driver

Functional Diagram



Pin Assignment

utput buffer n°4 utput buffer n°3 utput buffer n°2 utput buffer n°1			i r	/DD NC OUT	1 OUT2 NC	
utput buffer n°2			i r	DD NC OUT	1 OUI2 NC	
utput buffer n°2						
utput buffer n°1						
ositive supply voltage						
ri state input		нід ■ ЕМ 5060 ∨1	HIZ 🗖	EM 5060	V2	
put buffer n°1						
put buffer n°2						
put buffer n°3						
put buffer n°4		IN1 IN2 IN3 IN4 VSS	١	NC IN1 IN2	NC VSS	
egative supply voltage						
r 1 1	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4 HIZ EM5060 ∨1 EM5060 ∨1 HIZ EM5060 ∨1 EM5060 ∨1	ri state input put buffer n°1 put buffer n°2 put buffer n°3 put buffer n°4 IN1 IN2 IN3 IN4 VSS NC IN1 IN2 NC VSS



EM5060

Absolute Maximum Ratings

Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V_{DD}	-0.3		5.5	V
Voltage at	Vpin	Vss-0.3		V _{DD} +0.3	V
remaining pin					
Storage	Tstore	-55		+120	°C
temperature					

Table 1

Stresses above these listed maximum ratings may cause permanent damage to the device. Exposure to conditions beyond specified electrical characteristics may affect device reliability or cause malfunction.

Recommended Operating Conditions

Parameter	Symbol	Value	Units
Ambient temperature	Т	25	°C
Motor resistance	R _{ch}	200	Ohms
Positive supply	V _{DD}	1.55	V
Negative supply	Vss	0.0	V
Supply source resistance	Rı	10	Ohms
			Table 2

Electrical and Switching Characteristics

at recommended operating conditions (valid unless otherwise specified)

Handling Procedures

This device contains circuitry to protect the terminals against damage due to high static voltages or electrical fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than minimum rated voltages to this circuit.

Operating Conditions

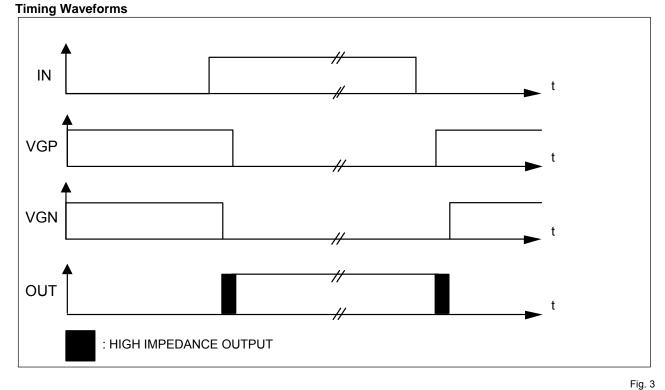
Parameter	Symbol	Min	Тур	Max	Units
Operating	T _{opr}	-20		+70	°C
temperature	-				

Table 3

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply voltage	V _{DD}	Operating	1.1	1.55	3.5	V
Standby current		Imot = 0			100	nA
		IN1, IN2, IN3, IN4				
		at V _{DD} or V _{SS} HIZ at V _{SS} or open				
Innuto						
Inputs Pulse width		V _{DD} = 1.2 V				
	twi	$V_{II} = V_{SS}$	1			ms
	t _{WH}	$V_{\rm IH} = V_{\rm DD}$	1			ms
	VIL	Overall voltage range		Vss	0.4	V
Voltage	VIH		V _{DD} -0.3	V _{DD}		V
HIZ Input Current	I _{HIZ}	HIZ at V_{DD}	0.5	2	5	μA
Outputs						
Motor Output Current	I _{OUT}	$R_{ch} = 200 \Omega, V_{DD} = 1.2 V$				
		Version V1	±4.3			mA
		Version V2 V _{DD} = 1.50 V	±4.8	±5.0		mA
		Version V1	±6.0			mA
		Version V2	±6.4	±6.6		mA
		V _{DD} =3.0 V				
		Version V1	±13.0			mA
		Version V2	±13.3	±13.5		mA
Timing Characteristics				_	100	
Propagation delay	t _{PHL}	$V_{DD} = 1.2 V, CL = 30 pF$		5	100	μs
Transition time	t _{PLH} t _{THL}	$V_{DD} = 1.2 V, CL = 30 pF$		5	100	μs
	t _{TLH}	$V_{DD} = 1.2 V, CL = 30 pF$		3	100	μs
	SILII	V _{DD} = 1.2 V,CL = 30pF		3	100	µs Table

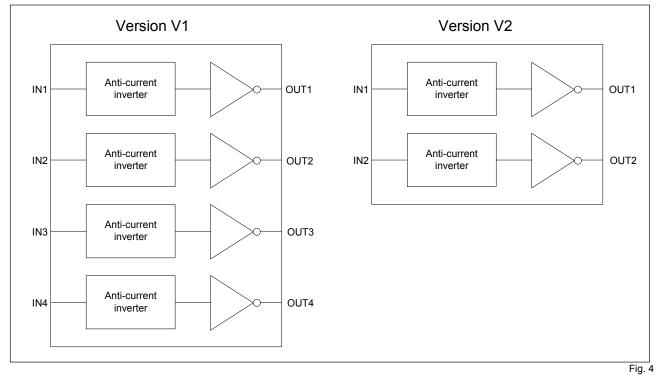


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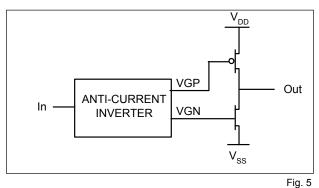
Block Diagram



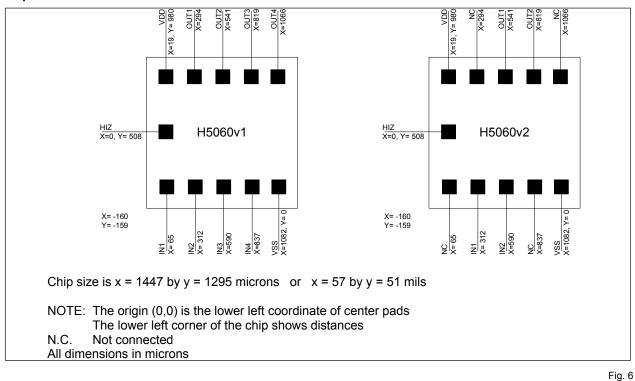




Functional Description



Chip Information



Ordering Information

EM5060 is available in two versions:

- Version V1 contains four input/outputs (INPUTS = IN1, IN2, IN3, IN4 ; OUTPUTS = OUT1, OUT2, OUT3, OUT4).
- Version V2 contains two input/outputs (INPUTS = IN1, IN2; OUTPUTS = OUT1, OUT2).

When ordering, please specify the complete Part Number below.

Part Number	Version	Die & Delivery Form
EM5060V1WP11	V1	Die in waffle pack, 11 mils thickness
EM5060V1WS11	V1	Sawn wafer, 11 mils thickness
EM5060V2WP11	V2	Die in waffle pack, 11 mils thickness
EM5060V2WS11	V2	Sawn wafer, 11 mils thickness

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