

PN5432 PN5433 PN5434



N-Channel Switch

This device is designed for analog or digital switching applications where very low On Resistance is mandatory. Sourced from Process 58. See J108 for characteristics.

Absolute Maximum Ratings*

TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	25	V
V _{GS}	Gate-Source Voltage	-25	V
I _{GF}	Forward Gate Current	10	mA
T _J , T _{stg}	Operating and Storage Junction Temperature Range -55 to +150 °C		°C

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

These ratings are based on a maximum junction temperature of 150 degrees C.
 These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics

TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		PN5432 / 5433 / 5434	7
P _D	Total Device Dissipation	350	mW
	Derate above 25°C	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	°C/W

N-Channel Switch

(continued)

ns

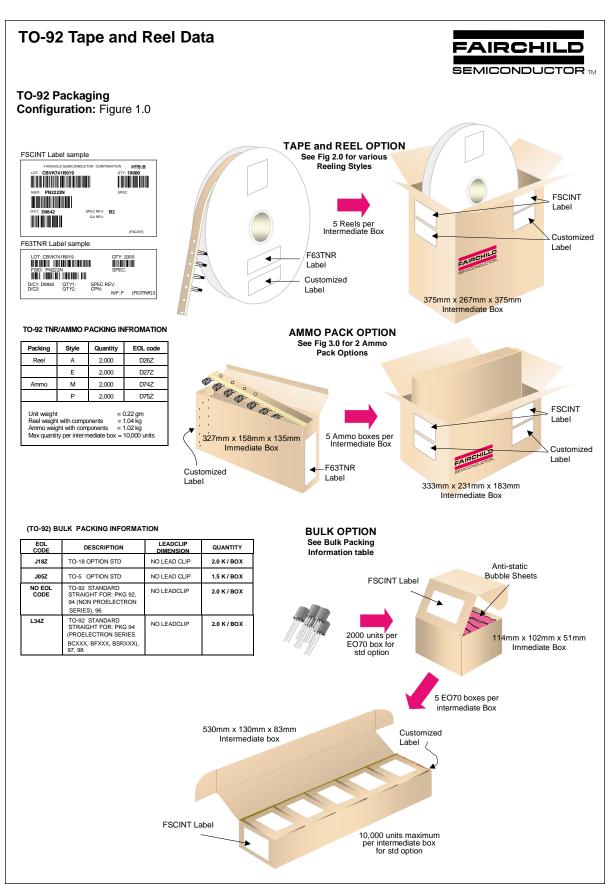
Symbol	Parameter	Test Conditions		Min	Max	Units
	DACTEDICTICS					
	RACTERISTICS Gate-Source Breakdown Voltage	1 101 1		-25		V
V _{(BR)GSS}	•	$I_G = 1.0 \mu\text{A}, V_{DS} = 0$		-25	000	
I _{GSS}	Gate Reverse Current	$V_{GS} = 15 \text{ V}, V_{DS} = 0$ $V_{GS} = 15 \text{ V}, V_{DS} = 0, T_A = 150$	°C		-200 -200	pA nA
I _{D(off)}	Drain Cutoff Leakage Voltage	$V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V}$ $V_{GS} = 10 \text{ V}, V_{DS} = 5.0 \text{ V},$	J		-200	pA
	0.1.0.0.1.0.1.0.1	T _A = 150 °C	- 400	4.0	-200	nA
$V_{\text{GS(off)}}$	Gate-Source Cutoff Voltage	$V_{DS} = 5.0 \text{ V}, I_{D} = 3.0 \text{ nA}$	5432 5433	-4.0 -3.0	-10 -9.0	V V
			5434	-3.0 -1.0	-9.0 -4.0	V
ON CHAR	RACTERISTICS Zero-Gate Voltage Drain Current*	V _{DS} = 15 V, V _{GS} = 0	5432	150		mA
I _{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 15 \text{ V}, V_{GS} = 0$	5432 5433	150 100		mA mA
			5434	30		mA
V _{DS(on)}	Drain-Source On Voltage	$I_D = 10 \text{ mA}, V_{GS} = 0$	5432		50	mV
			5433		70	mV
_	Drain-Source On Resistance	$I_D = 10 \text{ mA}, V_{GS} = 0$	5434	2.0	100	mV
r _{DS(on)}	Drain-Source On Resistance	$I_D = IU IIIA, V_{GS} = U$	5432 5433	2.0	5.0 7.0	Ω
			5434		10	Ω
		$I_D = 0$, $V_{GS} = 0$, $f = 1.0 \text{ kHz}$				
			5432 5433	2.0	5.0 7.0	Ω
			5434		10	Ω
0.44.1.0	IONAL OLIABAGTERIOTIOS		<u> </u>			22
C _{iss}	IGNAL CHARACTERISTICS Input Capacitance	V _{DS} = 0 , V _{GS} = 10 V, f = 1.0 I	MHz		30	pF
	Reverse Transfer Capacitance	$V_{DS} = 0$, $V_{GS} = 10$ V, $f = 1.0$ I			15	pF
SWITCHIN	NG CHARACTERISTICS	V _{DS} = 0 , V _{GS} = 10 V, I = 1.01	IVIIIZ		15	pr
t _d	Delay Time	$V_{DD} = 1.5 \text{ V}, \ V_{GS(on)} = 0,$			4.0	ns
t _r	Rise Time	$I_{D(on)} = 10 \text{ mA}$			1.0	ns
ts	Storage Time	$V_{GS(off)} = 12 \text{ V},$				
15		$V_{DS(on)} = 50 \text{ mV}$	5432		6.0	ns
		$V_{DS(on)} = 70 \text{ mV}$	5433		6.0	ns
		$V_{DS(on)} = 100 \text{ mV}$	5434		6.0	ns

 $V_{GS(off)} = 12 \text{ V}$

Fall Time

tf

^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

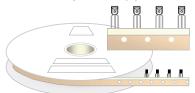


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TO-92 Tape and Reel Data, continued

TO-92 Reeling Style Configuration: Figure 2.0

Machine Option "A" (H)



Style "A", D26Z, D70Z (s/h)

Machine Option "E" (J)

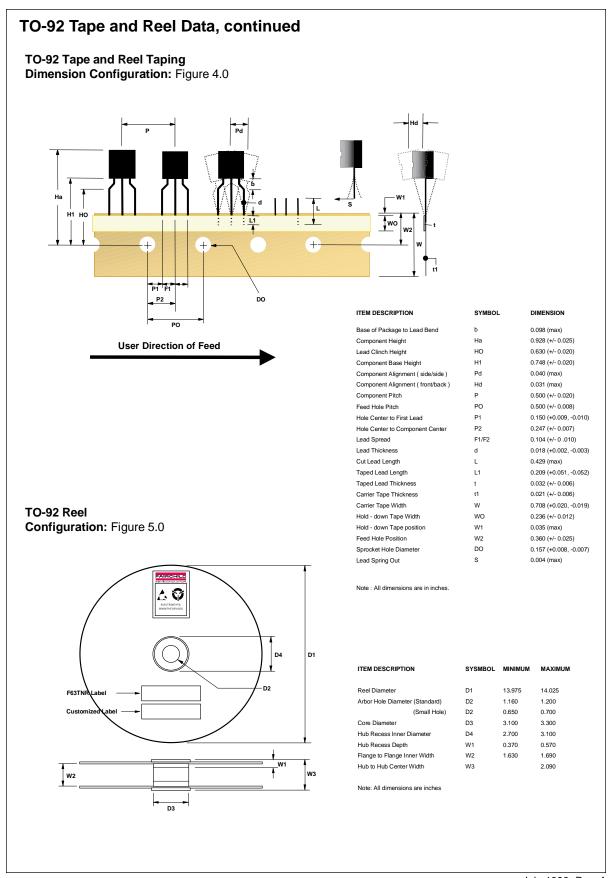
Style "E", D27Z, D71Z (s/h)

TO-92 Radial Ammo Packaging Configuration: Figure 3.0





FIRST WIRE OFF IS COLLECTOR (ON PKG. 92) ADHESIVE TAPE IS ON BOTTOM SIDE FLAT OF TRANSISTOR IS ON TOP



TO-92 Package Dimensions FAIRCHILD SEMICONDUCTOR TM TO-92 (FS PKG Code 92, 94, 96) Scale 1:1 on letter size paper Dimensions shown below are in: inches [millimeters] Part Weight per unit (gram): 0.1977 0.185 4.70 0.170 4.32 TO-92 (92,94,96) 94 96 B F В В В D D 2 В S С G Ε Ø0.060 [Ø1.52] G В S С G 0.010 [0.254] DEEP 5.0°TYP.

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0.095 0.084 2.13

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