



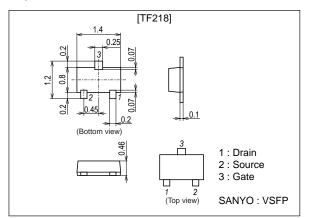
# **Capacitor Microphone Applications**

#### **Features**

- Ultrasmall package facilitates miniaturization in end products.
- Especially suited for use in audio, telephone capacitor microphones.
- · Excellent voltage characteristic.
- · Excellent transient characteristic.
- · Adoption of FBET process.

### **Package Dimensions**

unit : mm 2201



## **Specifications**

## Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Gate-to-Drain Voltage	VGDO		-20	V
Gate Current	IG		10	mA
Drain Current	ID		1	mA
Allowable Power Dissipation	PD		100	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

Parameter S	Symbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	Offic
Gate-to-Drain Breakdown Voltage	V(BR)GDO	IG=-100μA	-20			V
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =5V, I <sub>D</sub> =1μA	-0.2	-0.6	-1.0	V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =5V, V <sub>GS</sub> =0	140*		350*	μΑ
Forward Transfer Admittance	yfs	VDS=5V, VGS=0, f=1kHz	0.5	1.0		mS
Input Capacitance	Ciss	V <sub>DS</sub> =5V, V <sub>GS</sub> =0, f=1MHz		3.5		pF
Reverse Transfer Capacitance	Crss	VDS=5V, VGS=0, f=1MHz		0.65		pF

Continued on next page.

 $<sup>^{\</sup>star}$  : The TF218 is classified by IDSS as follows : (unit :  $\mu\text{A})$ 

Marking	A4	A5			
IDSS	140 to 240	210 to 350			

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Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
i aranietei			min	typ	max	Olili
[Ta=25°C, V <sub>CC</sub> =4.5V, R <sub>L</sub> =1kΩ, Cin=15pF, See specified Test Circuit.]						
Voltage Gain	GV	V <sub>IN</sub> =10mV, f=1kHz		-3.0		dB
Reduced Voltage Characteristic	∆G∨∨	V <sub>IN</sub> =10mV, f=1kHz, V <sub>CC</sub> =4.5→1.5V		-1.2	-3.5	dB
Frequency Characteristic	∆Gvf	f=1kHz to 110Hz			-1.0	dB
Input Impedance	ZIN	f=1kHz	25			ΜΩ
Output Impedance	z <sub>O</sub>	f=1kHz		1000		Ω
Total Harmonic Distortion	THD	V <sub>IN</sub> =30mV, f=1kHz		1.2		%
Output Noise Voltage	VNO	V <sub>IN</sub> =0, A curve			-110	dB

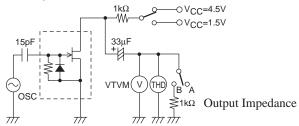
#### **Test Circuit**

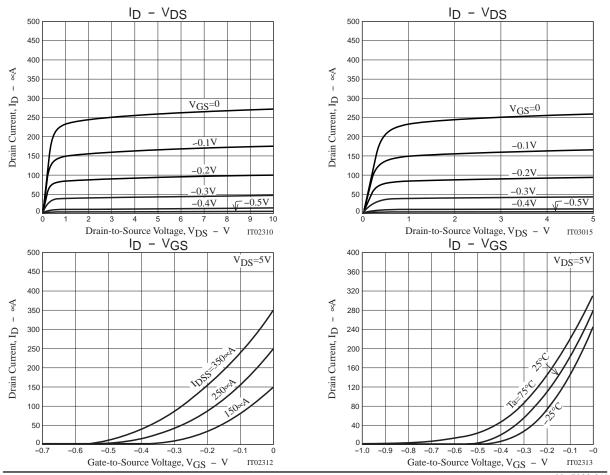
Voltage gain

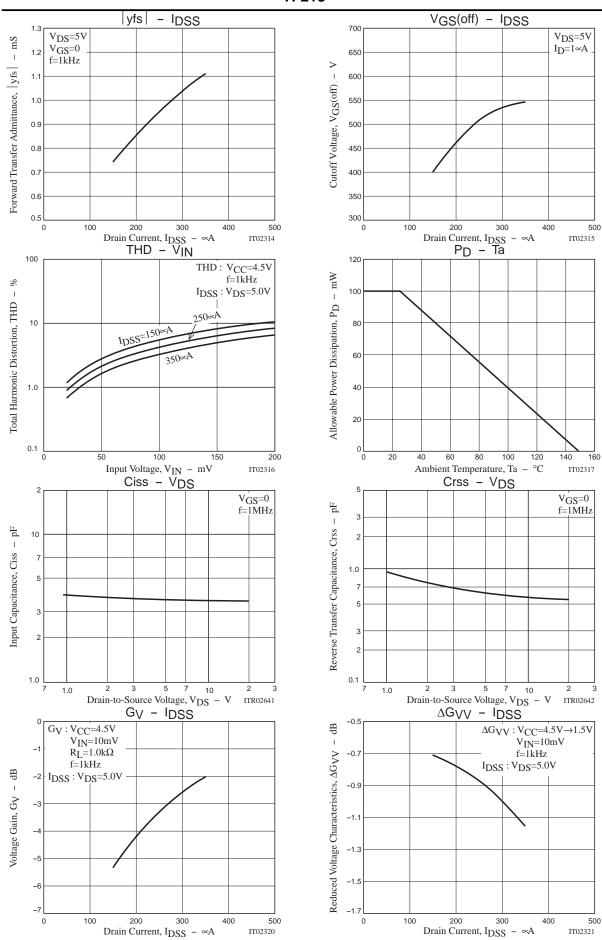
Frequency Characteristic

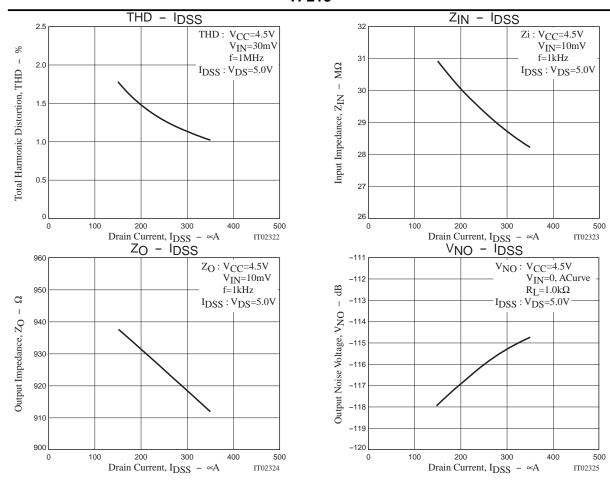
Distortion

Reduced Voltage Characteristic









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