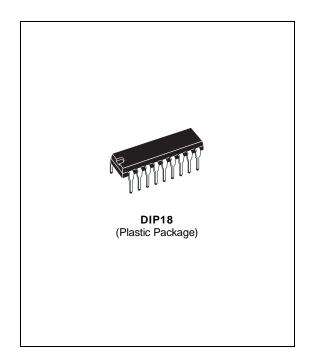
ULQ2801A ULQ2802A - ULQ2803A ULQ2804A - ULQ2805A

EIGHT DARLINGTON ARRAYS

- EIGHT DARLINGTONS PER PACKAGE
- EXTENDED TEMPERATURE RANGE (- 40 to 105°C)
- OUTPUT CURRENT TO 500mA
- OUTPUT VOLTAGE TO 50V
- INTEGRAL SUPPRESSION DIODES
- VERSIONS FOR ALL POPULAR LOGIC FAMI-LIES
- OUTPUT CAN BE PARALLELED
- INPUTS PINNED OPPOSITE OUTPUTS TO SIMPLIFY BOARD LAYOUT



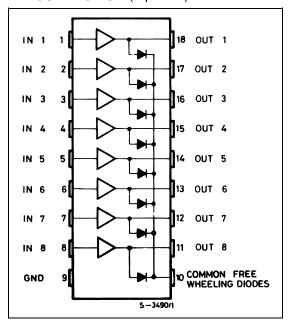
DESCRIPTION

The ULQ2801A-ULQ2805A each contain eight darlington transistors with common emitters and integral suppression diodes for inductive loads. Each darlington features a peak load current rating of 600mA (500mA continuous) and can withstand at least 50V in the off state. Outputs may be paralleled for higher current capability.

Five versions are available to simplify interfacing to standard logic families : the ULQ2801A is designed for general purpose applications with a current limit resistor ; the ULQ2802A has a 10.5k Ω input resistor and zener for 14-25V PMOS ; the ULQ2803A has a 2.7k Ω input resistor for 5V TTL and CMOS ; the ULQ2804A has a 10.5k Ω input resistor for 6-15V CMOS and the ULQ2805A is designed to sink a minimum of 350mA for standard and Schottky TTL where higher output current is required.

All types are supplied in a 18-lead plastic DIP with a copper lead frame and feature the convenient input-opposite-output pinout to simplify board layout.

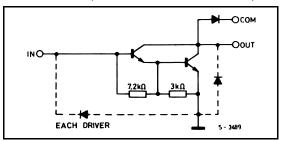
PIN CONNECTION (top view)



September 2003

SCHEMATIC DIAGRAM AND ORDER CODES

For ULQ2801A (each driver for PMOS-CMOS) For ULQ2802A (each driver for 14-15 V PMOS)



10.5kn OOUT

7.2kn 3kn

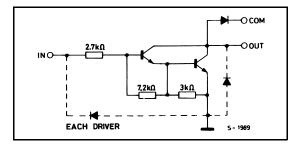
EACH DRIVER

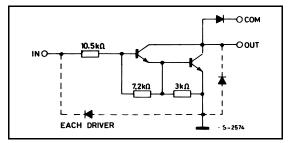
5.1984

→ О СОМ

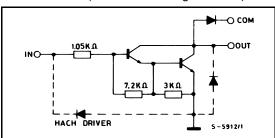
For ULQ2803A (each driver for 5 V, TTL/CMOS)

For ULQ2804A (each driver for 6-15 V CMOS/PMOS





For ULQ2805A (each driver for high out TTL)



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
Vo	Output Voltage	50	V	
Vi	Input Voltage for ULQ2802A, 2803A, 2804A for ULQ2805A	30 15	V	
Ic	Continuous Collector Current	500	mA	
Ι _Β	Continuous Base Current	25	mA	
P _{tot}	Power Dissipation (one Darlington pair) (total package)	1.0 2.25	W	
T _{amb}	Operating Ambient Temperature Range	- 40 to 105	°C	
T _{stg}	Storage Temperature Range	- 55 to 150	°C	

THERMAL DATA

				1
Symbol	Parameter	Value	Unit	
R _{th i-amb}	Thermal Resistance Junction-ambient	Max.	55	°C/W

ELECTRICAL CHARACTERISTICS ($T_j = -40$ to 105° C, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	Fig.
I _{CEX}	Output Leakage Current	V _{CE} = 50V T _J = 105°C, V _{CE} = 50V T _J = 105°C			50 100	μΑ μΑ	1a 1a
		for ULQ2802A V _{CE} = 50V, V _i = 6V for ULQ2804A V _{CE} = 50V, V _i = 1V			500 500	μΑ μΑ	1b 1b
V _{CE} (sat)	Collector-emitter Saturation Voltage	$\begin{array}{l} I_{C} = 100 \text{mA}, \ I_{B} = 250 \mu \text{A} \\ I_{C} = 200 \text{mA}, \ I_{B} = 350 \mu \text{A} \\ I_{C} = 350 \text{mA}, \ I_{B} = 500 \mu \text{A} \\ \end{array}$		0.9 1.1 1.3	1.1 1.3 1.6	V V V	2
I _{i(on)}	Input Current			0.82 0.93 0.35 1 1.5	1.25 1.35 0.5 1.45 2.4	mA mA mA mA	3
$I_{i(off)}$	Input Current	$T_J = 105^{\circ}C$, $I_C = 500\mu A$	50	65		μΑ	4
V _{i(on)}	Input Voltage	$\begin{array}{llllllllllllllllllllllllllllllllllll$			13 2.4 2.7 3 5 6 7 8 2.4	>>>>>>>>	5
h _{FE}	DC Forward Current Gain	for ULQ2802A $V_{CE} = 2V$, $I_{c} = 350 \text{mA}$	1000			-	2
Ci	Input Capacitance			15	25 (*)	pF	_
t _{PLH}	Turn-on Delay Time	0.5 V _i to 0.5 V _o		0.25	1 (*)	μs	_
t _{PHL}	Turn-off Delay Time	0.5 V _i to 0.5 V _o		0.25	1 (*)	μs	_
I _R	Clamp Diode Leakage Current	$V_R = 50V$ $T_J = 105^{\circ}C$, $V_R = 50V$			50 100	μΑ μΑ	6
VF	Clamp Diode Forward Voltage	I _F = 350mA		1.7	2	V	7

^(*) Guaranteed by design



TEST CIRCUITS

Figure 1a.

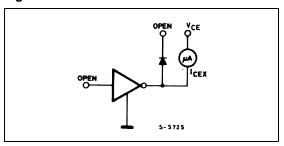


Figure 1b.

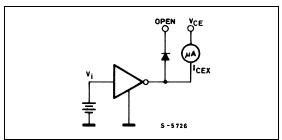


Figure 2.

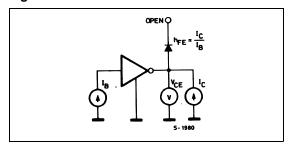


Figure 3.

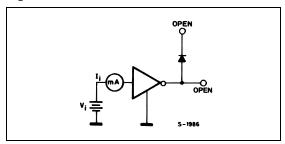


Figure 4.

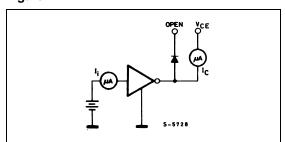


Figure 5.

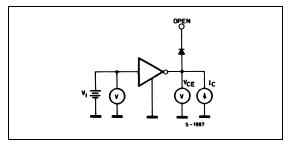


Figure 6.

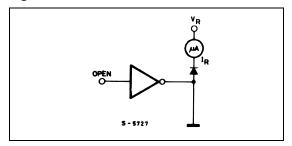


Figure 7.

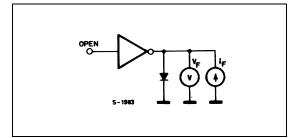


Figure 8 : Collector Current as a Function of Saturation Voltage.

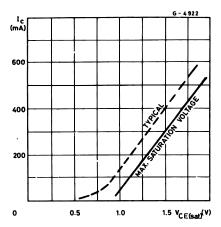


Figure 10 : Allowable Average Power Dissipation as a Function of Ambient Temperature.

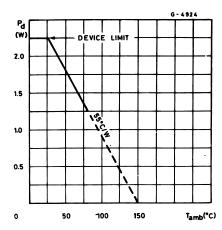


Figure 12 : Peak Collector Current as a Function of Duty.

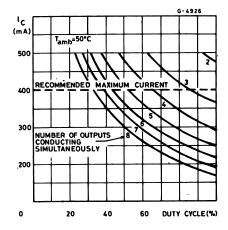


Figure 9 : Collector Current as a Function of Input Current.

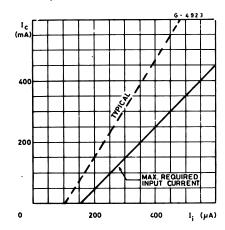


Figure 11 : Peak Collector Current as a Function of Duty Cycle.

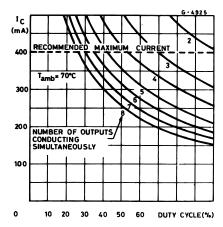


Figure 13 : Input Current as a Function of Input Voltage (for ULQ2802A).

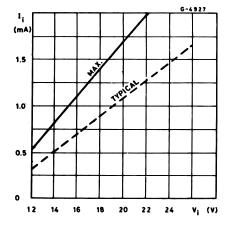


Figure 14 : Input Current as a Function of Input Voltage (for ULQ2804A)

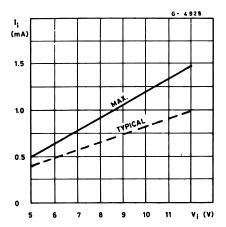


Figure 16: Input Current as a Function of Input Voltage (for ULQ2805A)

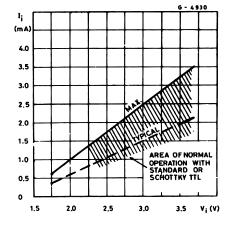
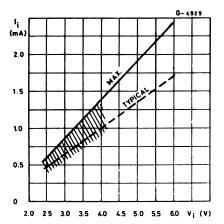
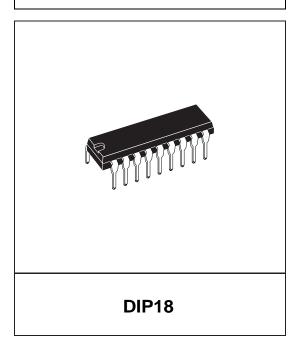


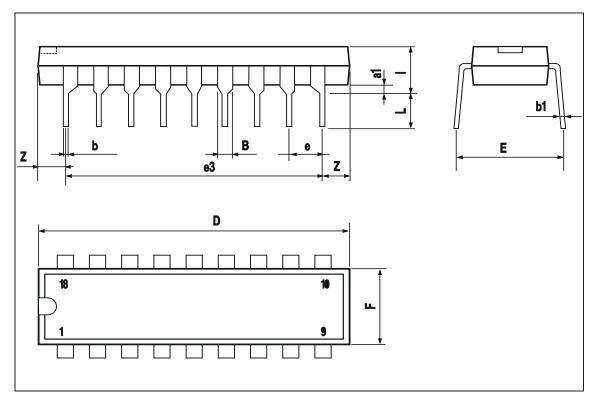
Figure 15: Input Current as a Function of Input Voltage (for ULQ2803A)



DIM.	mm			inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
a1	0.254			0.010			
В	1.39		1.65	0.055		0.065	
b		0.46			0.018		
b1		0.25			0.010		
D			23.24			0.915	
Е		8.5			0.335		
е		2.54			0.100		
e3		20.32			0.800		
F			7.1			0.280	
I			3.93			0.155	
L		3.3			0.130		
Z		1.27	1.59		0.050	0.063	

OUTLINE AND MECHANICAL DATA





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