

10 Amp Dual Low VF Schottky Barrier  
Rectifier PowerDI™ 5

DESCRIPTION

The UPDS1040CTL offers a small and powerful surface mount package for a 40 Volt 10 Amp rated dual Schottky with very low forward voltage and leakage current. For critical applications requiring very fast switching, these Schottky higher reverse voltage ratings with their “hot carrier” features provide extremely fast switching to replace conventional ultrafast rectifiers. The very low thermal resistance of the PowerDI™5 package design permits cooler operating junction temperatures for minimal reverse leakage currents and lower power loss.

APPEARANCE



PowerDI™5

**IMPORTANT:** For the most current data, consult MICROSEMI's website: <http://www.microsemi.com>

FEATURES

- Guard ring die construction for transient protection
- Low power loss, high efficiency
- Low forward voltage drop
- Low reverse leakage current
- High junction temperature capability
- High forward surge current capability
- Environmentally friendly molding compound (no Br, Sb)
- Low inductive parasitics for minimal Ldi/dt effects
- Lead-Free Finish & RoHS Compliant per EU Directive Rev 13.2.2003 (glass and high temperature solder exemptions per Annex Notes 5 and 7 therein)

APPLICATIONS / BENEFITS

- Silicon Schottky (hot carrier) rectifier for minimal  $t_{rr}$  and elimination of reverse-recovery oscillations to reduce need for EMI filtering
- For use in high-frequency switching power supplies, inverters, free wheeling, polarity protection, and “O-Ring” applications
- Low power loss and high efficiency
- Robust package configuration for pick-and-place handling
- Full-metallic bottom eliminates flux entrapment

MAXIMUM RATINGS

- Junction and Storage Temperature ( $T_J$ ,  $T_{STG}$ ): -65 to +150°C
- Average Rectified Output Current ( $I_O$ ): 5 & 10 Amps per element for Single phase, half wave, 60Hz, resistive or inductive load (also see Figure 5). For capacitive load, derate current by 20%.
- Peak Repetitive Reverse Voltage ( $V_{RRM}$ ): 40 V Working  
Peak Reverse Voltage ( $V_{RWM}$ ): 40 V  
DC Blocking Voltage ( $V_R$ ): 40 V
- RMS Reverse Voltage ( $V_{R(RMS)}$ ): 28 V
- Non-Repetitive Peak Forward Surge Current @ 8.3 ms  
Single half sine-wave Superimposed on Rated Load ( $I_{FSM}$ ): 110A
- Thermal Resistance Junction to case bottom ( $R_{\theta JC}$ ) or Soldering Point ( $R_{\theta JS}$ ): 2.0°C/W
- Thermal Resistance ( $R_{\theta JA}$ ): 95°C/W (Note 1), 75°C/W (Note 2), 50°C/W (Note 3) junction to ambient air (see last page)

Notes: 1. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout (pg 4)  
2. Polyimide PCB, 2 oz. Copper, minimum recommended pad layout (pg 4)  
3. Polyimide PCB, 2 oz. Copper with larger Cathode pad dimensions 9.4 mm x 7.2 mm and Anode pad dimensions 2.7 mm x 1.6 mm

MECHANICAL AND PACKAGING

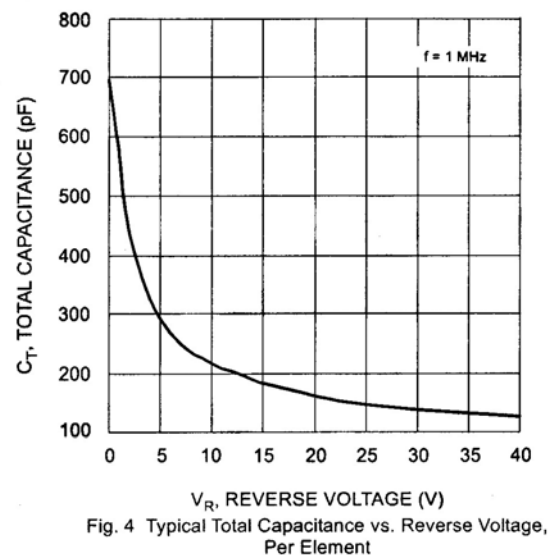
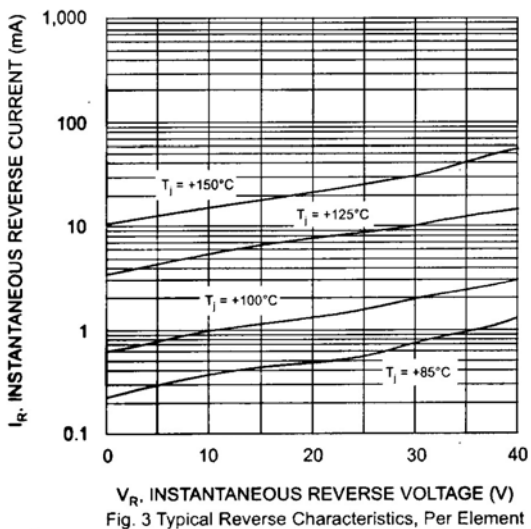
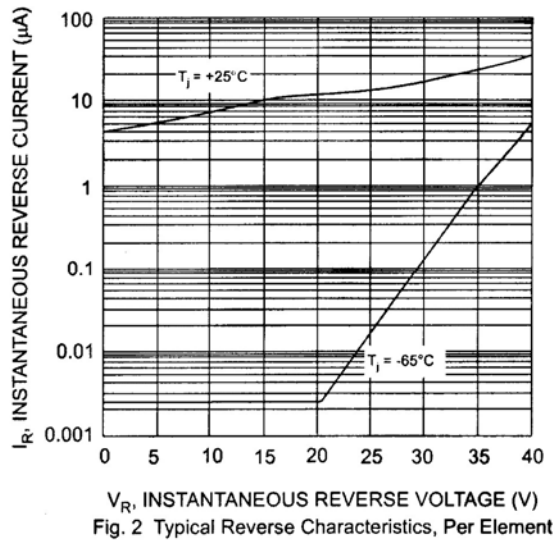
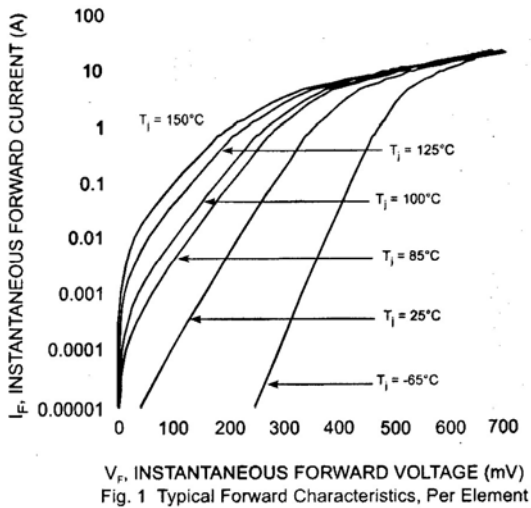
- Case Material: Molded Plastic, Environmentally Friendly “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C.
- Terminals: Finish – Matte Tin annealed over Copper lead frame (  $\text{€3}$  per JESD97) Solderable per MIL-STD-202, Method 208
- Marking: See marking information on page 3
- Polarity: See diagram in “Dimensions & Schematic” (page 4)
- Weight: 0.096 grams (approx.)
- Tape & Reel Option: 5000/reel (13”)

**ELECTRICAL CHARACTERISTICS @ 25°C** unless specified otherwise

Part Number	Working Peak Reverse Voltage	Maximum RMS Voltage	Minimum Reverse Breakdown Voltage (Note 1)	Maximum Forward Voltage (Note 3)		Maximum Reverse Current (Note 1 & 2)			Maximum Reverse Current $I_R$ @ 100°C (Note 1 & 2)		
	$V_{RWM}$	$V_{R(RMS)}$	$V_{BR}$ @ 500 $\mu A$	$V_F$ @ 5A	$V_F$ @ 10A	$I_R$ @ 17.5 V	$I_R$ @ 35 V	$I_R$ @ 40 V	$I_R$ @ 17.5 V	$I_R$ @ 35 V	$I_R$ @ 40 V
	Volts	Volts	Volts	Volts	Volts	$\mu A$	$\mu A$	$\mu A$	mA	mA	mA
UPDS1040CTL	40	28	40	0.50	0.60	80	150	200	5	10	25

NOTE 1: Short duration test pulse used to minimize self-heating effect.  
NOTE 2: See Figures 2 & 3 for typical values at other voltages and temperatures.  
NOTE 3: See Figure 1 for typical values at various temperatures

**GRAPHS**



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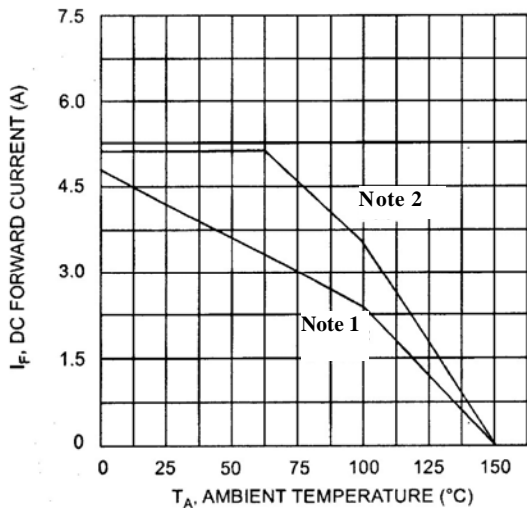


Fig. 5 DC Forward Current Derating, Per Element

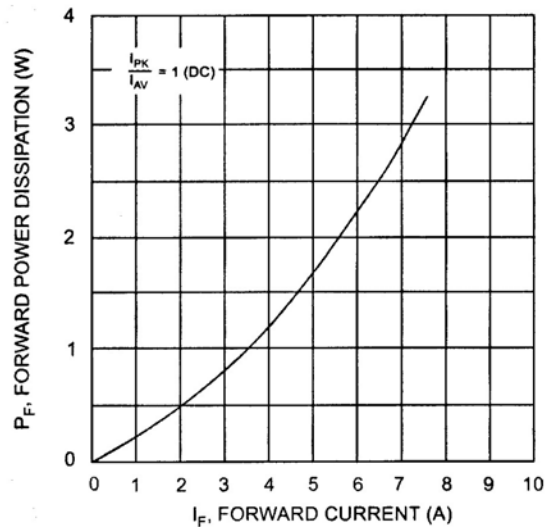


Fig. 6 Forward Power Dissipation, Per Element

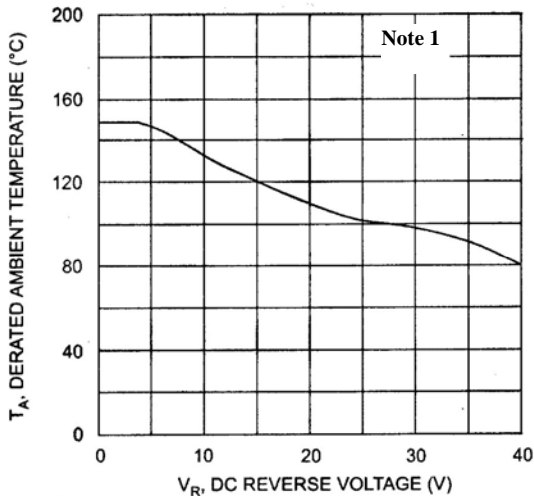
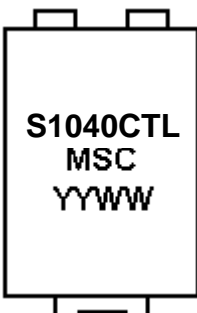


Fig. 7 Operating Temperature Derating, Per Element

NOTE 1: FR-4 PCB, 2 oz. Copper, minimum recommended pad layout (see mounting pad illustration)

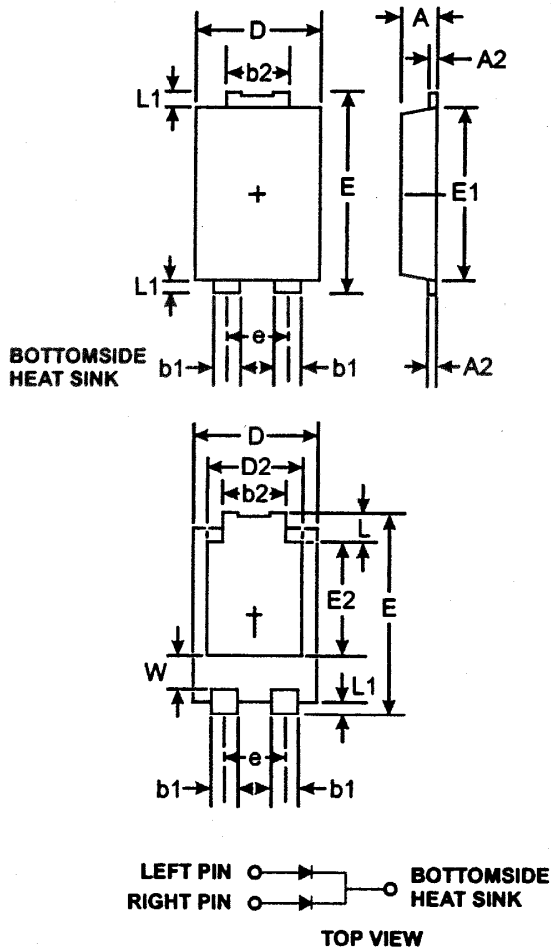
NOTE 2: Polyimide PCB, 2 oz. Copper. Cathode pad dimensions 18.8 mm x 14.4 mm. Anode pad dimensions 5.6 mm x 3.0 mm.

MARKING INFORMATION



S1040L = Product type marking code.  
MSC = Manufacturers' code marking  
YYWW = Date code marking  
YY = Last digit of year ex: 04 for 2004  
WW = Week code 01 to 52

**DIMENSIONS AND SCHEMATIC**



PowerDI™5		
Dim	Min	Max
A	1.05	1.15
A2	0.33	0.43
b1	0.80	0.99
b2	1.70	1.88
D	3.90	4.05
D2	3.05 NOM	
E	6.40	6.60
e	1.84 NOM	
E1	5.30	5.45
E2	3.55 NOM	
L	0.75	0.95
L1	0.50	0.65
W	1.20	1.50
All Dimensions in mm		

**MOUNTING PAD DIMENSIONS**

PAD dimensions (mm)	
Z	6.6
X1	1.4
X2	3.6
Y1	0.8
Y2	4.7
C	3.87
E1	0.9

