

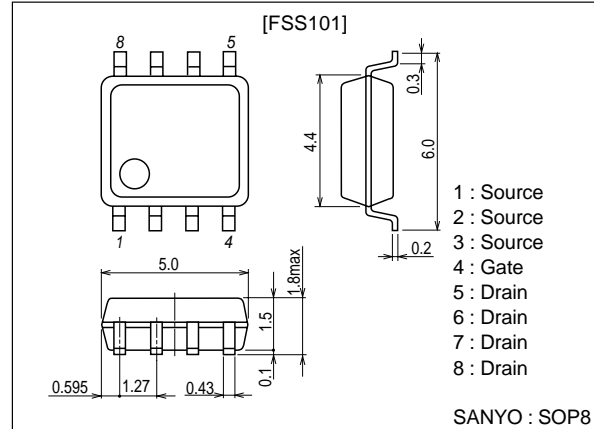
**FSS101****Load S/W Applications****Features**

- Low ON resistance.
- Ultrahigh-speed switching.
- 2.5V drive.

**Package Dimensions**

unit:mm

2116

**Specifications****Absolute Maximum Ratings** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DS}$		-20	V
Gate-to-Source Voltage	$V_{GS}$		$\pm 10$	V
Drain Current (DC)	$I_D$		-5	A
Drain Current (pulse)	$I_{DP}$	$PW \leq 10\mu s$ , duty cycle $\leq 1\%$	-32	A
Allowable Power Dissipation	$P_D$	Mounted on a ceramic board (1200mm <sup>2</sup> ×0.8mm)	1.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

**Electrical Characteristics** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA$ , $V_{GS} = 0$	-20			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = -20V$ , $V_{GS} = 0$			-10	$\mu A$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 8V$ , $V_{DS} = 0$			$\pm 10$	$\mu A$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V$ , $I_D = -1mA$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V$ , $I_D = -5A$	10		16	S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D = -5A$ , $V_{GS} = -4V$		44	58	m $\Omega$
	$R_{DS(on)2}$	$I_D = -2A$ , $V_{GS} = -2.5V$		65	98	m $\Omega$
Input Capacitance	Ciss	$V_{DS} = -10V$ , $f = 1MHz$		980		pF
Output Capacitance	Coss	$V_{DS} = -10V$ , $f = 1MHz$		500		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = -10V$ , $f = 1MHz$		210		pF

Marking : S101

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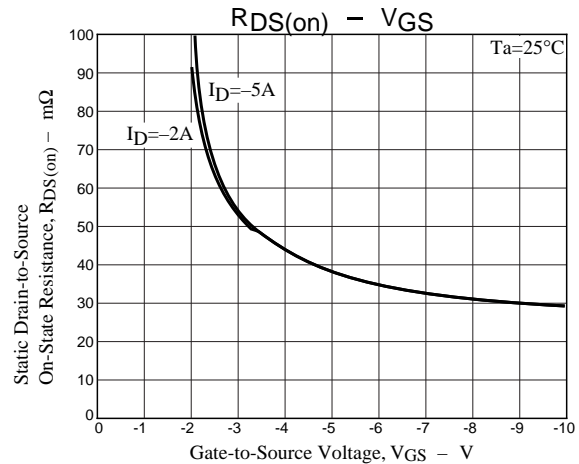
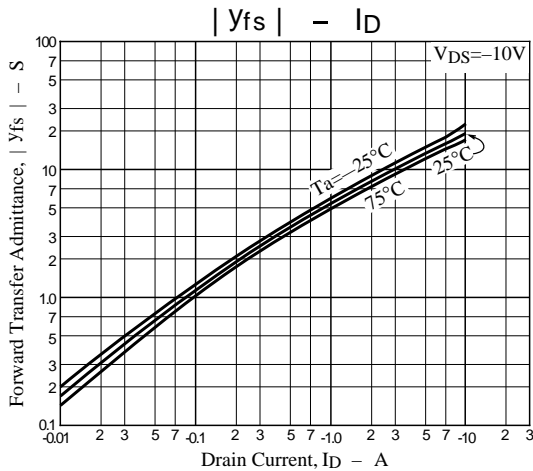
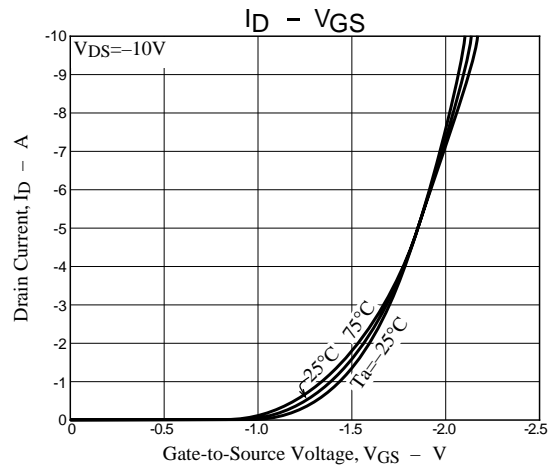
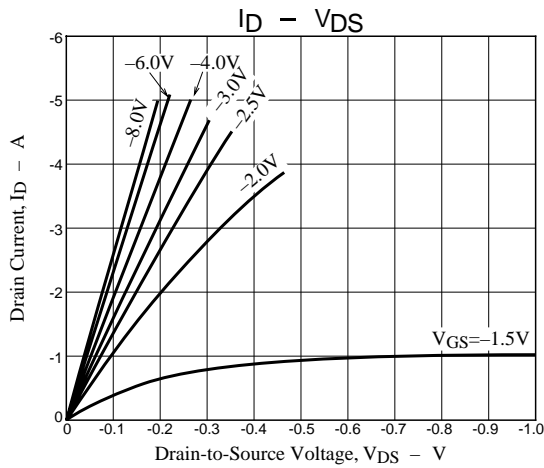
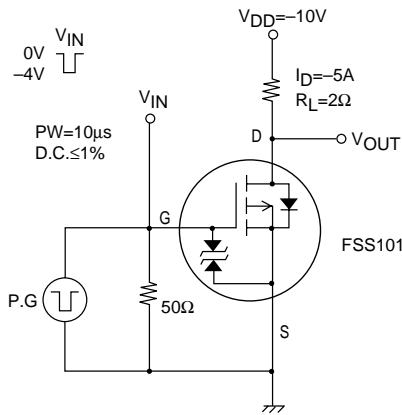
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# FSS101

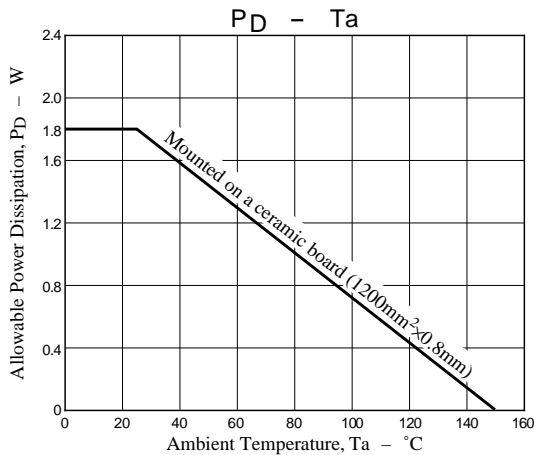
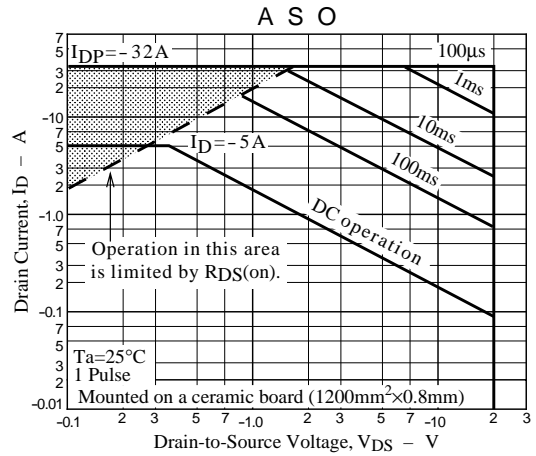
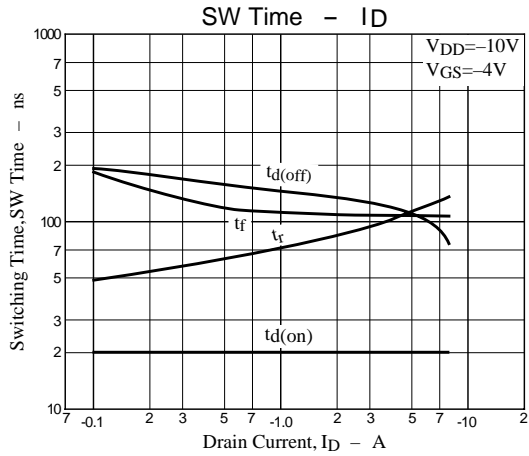
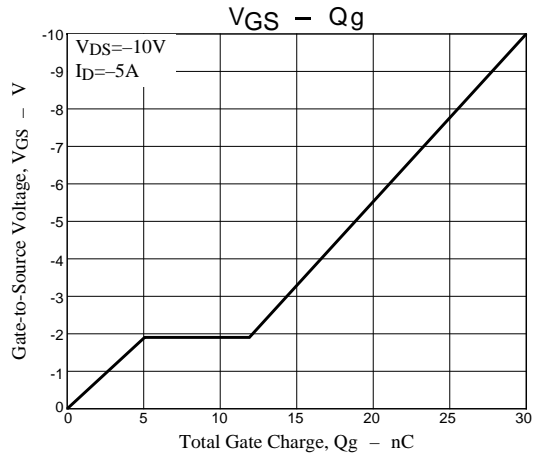
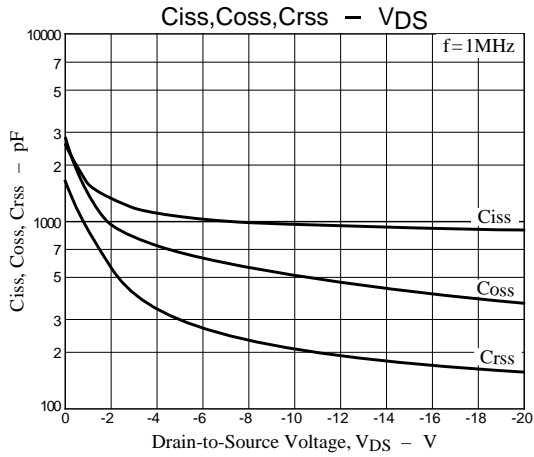
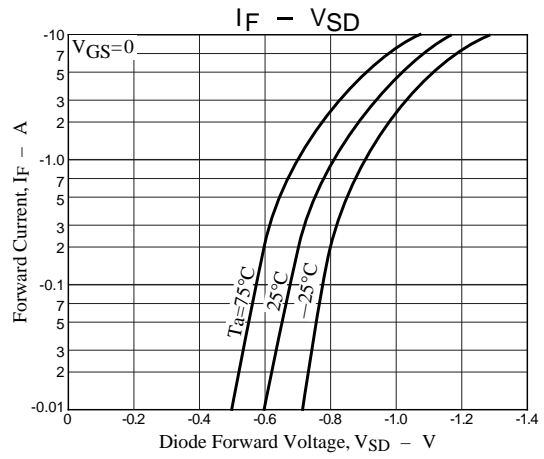
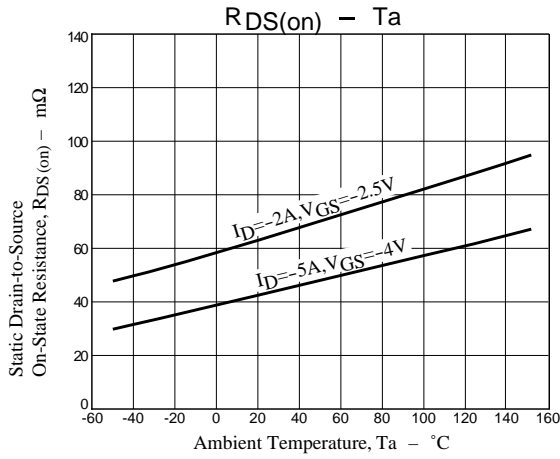
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit		20		ns
Rise Time	$t_r$	See specified Test Circuit		115		ns
Turn-OFF Delay Time	$t_{d(off)}$	See specified Test Circuit		110		ns
Fall Time	$t_f$	See specified Test Circuit		105		ns
Total Gate Charge	Qg	$V_{DS}=-10V, V_{GS}=-10V, I_D=-5A$		30		nC
Gate-to-Source Charge	Qgs	$V_{DS}=-10V, V_{GS}=-10V, I_D=-5A$		5		nC
Gate-to-Drain "Miller" Charge	Qgd	$V_{DS}=-10V, V_{GS}=-10V, I_D=-5A$		7		nC
Diode Forward Voltage	$V_{SD}$	$I_S=-5A, V_{GS}=0$	-1.0	-1.5		V

## Switching Time Test Circuit



# FSS101



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