

SANYO Semiconductors DATA SHEET

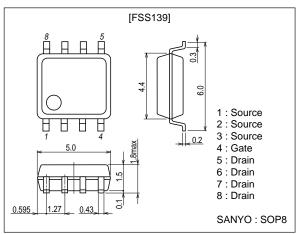
FSS139 — P-Channel Silicon MOSFET Load Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2116



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		-20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ΙD		-4	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	-48	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (1200mm ² X0.8mm)	1.8	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tsta		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Oill
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0	-20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =-20V, V _{GS} =0			-1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±8V, V _{DS} =0			±10	μΑ
Cutoff Voltage	VGS(off)	V _{DS} =-10V, I _D =-1mA	-0.4		-1.4	V
Forward Transfer Admittance	yfs	VDS=-10V, ID=-4A	6.3	9		S

Marking: \$139 Continued on next page.

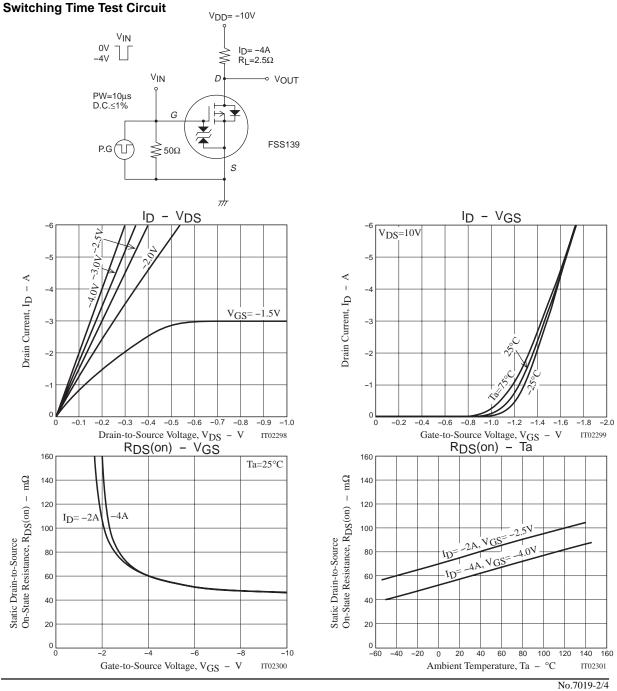
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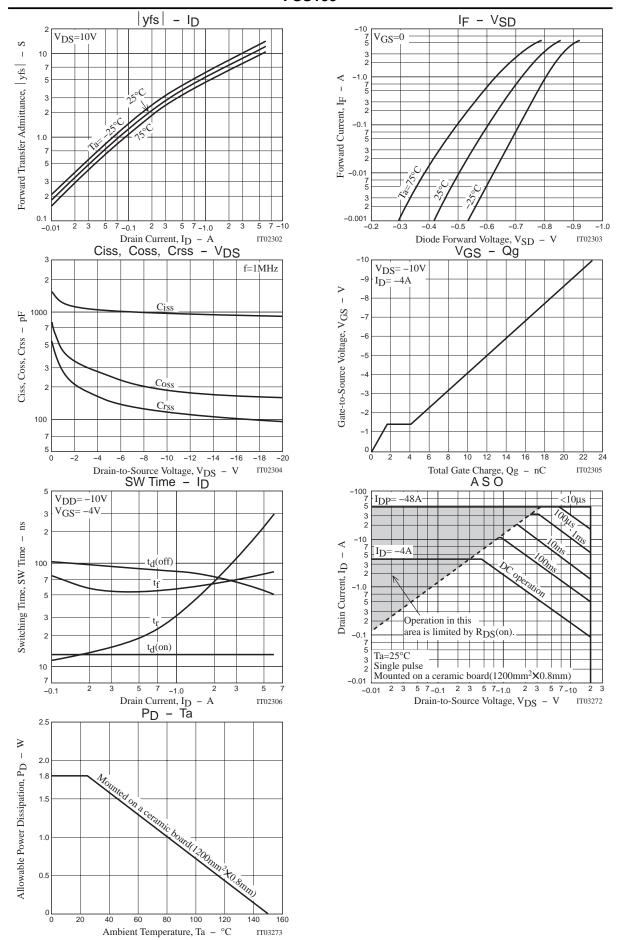
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Parameter	Symbol	Conditions	Ratings			I lais
			min	typ	max	Unit
Static Drain-to-Source On-State Resistance	RDS(on) 1	ID=-4A, VGS=-4V		60	78	$m\Omega$
	R _{DS} (on) 2	I _D =-2A, V _G S=-2.5V		78	110	mΩ
Input Capacitance	Ciss	V _{DS} =-10V, f=1MHz		1000		pF
Output Capacitance	Coss	V _{DS} =-10V, f=1MHz		190		pF
Reverse Transfer Capacitance	Crss	V _{DS} =-10V, f=1MHz		120		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		13		ns
Rise Time	tr	See specified Test Circuit		200		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		52		ns
Fall Time	tf	See specified Test Circuit		78		ns
Total Gate Charge	Qg	V _{DS} =-10V, V _{GS} =-10V, I _D =-4A		23		nC
Gate-to-Source Charge	Qgs	V _{DS} =-10V, V _{GS} =-10V, I _D =-4A		1.6		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =-10V, V _{GS} =-10V, I _D =-4A		2.3		nC
Diode Forward Voltage	VSD	IS=-4A, VGS=0		-0.89	-1.5	V





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