

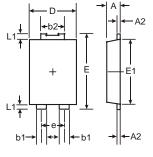
5A SCHOTTKY BARRIER RECTIFIER PowerDI®5

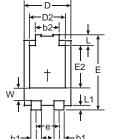
Features

- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Leakage Current
- Low Power Loss, High Efficiency
- For Use in High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- High Forward Surge Current Capability
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDl[®]5
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Polarity: See Diagram
- Marking Information: See Page 3
- Weight: 0.094 grams (approximate)





PowerDI[®]5 Min Dim Max 1.05 Α 1.15 0.33 0.43 A2 b1 0.80 0.99 b2 1.70 1.88 D 3.90 4.05 D2 3.05 NOM Е 6.40 6.60 1.84 NOM е 5.30 E1 5.45 E2 3.55 NOM L 0.75 0.95 L1 0.50 0.65 w 1.20 1.50 All Dimensions in mm

LEFT PIN O

Note: Pins Left & Right must be electrically connected at the printed circuit board.

Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
RMS Reverse Voltage	V _{R(RMS)}	42	V
Average Rectified Output Current (See figure 4)	lo	5	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load	I _{FSM}	150	А

Thermal Characteristics

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance Junction to Soldering Point	$R_{\theta JS}$	_	2.0	°C/W
Thermal Resistance Junction to Ambient Air (Note 2) $T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	95	—	°C/W
Thermal Resistance Junction to Ambient Air (Note 3) $T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	70		°C/W
Thermal Resistance Junction to Ambient Air (Note 4) $T_A = 25^{\circ}C$	$R_{\theta JA}$	50	_	°C/W
Operating Temperature Range	Tj	-65 to	o +150	°C
Storage Temperature Range	T _{STG}	-65 to	o +150	°C

Notes: 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see EU Directive Annex Note 7.

2. FR-4 PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

3. Polymide PCB, 2 oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.

4. Polymide PCB, 2 oz. Copper. Cathode pad dimensions 9.4mm x 7.2mm. Anode pad dimensions 2.7mm x 1.6mm.

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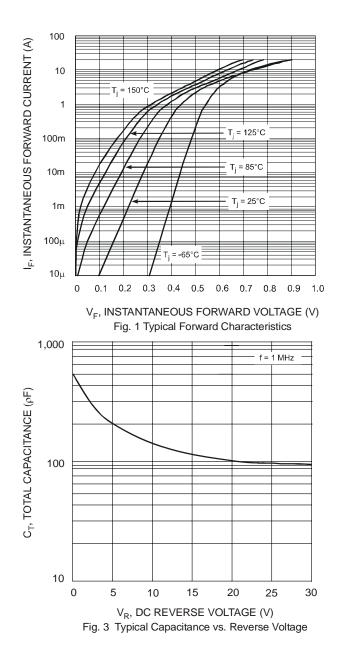


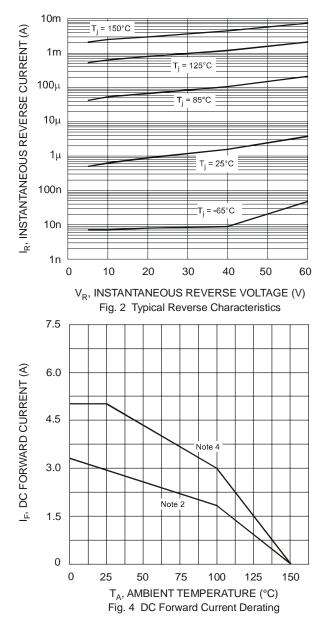
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	60	_		V	$I_R = 0.2 mA$
Forward Voltage	VF	_	0.61	0.67	V	$I_F = 5A, T_S = 25^{\circ}C$
			0.54	0.60		I _F = 5A, T _S = 125°C
			0.71	0.77		$I_F = 8A, T_S = 25^{\circ}C$
		_	—	0.68		I _F = 8A, T _S = 125°C
Reverse Leakage Current (Note 5)	I _R	_	4	150	μA	$T_{S} = 25^{\circ}C, V_{R} = 60V$
			—	15	mA	$T_{S} = 100^{\circ}C, V_{R} = 60V$
		_	2	30	mA	$T_{S} = 125^{\circ}C, V_{R} = 60V$

Notes:

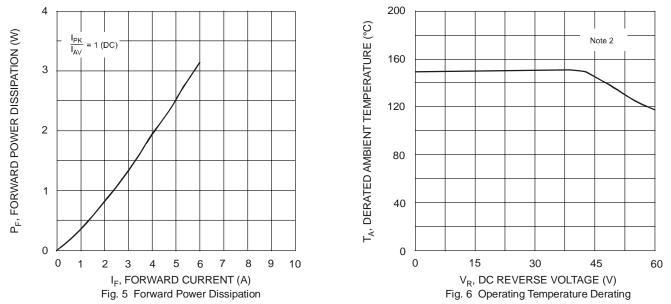
5. Short duration pulse test used to minimize self-heating effect.





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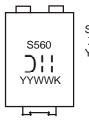


Ordering Information (Note 6)

Device	Packaging	Shipping
PDS560-13	PowerDI [®] 5	5000/Tape & Reel

Note: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



 $\begin{array}{l} \mathsf{S560} = \mathsf{Product} \ \mathsf{type} \ \mathsf{marking} \ \mathsf{code} \\ \mathfrak{II} = \mathsf{Manufacturers'} \ \mathsf{code} \ \mathsf{marking} \\ \mathsf{YYWW} = \mathsf{Date} \ \mathsf{code} \ \mathsf{marking} \\ \mathsf{YYWW} = \mathsf{Date} \ \mathsf{code} \ \mathsf{marking} \\ \mathsf{YY} = \mathsf{Last} \ \mathsf{two} \ \mathsf{digits} \ \mathsf{of} \ \mathsf{year} \ \mathsf{ex:} \ \mathsf{05} \ \mathsf{for} \ \mathsf{2005} \\ \mathsf{WW} = \mathsf{Week} \ \mathsf{code} \ \mathsf{01} \ \mathsf{to} \ \mathsf{52} \\ \mathsf{K} = \mathsf{Factory} \ \mathsf{Designator} \end{array}$

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