## K1526C & K1536C

### 9x11 mm, 5.0 or 3.3 Volt, CMOS/TTL, VCXO



Χ X 00.0000

MHz

Ordering Information

**Product Series** K1526C = 5.0 Volt K1536C = 3.3 Volt

Model Selection: See Electrical Specs

**Temperature Range** Blank: 0°C to +70°C

-40°C to +85°C Symmetry/Logic Compatibility Blank: TTL/CMOS 40%/60% CMOS 45%/55% TTL 45%/55%

Frequency (customer specified)

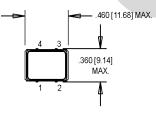
K15X6CX

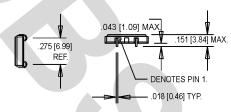


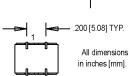




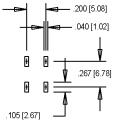
- Former Champion Product
- Phase-Locked Loops (PLL's), Clock Recovery, Reference Signal Tracking, Synthesizers, Frequency Modulation/Demodulation







SUGGESTED SOLDER PAD LAYOUT



### **Pin Connections**

PIN	FUNCTION			
1	Voltage Control			
2	Ground & Gnd Plane			
3	Output			
4	+Vdd			

Т	PARAMETER	Cumpleal					Units		
ŀ	Model	Symbol	1/15	26CA	K1526CD		Units		
1	Model			526CA 536CA	K1526CD K1536CD	K1526CE			
ŀ	F	F					NAU-		
ŀ	Frequency Range	F	2 to 55	55.1 to 80	2 to 55	2 to 40	MHz		
ı	Frequency Stability	ΔF/F	I						
ı	Overall			Calibration, Ter	*				
ı	0°C to +70°C		±25	±40	±25	±32	ppm		
L	-40°C to +85°C		±50	±60	±50	±50	ppm		
- [	Pullability								
ı	Minimum		±100	±80	±80	±200	ppm		
L	Maximum		±150	±160	±130		ppm		
	PARAMETER	Symbol	Min.	Тур.	Max.	Units	Condition/Notes		
	Operating Temperature	T <sub>A</sub>	(See ordering	ng information)					
, Г	Storage Temperature	Ts	-40		+125	°C			
	Aging								
ı	์ 1 <sup>ฐ</sup> ์ Year		-3/-5		+3/+5	ppm	<52 MHz / ≥52 MHz		
H	Thereafter (per year)		-1/-2		+1/+2	ppm	<52 MHz / ≥52 MHz		
ı	Control Voltage	Vc	0.5	2.5	4.5	V	K1526C		
1	<b>3</b> -		0.3	1.65	3.0	V	K1536C		
ı			0		5.0	V	K1526CE		
ı	Linearity				10	%	Positive Monotonic Slope		
ľ	Modulation Bandwidth	fm	20		_	kHz	+3 dB		
	Input Impedance	Zin	50K			Ohms	@ 10 kHz		
t	Input Voltage	Vdd	4.5	5.0	5.5	V	K1526C		
1			3.0	3.3	3.6	V	K1536C		
ı	Input Current	ldd	1		30	mA			
ı	Output Type		1				CMOS/TTL		
t	Load		1	1	15	pF	HCMOS		
t	Symmetry (Duty Cycle)		(See ordering information)						
ı	Logic "1" Level	Voh	Vdd -0.5	Ĭ		V			
ı	Logic "2" Level	Vol			0.5	V			
	Output Current				20	mA			
	Rise/Fall Time	Tr/Tf	1		5	ns	20% to 80% Vdd, CL = 15 p		
t	Start up Time		1		10	ms			
t	Phase Jitter @ 26 MHz	ΦЈ		4		ps RMS	Integrated 12 kHz - 20 MHz		
t	Phase Noise (Typical)	10 Hz	100 Hz	1 kHz	10 kHz	100 kHz	Offset from carrier		
ı	@ 26 MHz	-65	-95	-115	-130	-140	dBc/Hz		
1	Mechanical Shock	Per MIL-STD-202, Method 213, Condition C (100 g/s, 6 mS duration, ½ sinewave)							
-	Vibration	Per MIL-STD-202, Method 201 & 204 (10 g's from 10-2000 Hz)							
t	Hermeticity	Per MIL-STD-202, Method 112, (1x10-8 atm. cc/s of Helium)							
ŀ	Thermal Cycle	Per MIL-STD-883, Method 1010, Condition B (-55°C to +125°C, 15 min. dwell, 10 cycles)							
	Solderability	Per EIAJ-STD-002							

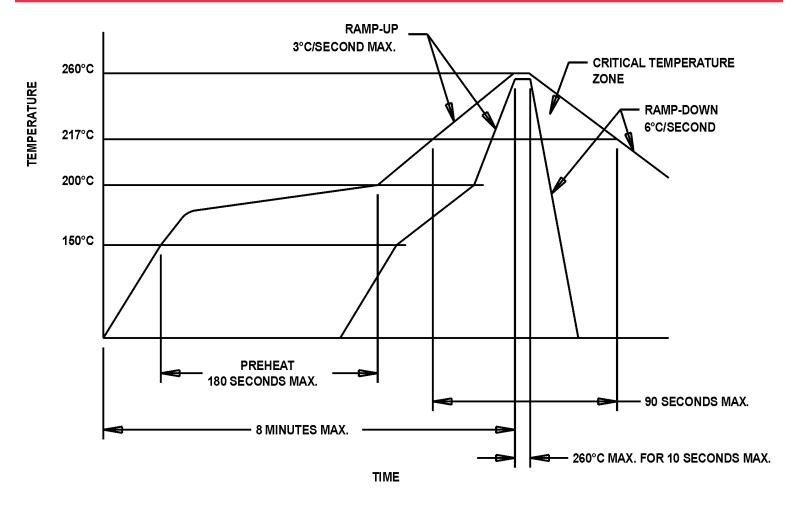
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Soldering Conditions +240°C max. for 10 secs.

Please see www.mtronpti.com for our complete offering and detailed datasheets. Contact us for your application specific requirements: MtronPTI 1-800-762-8800.



# MtronPTI Lead Free Solder Profile



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