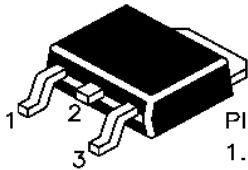


NPN SILICON POWER TRANSISTOR

CJD81

DPAK (TO-252)
Plastic Package



PIN CONFIGURATION

1. BASE
2. COLLECTOR
3. EMITTER

For High Current Driver Applications

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector Base Voltage	V _{CBO}	30	V
Collector Emitter Voltage	V _{CEO}	10	V
Emitter Base Voltage	V _{EBO}	6.0	V
Collector Current	I _C	3.0	A
Collector Current (Pulse)	I _{CP}	5.0	A
Collector Power Dissipation	P _D	0.9	W
Junction Temperature	T _j	150	°C
Storage Temperature	T _{stg}	- 55 to +150	°C

ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

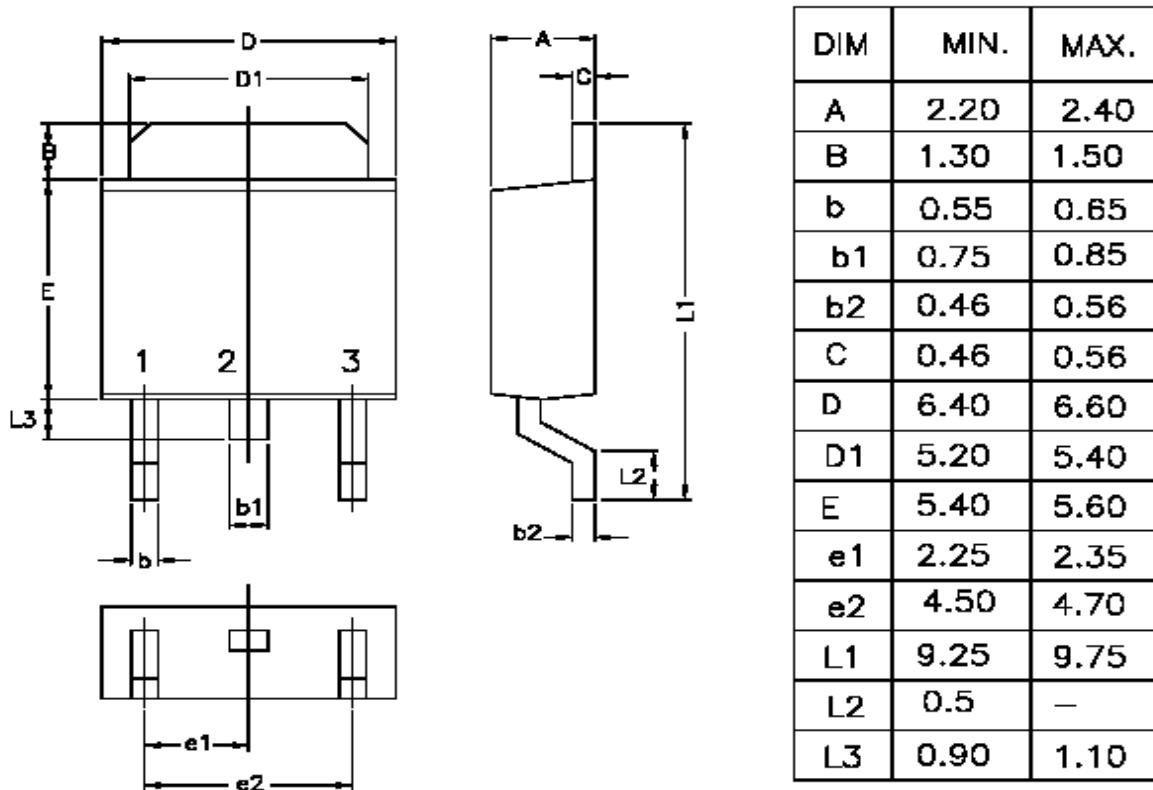
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage	V _{CEO}	I _C =1mA, I _B =0	10			V
Collector Base Voltage	V _{CBO}	I _C =10μA, I _E =0	30			V
Emitter Base Voltage	V _{EBO}	I _E =10μA, I _C =0	6.0			V
Collector Cut Off Current	I _{CBO}	V _{CB} =20V, I _E =0			0.1	μA
Emitter Cut Off Current	I _{EBO}	V _{EB} =4V, I _C = 0			0.1	μA
DC Current Gain	h _{FE}	I _C =3A, V _{CE} =2V	140			

DYNAMIC CHARACTERISTICS

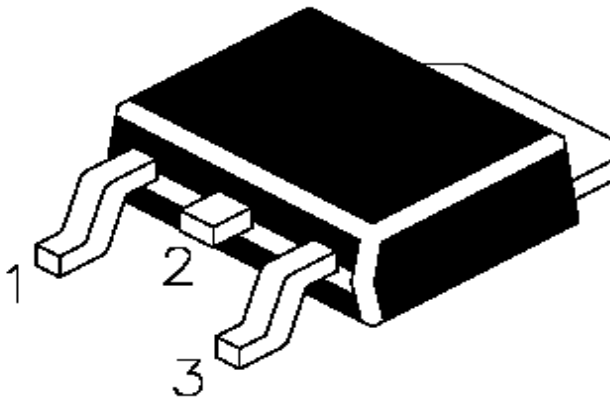
Transition Frequency	f _T	V _{CE} =10V, I _C =50mA		200		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, I _E =0, f=1MHz	30			pF

MARKING	CDIL
	CJD81
	MX XY
XY= Date Code	

CJD81Rev230205E

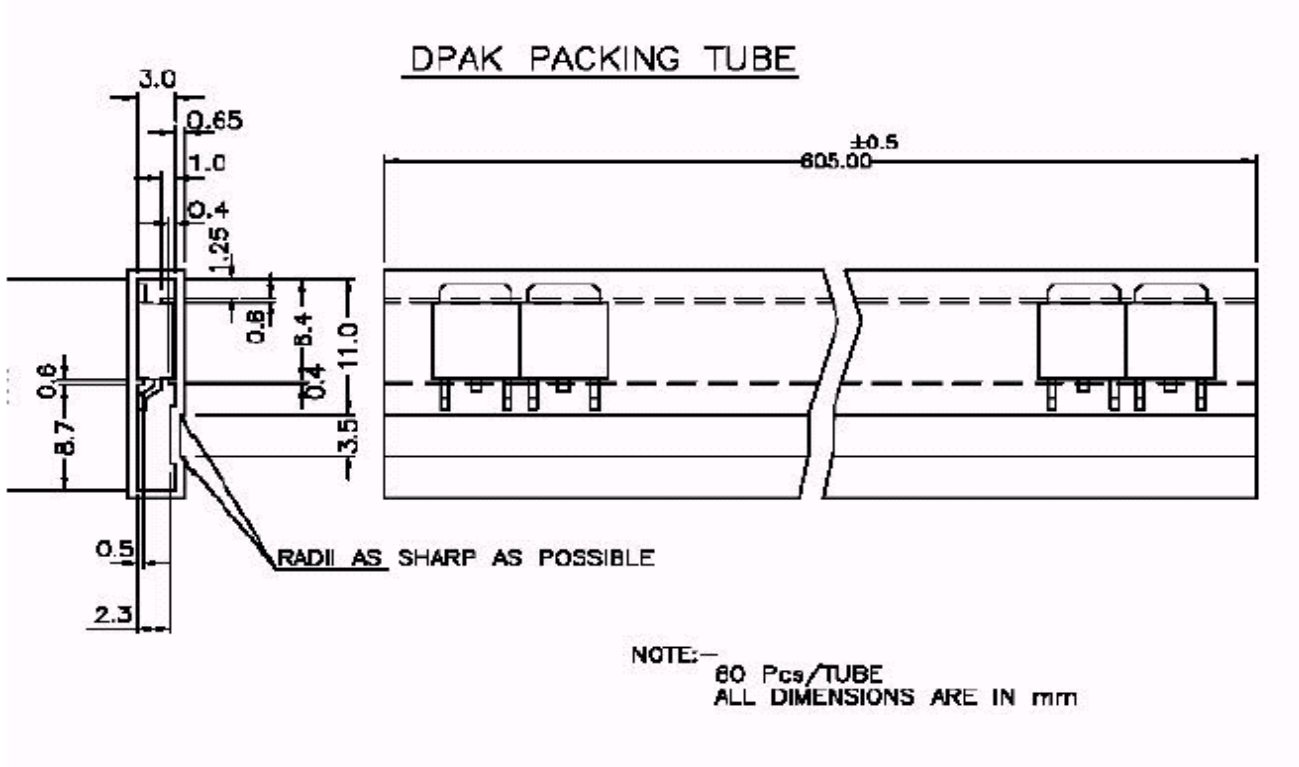
DPAK PACKAGE OUTLINE DIMENSIONS

ALL DIMENSIONS ARE IN mm

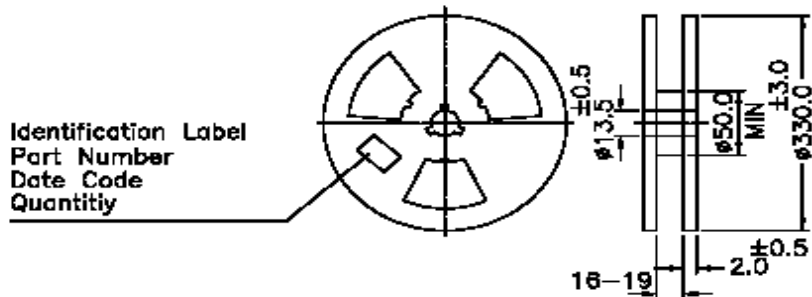


PIN CONFIGURATION

1. BASE
2. COLLECTOR
3. EMITTER

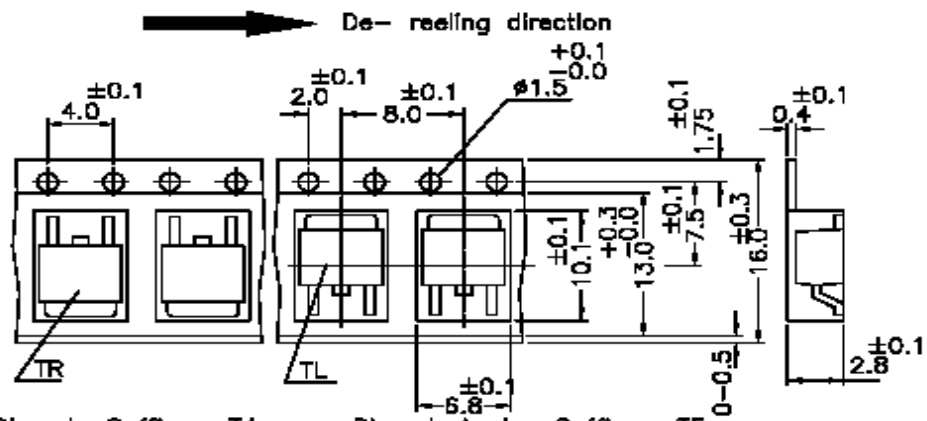


DPAK TAPE & REEL SPECIFICATION



ALL DIMENSIONS ARE IN mm
REEL ϕ 330 mm (13")
No of Device 2500

TAPE & REEL

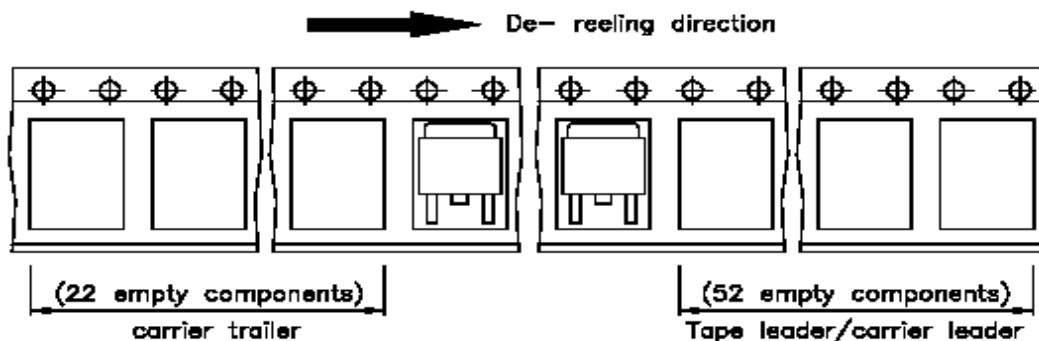


Discrete Suffix - T4
Analog Suffix - RK

Discrete, Analog Suffix - T5

Notes:-

A maximum of three consecutive components may be missing. Provided this gap is followed by six consecutive components.



Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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