# General purpose (Dual digital transistors)

## EMD3 / UMD3N / IMD3A

#### Features

- 1) Both the DTA114E chip and DTC114E chip in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

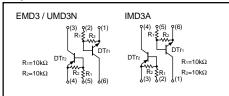
#### ●Structure

Epitaxial planar type

NPN / PNP silicon transistor (Built-in resistor type)

The following characteristics apply to both the DTr1 and DTr2, however, the "-" sign on DTr2 values for the PNP type have been omitted.

#### Equivalent circuits

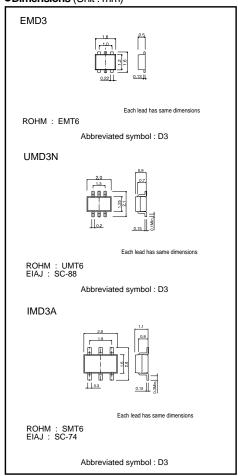


#### ● Absolute maximum ratings (Ta=25°C)

		- 1			
Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		VIN	-10	V	
		VIN	40		
Output current		lo	50	mA	
		Ic (Max.)	Ic (Max.) 100		
Power dissipation	EMD3, UMD3N	Pd	150 (TOTAL)	mW *1	
	IMD3A	Pu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

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### ●Dimensions (Unit: mm)



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<sup>\*1 120</sup>mW per element must not be exceeded. \*2 200mW per element must not be exceeded.

#### ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innut voltogo	V <sub>I (off)</sub>	-	-	0.5	V	Vcc=5V, Io=100μA	
Input voltage	VI (on)	3	-	-	V	Vo=0.3V, Io=10mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo=10mA, lı=0.5mA	
Input current	lı	-	-	0.88	mA	V <sub>I</sub> =5V	
Output current	lo (off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	30	-	-	-	Vo=5V, Io=5mA	
Transition frequency	f⊤	-	250	-	MHz	Vc=10V, I=-5mA, f=100MHz *	
Input resistance	R <sub>1</sub>	7	10	13	kΩ	-	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	-	

<sup>\*</sup> Transition frequency of the device

### Packaging specifications

•	Package	Taping				
	Code	T2R	TR	T110		
Туре	Basic ordering unit (pieces)	8000	3000	3000		
EMD3		0	_	_		
UMD3N		_	0	_		
IMD3A		_	_	0		

#### •Electrical characteristic curves

#### DTr<sub>1</sub> (NPN)

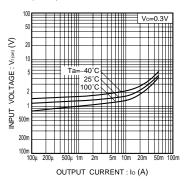


Fig.1 Input voltage vs. output current (ON characteristics)

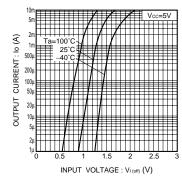


Fig.2 Output current vs. input voltage (OFF characteristics)

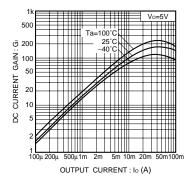


Fig.3 DC current gain vs. output

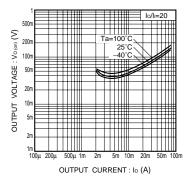


Fig.4 Output voltage vs. output current

#### DTr<sub>2</sub> (PNP)

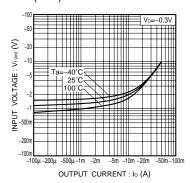


Fig.5 Input voltage vs. output current (ON characteristics)

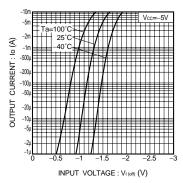


Fig.6 Output current vs. input voltage (OFF characteristics)

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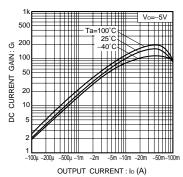


Fig.7 DC current gain vs. output

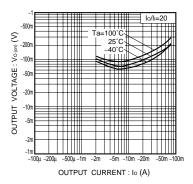


Fig.8 Output voltage vs. output current

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Appendix1-Rev2.0