



N-Channel 100-V (D-S) 175°C MOSFET

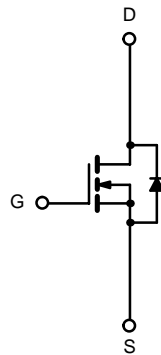
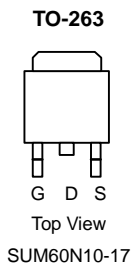
PRODUCT SUMMARY		
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ (Ω)	I_D (A)
100	0.0165 @ $V_{GS} = 10$ V	60
	0.019 @ $V_{GS} = 6$ V	56

FEATURES

- TrenchFET® Power MOSFETS
- 175°C Junction Temperature
- New Low Thermal Resistance Package
- PWM Optimized for Fast Switching

APPLICATIONS

- Isolated DC/DC converters
 - Primary-Side Switch



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$)	I_D	$T_C = 25^\circ\text{C}$	60 ^a
		$T_C = 125^\circ\text{C}$	34 ^a
Pulsed Drain Current	I_{DM}	100	A
Avalanche Current	I_{AR}	40	
Repetitive Avalanche Energy ^b	E_{AR}	80	mJ
Maximum Power Dissipation ^b	P_D	$T_C = 25^\circ\text{C}$	150 ^c
		$T_A = 25^\circ\text{C}^d$	3.75
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient	R_{thJA}	40	$^\circ\text{C/W}$
Junction-to-Case (Drain)	R_{thJC}	1.0	

Notes

- Package limited.
- Duty cycle $\leq 1\%$.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR-4 material).

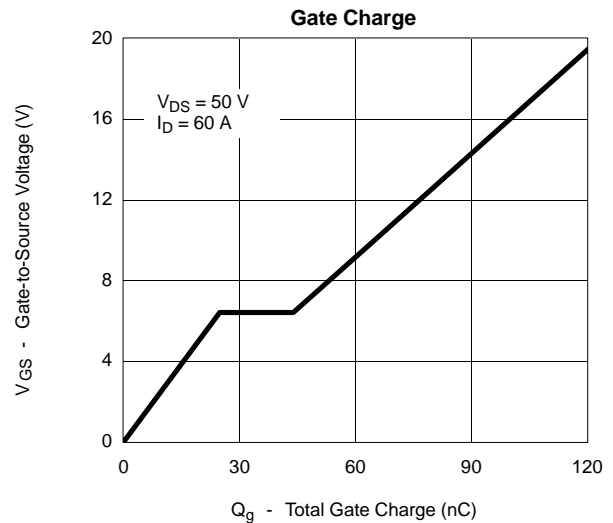
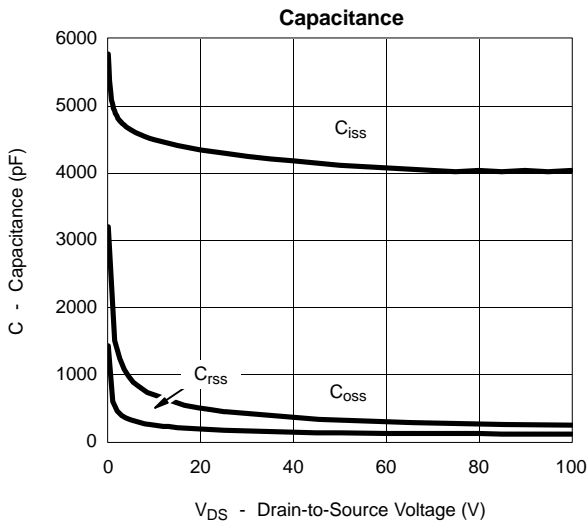
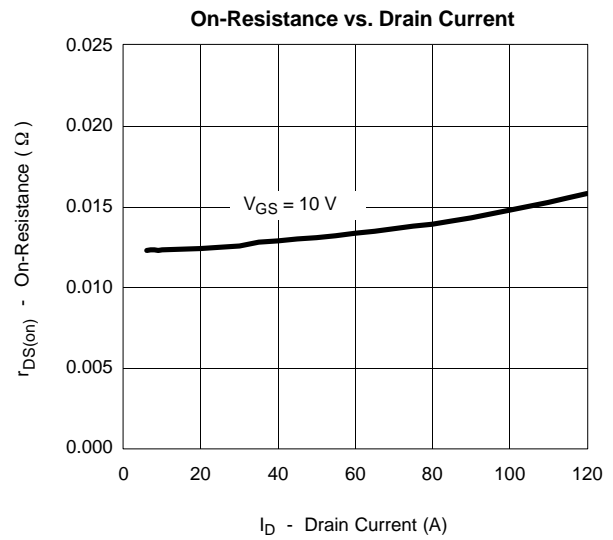
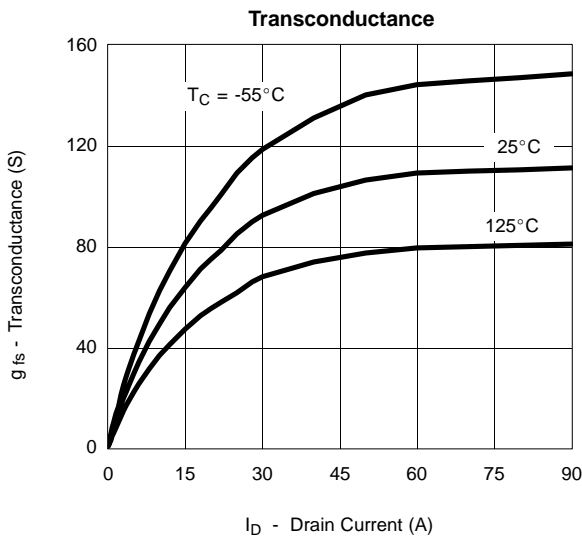
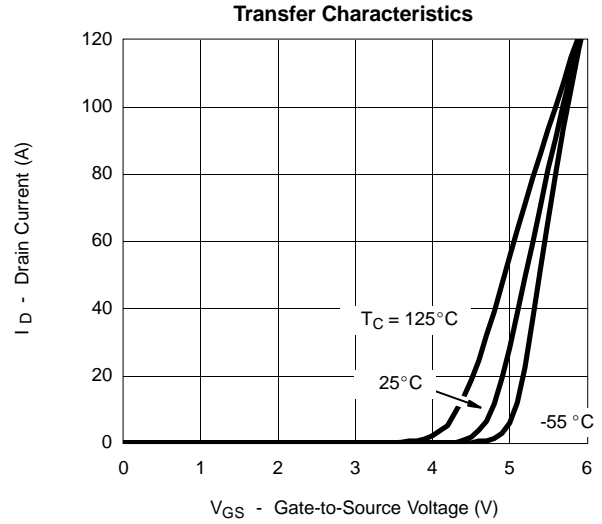
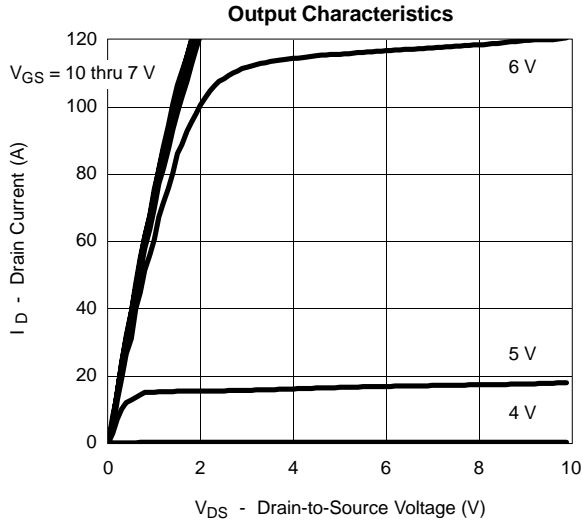
SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{DS} = 0 V, I _D = 250 μA	100			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	2		4	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 80 V, V _{GS} = 0 V			1	μA
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 125 °C			50	
		V _{DS} = 80 V, V _{GS} = 0 V, T _J = 175 °C			250	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	100			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 30 A		0.013	0.0165	Ω
		V _{GS} = 6 V, I _D = 20 A		0.015	0.019	
		V _{GS} = 10 V, I _D = 30 A, T _J = 125 °C			0.031	
		V _{GS} = 10 V, I _D = 30 A, T _J = 175 °C			0.041	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 30 A	25			S
Dynamic^b						
Input Capacitance	C _{iSS}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		4300		pF
Output Capacitance	C _{oss}			450		
Reverse Transfer Capacitance	C _{rSS}			175		
Total Gate Charge ^c	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 60 A		65	100	nC
Gate-Source Charge ^c	Q _{gs}			25		
Gate-Drain Charge ^c	Q _{gd}			19		
Gate Resistance	R _G			1.5		Ω
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 50 V, R _L = 1.5 Ω I _D ≅ 60 A, V _{GEN} = 10 V, R _G = 2.5 Ω		15	25	ns
Rise Time ^c	t _r			12	20	
Turn-Off Delay Time ^c	t _{d(off)}			30	45	
Fall Time ^c	t _f			10	15	
Source-Drain Diode Ratings and Characteristics (T_C = 25 °C)^b						
Continuous Current	I _S				60	A
Pulsed Current	I _{SM}				100	
Forward Voltage ^a	V _{SD}	I _F = 30 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 50 A, di/dt = 100 A/μs		125	200	ns
Peak Reverse Recovery Current	I _{RM(REC)}			8	12	A
Reverse Recovery Charge	Q _{rr}			0.5	1.2	μC

Notes

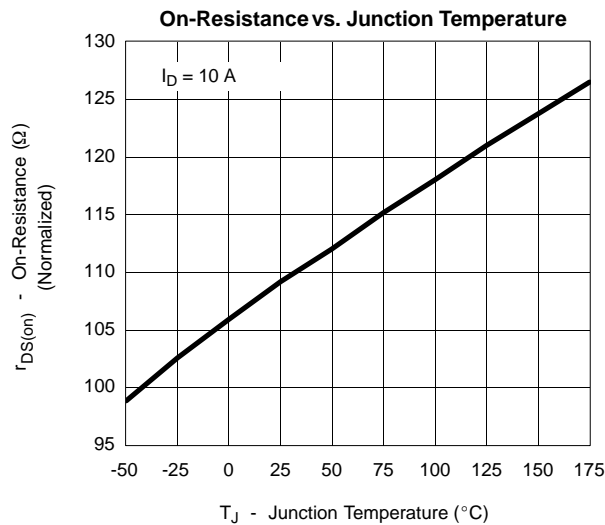
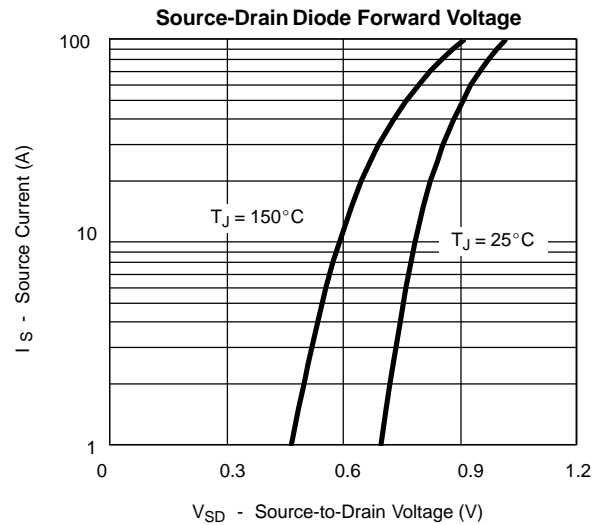
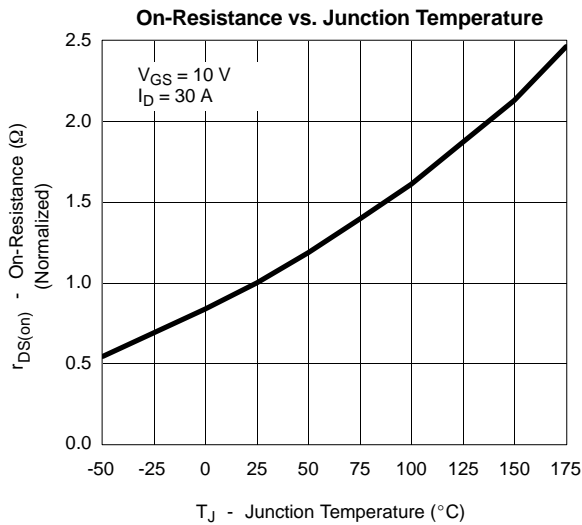
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.



TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

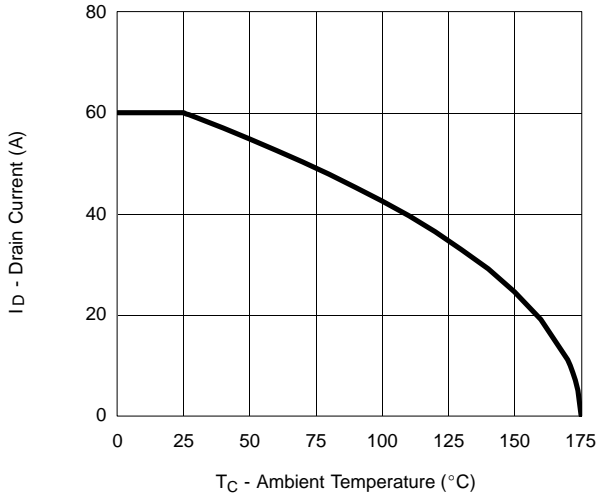


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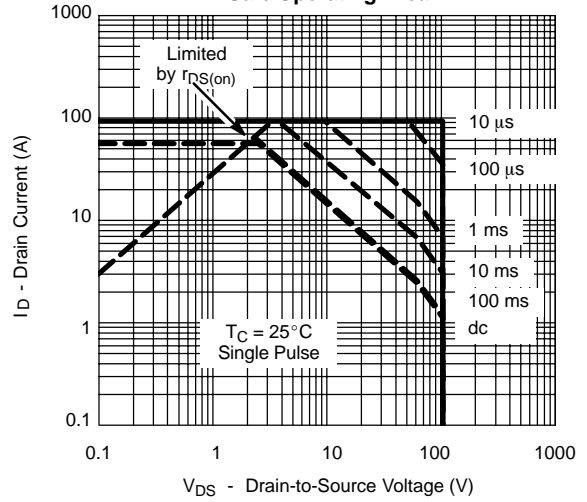


THERMAL RATINGS

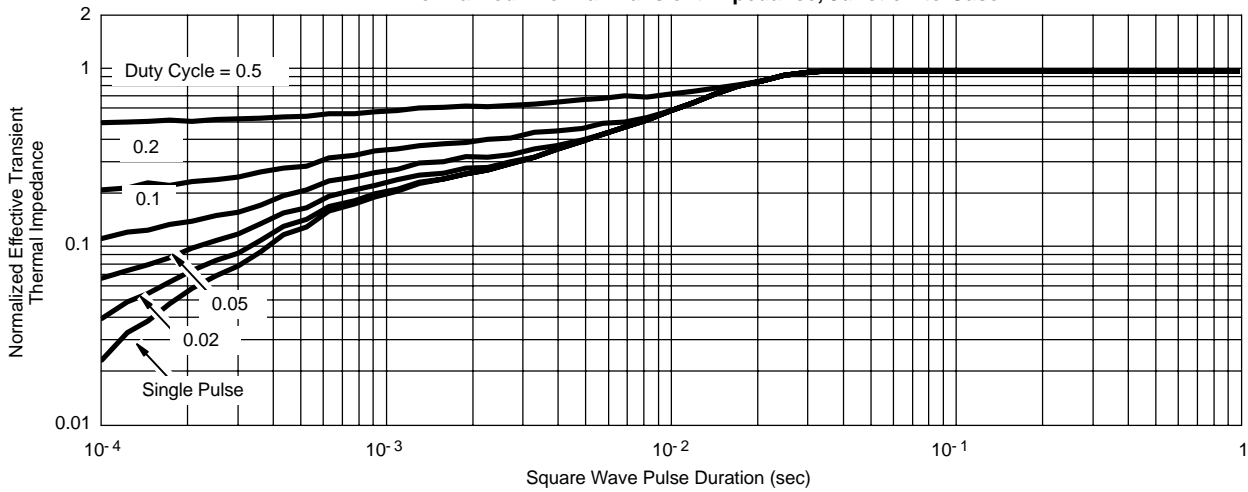
Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case





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