



MCR100

SCR

SENSITIVE GATE SILICON CONTROLLED RECTIFIERS REVERSE BLOCKING THYRISTORS

DESCRIPTION

PNPN devices designed for high volume, line-powered consumer applications such as relay and lamp drivers, small motor controls, gate drivers for larger thyristors, and sensing and detection circuits.

FEATURES

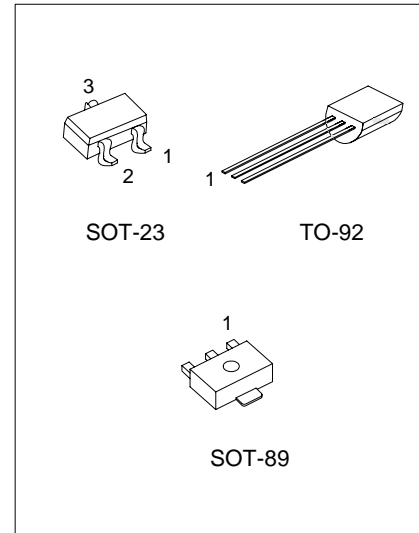
- * Sensitive gate allows triggering by micro controllers and other logic circuits
- * Blocking voltage to 600V
- * On-state current rating of 0.8A RMS at 80°C
- * High surge current capability – 10A
- * Minimum and maximum values of I_{GT} , V_{GT} and I_H specified for ease of design
- * Immunity to dV/dt – 20V/ μ sec minimum at 110
- * Glass-passivated surface for reliability and uniformity

ORDERING INFORMATION

Ordering Number		Package	Pin assignment			Packing
Normal	Lead Free Plating		1	2	3	
MCR100-4-x-AB3-R	MCR100L-4-x-AB3-R	SOT-89	G	A	K	Tape Reel
MCR100-4-x-AE3-R	MCR100L-4-x-AE3-R	SOT-23	G	K	A	Tape Reel
MCR100-4-x-T92-B	MCR100L-4-x-T92-B	TO-92	K	G	A	Tape Box
MCR100-4-x-T92-K	MCR100L-4-x-T92-K	TO-92	K	G	A	Bulk
MCR100-6-x-AB3-R	MCR100L-6-x-AB3-R	SOT-89	G	A	K	Tape Reel
MCR100-6-x-AE3-R	MCR100L-6-x-AE3-R	SOT-23	G	K	A	Tape Reel
MCR100-6-x-T92-B	MCR100L-6-x-T92-B	TO-92	K	G	A	Tape Box
MCR100-6-x-T92-K	MCR100L-6-x-T92-K	TO-92	K	G	A	Bulk
MCR100-8-x-AB3-R	MCR100L-8-x-AB3-R	SOT-89	G	A	K	Tape Reel
MCR100-8-x-AE3-R	MCR100L-8-x-AE3-R	SOT-23	G	K	A	Tape Reel
MCR100-8-x-T92-B	MCR100L-8-x-T92-B	TO-92	K	G	A	Tape Box
MCR100-8-x-T92-K	MCR100L-8-x-T92-K	TO-92	K	G	A	Bulk

Note: Pin assignment: G: Gate K: Cathode A: Anode

MCR100L-4-x-AB3-R	(1) Packing Type	(1) B: Tape Box, K: Bulk, R: Tape Reel
	(2) Package Type	(2) AB3: SOT-89, AE3: SOT-23, T92: TO-92
	(3) Rank	(3) x: Refer to CLASSIFICATION OF I_{GT}
	(4) Lead Plating	(4) L: Lead Free Plating, Blank: Pb/Sn



*Pb-free plating product number: MCR100L

■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Peak Repetitive Off-State Voltage(Note) ($T_J = -40 \sim 110$, Sine Wave, 50 ~ 60Hz; Gate Open)	V_{DRM}, V_{RRM}	200	V
		400	V
		600	V
On-State RMS Current ($T_C = 80$) 180 Condition Angles	$I_{T(RMS)}$	0.8	A
Peak Non-Repetitive Surge Current (1/2 cycle, Sine Wave, 60Hz, $T_J = 25$)	I_{TSM}	10	A
Circuit Fusing Considerations ($t = 8.3$ ms)	I^2t	0.415	A ² s
Forward Peak Gate Power ($T_A = 25$, Pulse Width $\leq 1.0\mu s$)	P_{GM}	0.1	W
Forward Average Gate Power ($T_A = 25$, $t = 8.3$ ms)	$P_{G(AV)}$	0.1	W
Peak Gate Current – Forward ($T_A = 25$, Pulse Width $\leq 1.0\mu s$)	I_{GM}	1	A
Peak Gate Voltage – Reverse ($T_A = 25$, Pulse Width $\leq 1.0\mu s$)	V_{GRM}	5	V
Operating Junction Temperature Range @ Rated V_{RRM} and V_{DRM}	T_J	-40 ~ +110	
Storage Temperature Range	T_{STG}	-40 ~ +150	

Note: V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL DATA

PARAMETER	SYMBOL	MAX	UNIT
Thermal Resistance, Junction to Ambient	TO-92	200	/W
	SOT-23/SOT-89	400	/W

■ ELECTRICAL CHARACTERISTICS ($T_J = 25$, unless otherwise stated)

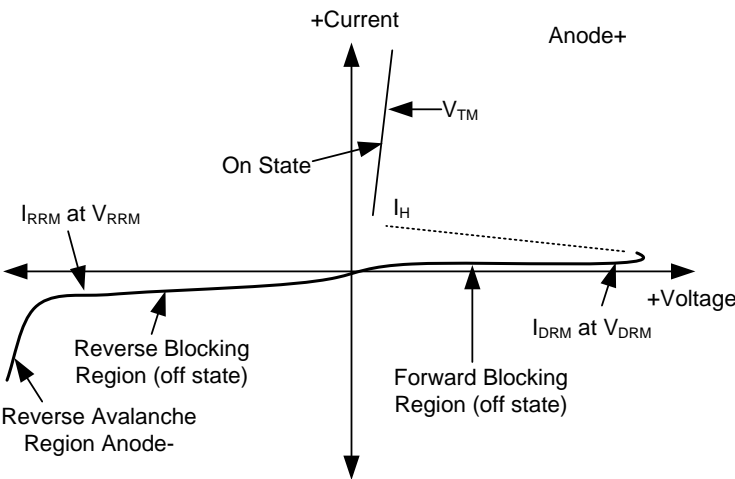
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Peak Forward or Reverse Blocking Current	T _C =25	I _{DRM} , I _{RRM}	V _D =Rated V _{DRM} and V _{RRM} ; R _{GK} =1kΩ			10	μA
	T _C =125					100	μA
ON CHARACTERISTICS							
Peak Forward On-State Voltage (Note1)		V _{TM}	I _{TM} =1A Peak @ T _A =25			1.7	V
Gate Trigger Current (Continuous DC)(Note2)		I _{GT}	V _{AK} =7Vdc, R _L =100Ω, T _C =25		40	200	μA
Holding Current (Note 3)	T _C =25	I _H	V _{AK} =7Vdc, initiating current=20mA		0.5	5	mA
	T _C =-40					10	mA
Latch Current	T _C =25	I _L	V _{AK} =7V, I _g =200μA		0.6	10	mA
	T _C =-40					15	mA
Gate Trigger Voltage (continuous dc) (Note 2)	T _C =25	V _{GT}	V _{AK} =7Vdc, R _L =100Ω		0.62	0.8	V
	T _C =-40					1.2	V
DYNAMIC CHARACTERISTICS							
Critical Rate of Rise of Off-State Voltage		d _v /dt	V _D =Rated V _{DRM} , Exponential Waveform, R _{GK} =1000Ω, T _J =110	20	35		V/μs
Critical Rate of Rise of On-State Current		di/dt	I _{PK} =20A; P _w =10μsec; diG/dt=1A/μsec. I _{gt} =20mA			50	A/μs

Note: 1. Indicates Pulse Test Width $\leq 1.0ms$, duty cycle $\leq 1\%$

2. $R_{GK} = 1000\Omega$ included in measurement.
3. Does not include R_{GK} in measurement.

■ VOLTAGE CURRENT CHARACTERISTIC OF SCR

PARAMETER	SYMBOL
Peak Repetitive Off Stat Forward Voltage	V_{DRM}
Peak Forward Blocking Current	I_{DRM}
Peak Repetitive Off State Reverse Voltage	V_{RRM}
Peak Reverse Blocking Current	I_{RRM}
Peak On State Voltage	V_{TM}
Holding Current	I_H

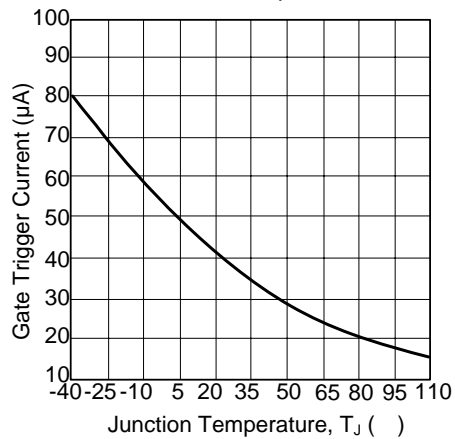


■ CLASSIFICATION OF I_{GT}

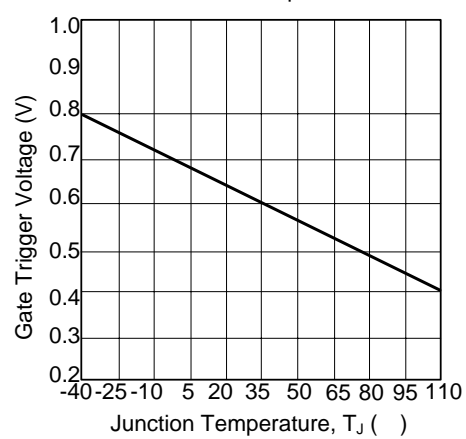
RANK	B	C	AA	AB	AC	AD
RANGE	48~105 μ A	95~200 μ A	8~16 μ A	14~21 μ A	19~25 μ A	23~52 μ A

TYPICAL CHARACTERISTICS

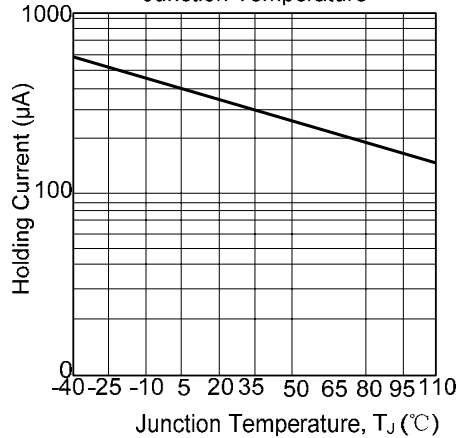
Typical Gate Trigger Current vs.
Junction Temperature



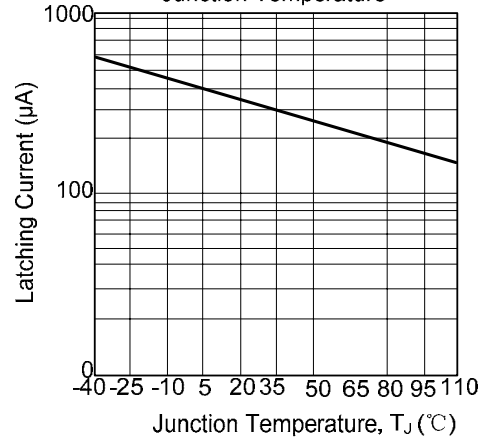
Typical Gate Trigger Voltage vs.
Junction Temperature



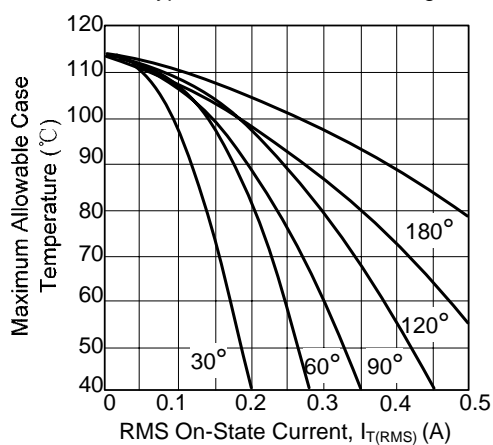
Typical Holding Current vs.
Junction Temperature



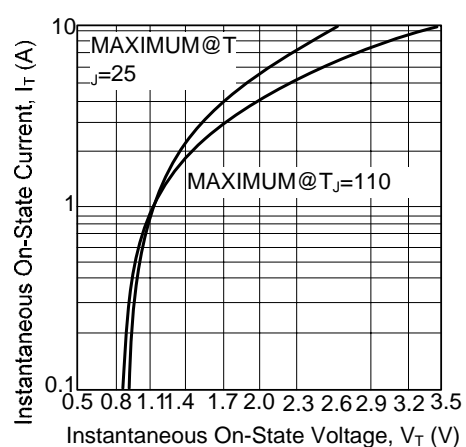
Typical Latching Current vs.
Junction Temperature



Typical RMS Current Derating



Typical On-State Characteristics



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