



Adjustable step-down, current-mode PWM DC-DC converters

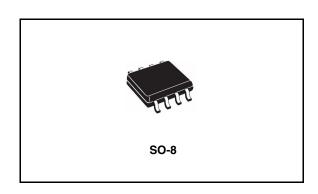
Features

- Up to 450mA load currents
- 200kHz high-frequency current-mode PWM
- 85% to 96% efficiencies
- 33mH or 100mH pre-selected inductor value, no component design required
- 0.8mA quiescent current
- 0.3µA shutdown supply current
- Adjustable output voltage
- Overcurrent, soft-start and undervoltage lockout protection
- Cycle-by-cycle current limiting
- Package available: SO-8



The ST750A is an adjustable output CMOS, stepdown switching regulator. The ST750A accepts inputs between 4V and 11V and delivers 450mA. Typical efficiencies are 85% to 96%.

Quiescent supply current is 0.8mA and only 0.3mA in shutdown mode. The output does not exhibit frequency over this specified range. Pulsewidth modulation (PWM) current-mode control



provides precise output regulation and excellent transient responses. Output voltage accuracy is guaranteed to be ±4.5% plus feedback resistor tolerance over line, load, and temperature variations.

Fixed-frequency switching and absence of subharmonic ripple allows easy filtering of output ripple and noise, as well as the use of small external components. This regulators require only a single inductor value to work in most applications, so no inductor design is necessary. Typical applications are: Cellular phones & radios, portable Instruments, Portable Communications Equipment and Computer Peripherals.

Order code

Part number	Packaging		
ST750AC	ST750ACDTR		

May 2007 Rev. 3 1/14

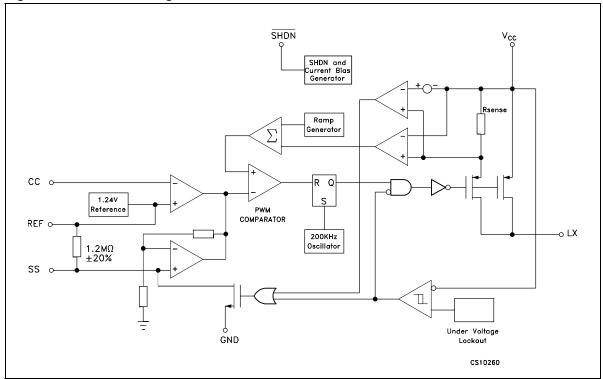
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ST750A Diagram

1 Diagram

Figure 1. Schematic diagram



Pin configuration ST750A

2 Pin configuration

Figure 2. Pin connections (top view)

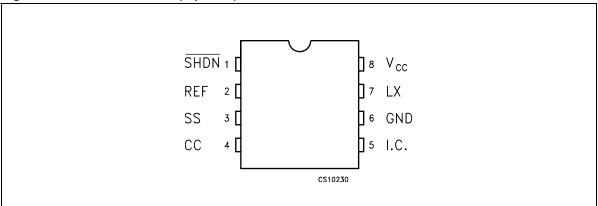


Table 1. Pin description

Pin N°	Symbol	Name and function
1	SHDN	Shutdown control (active low): If connected to GND the IC is in shutdown. Connect to V_{CC} for normal operation (ON MODE)
2	REF	Reference output voltage: (1.25V): Bypass to GND with a capacitor that does not exceed 47nF
3	SS	Soft start: a capacitor between SS and GND provides soft-start and short-circuit protections.
4	CC	Compensation capacitor input: externally compensates the outer (voltage) feedback loop. Connect to OUT with 330pF capacitor
5	IC	Internal connection: make no external connection to this pin
6	GND	Ground
7	LX	Switch output. Drain of internal P-channel power MOSFET
8	V _{CC}	Supply voltage input. Bypass to GND with $1\mu F$ ceramic capacitance and large value electrolytic capacitor in parallel. The $1\mu F$ capacitor must be as close as possible to the GND and V_{CC} pins

ST750A Maximum ratings

3 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit	
V _{CC}	DC Input voltage		-0.3 to 12	V
V_{LX}	Switch pin voltage		-0.3 to (V _{CC} + 0.3)	V
V _{SHDN}	Shutdown voltage (SHDN)	-0.3 to (V _{CC} + 0.3)	V	
V _S ,V _C	Soft start (SS) and compensation capacitor (C	-0.3 to (V _{CC} + 0.3)	٧	
I _{LX}	Switching peak current	2	Α	
I _{REF}	Reference current	2.5	mA	
P _{TOT}	Continuous power dissipation at T _A =70°C	344	mW	
T _{stg}	Storage temperature range	-40 to +150	°C	
T _{op}	Operating junction temperature range	0 to +70	°C	

Note:

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied

Table 3. Thermal data

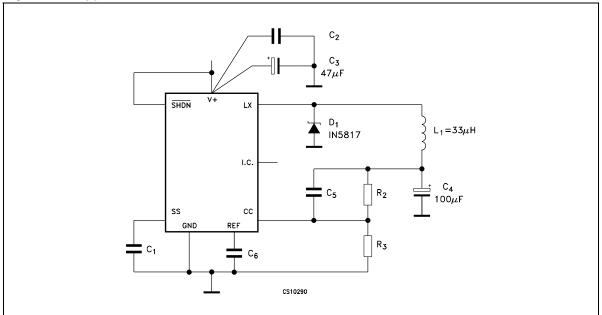
Symbol	Parameter	SO-8	Unit	
R _{thJA} Thermal resistance junction-ambient ⁽¹⁾		160	°C/W	

^{1.} This value depends from thermal design of PCB on which the device is mounted.

Typical application ST750A

4 Typical application

Figure 3. Application circuit



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5 Electrical characteristics

Table 4. Electrical characteristics (V_{CC} =5V, I_O = 0mA, T_A = T_{MIN} to T_{MAX} , unless otherwise specified).

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V _{CC}	Input voltage		4		11	V
V_{O}	Output voltage	$V_{CC} = 6 \text{ to } 11V, I_{O} = 0 \text{ to } 450\text{mA}$	4.75	5	5.25	V
ΔV_{O}	Line regulation	V _{CC} = 4 to 11V		0.15		%/V
ΔV_{O}	Load regulation	I _O = 0 to 450mA		0.005		%/mA
η	Power efficiency	I _O = 300mA		92		%
	Cupalitation	ON Mode		8.0	2.5	mA
I _{SUPPLY}	Supply current	OFF Mode, SHDN = 0		0.3	100	μΑ
V _{IH}	SHDN Input high threshold		2			V
V_{IL}	SHDN Input low threshold	V _{CC} Falling			0.25	V
I _{SHDN}	Shutdown input leakage current	I _{LX} = 500mA			1	μА
V_{LOCK}	Under voltage lockout	$V_{CC} = 12V, V_{LX} = 0V$		2.7	3	V
R _{DS(on)}	LX On resistance	T _A = 25°C		0.5		Ω
I _{LX}	LX Leakage current			1		μΑ
V_{REF}	Reference voltage		1.17	1.24	1.31	V
ΔV_{REF}	Temperature reference drift			50		ppm/°C
fosc	Switching frequency	C series	160	200	280	KHz
R_{C}	Compensation pin impedance			7500		Ω

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Typical performance characteristics 6

(Unless otherwise specified $T_J = 25^{\circ}C$)

Figure 4. Efficiency vs output current

ν(%) $V_1 = 5.5V$ $V_1 = 7V$ 90 =9V 80 70 60 200 600 400 800 lout(mA)

Figure 5. Supply current vs temperature

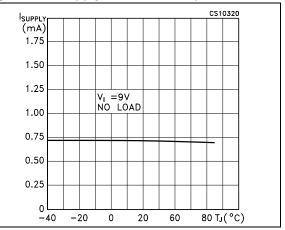


Figure 6. Oscillator frequency vs temp.

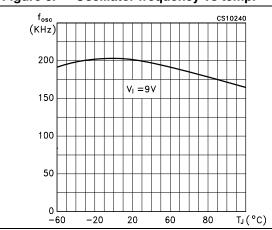


Figure 7. Oscillator frequency vs input volt.

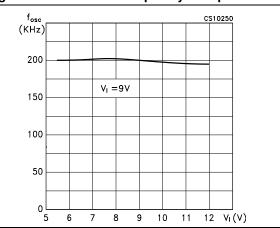


Figure 8. Peak inductor current vs out. curr.

 $V_1 = 12V$

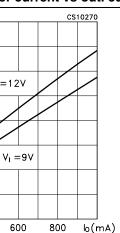
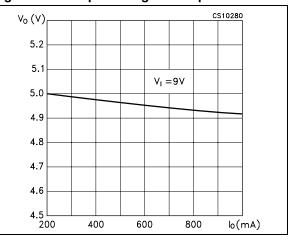


Figure 9. Output voltage vs output current



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I_{PEAK} (mA)

1400

1200

1000

800

600

400

200

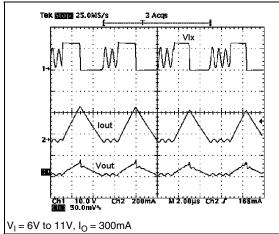
0

200

400

Figure 10. Switching waveforms, continuous conduction

Figure 11. Switching waveforms, discontinuous conduction



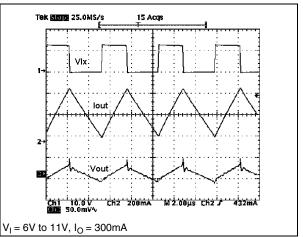
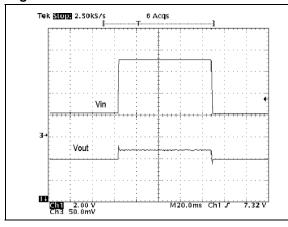
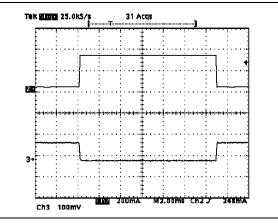


Figure 12. Line transient

Figure 13. Load transient





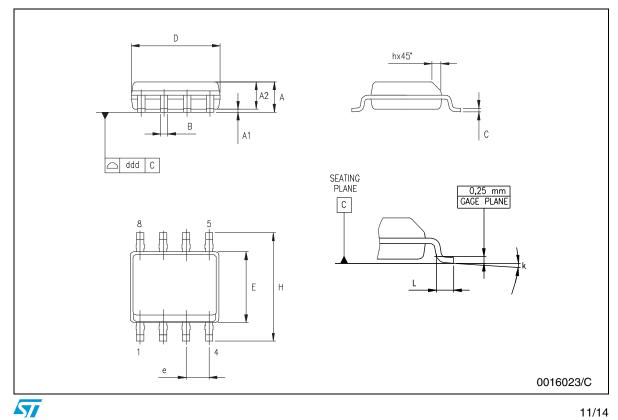
7 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK[®] packages. These packages have a Lead-free second level interconnect. The category of second Level Interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

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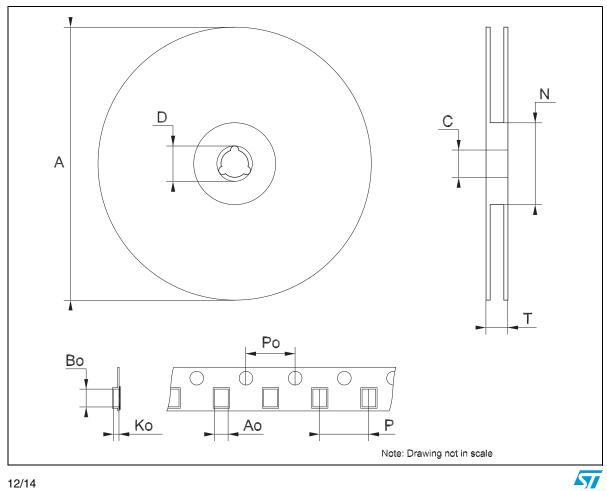
SO-8 MECHANICAL DATA

DIM	mm.				inch	
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
E	3.80		4.00	0.150		0.157
е		1.27			0.050	
Н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k			8° (n	nax.)		•
ddd			0.1			0.04



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DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
Α			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
Т			22.4			0.882
Ao	8.1		8.5	0.319		0.335
Во	5.5		5.9	0.216		0.232
Ko	2.1		2.3	0.082		0.090
Ро	3.9		4.1	0.153		0.161
Р	7.9		8.1	0.311		0.319



ST750A Revision history

8 Revision history

Table 5. Revision history

Date	Revision	Changes
29-May-2007	3	Order code has been updated and the document has been reformatted.

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