

DUAL BIDIRECTIONAL THYRISTOR OVERVOLTAGE PROTECTORS

TISP3xxxT3BJ Overvoltage Protector Series

Dual High Current Protectors in a Space Efficient Package

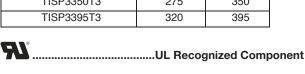
- 2 x 100 A 10/560 Current Rating
- Modified 3-pin SMB (DO-214AA) Package 50 % Space Saving over Two SMBs
- Y Configurations with Two SMB Packages

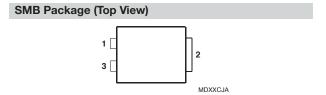
2 x 80 A, 10/1000TISP3xxxT3BJ + TISP4xxxJ1BJ 2 x 100 A, 10/700TISP3xxxT3BJ + TISP4xxxH3BJ

Ion-Implanted Breakdown Region

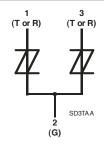
- Precise and Stable Voltage
- Low Voltage Overshoot under Surge

Device	V _{DRM} V	V _(BO)
TISP3070T3	58	70
TISP3080T3	65	80
TISP3095T3	75	95
TISP3115T3	90	115
TISP3125T3	100	125
TISP3145T3	120	145
TISP3165T3	135	165
TISP3180T3	145	180
TISP3200T3	155	200
TISP3219T3	180	219
TISP3250T3	190	250
TISP3290T3	220	290
TISP3350T3	275	350
TISP3395T3	320	395





Device Symbol



Rated for International Surge Wave Shapes

Wave Shape	Standard	I _{PPSM}
wave onape	Standard	Α
2/10	GR-1089-CORE	250
8/20	IEC 61000-4-5	250
10/160	TIA/EIA-IS-968 (FCC Part 68)	150
10/700	ITU-T K.20/.21/.45	120
10/560	TIA/EIA-IS-968 (FCC Part 68)	100
10/1000	GR-1089-CORE	80

Description

These dual bidirectional thyristor devices protect central office, access and customer premise equipment against overvoltages on the telecom line. The TISP3xxxT3BJ is available in a wide range of voltages and has an 80 A 10/1000 current rating. These protectors have been specified mindful of the following standards and recommendations: GR-1089-CORE, TIA/EIA-IS-968, UL 60950, EN 60950, IEC 60950, ITU-T K.20, K.21 and K.45. The TISP3350T3BJ meets the FCC Part 68 "B" ringer voltage requirement (V_{DRM} = ±275 V). Housed in a 3-pin modified SMB (DO-214AA) package, the TISP3xxxT3BJ range is space efficient solution for protection designs of 80 A or less which use multiple SMBs.

These devices allow signal voltages, without clipping, up to the maximum off-state voltage value, V_{DRM} , see Figure 1. Voltages above V_{DRM} are limited and will not exceed the breakover voltage, $V_{(BO)}$, level. If sufficient current flows due to the overvoltage, the device switches into a low-voltage on-state condition, which diverts the current from the overvoltage through the device. When the diverted current falls below the holding current, I_H , level the device switches off and restores normal system operation.

How To Order

Device	Package	Carrier	Order As
TISP3xxxT3BJ	BJ (3-pin modified SMB/DO-214AA J-Bend)	R (Embossed Tape Reeled)	TISP3xxxT3BJR-S

Insert xxx value corresponding to protection voltages of 070, 080, 095, 115, etc.

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Absolute Maximum Ratings, $T_A = 25$ °C (Unless Otherwise Noted)

Rating		Symbol	Value	Unit
'3'	070		±58	
'3	080		±65	
'3	095		±75	
'3	115		±90	
	125		±100	
	145		±120	
Renetitive neak off-state voltage (terminals 1-2 and 3-2)	165	V_{DRM}	±135	V
	180	Ditti	±145	
	200		±155	
	219 250		±180 ±190	
	290		±190 ±220	
	350		±275	
	395		±320	
Non-repetitive peak on-state pulse current (see Notes 1 and 2)				
2/10 (Telcordia GR-1089-CORE, 2/10 voltage wave shape)			2x250	
8/20 (IEC 61000-4-5, combination wave generator, 1.2/50 voltage wave shape)			2x250	
10/160 (TIA/EIA-IS-968 (replaces FCC Part 68), 10/160 μs voltage wave shape)			2x150	
5/310 (ITU-T K.44, 10/700 μs voltage wave shape used in K.20/.45/.21)		I _{PPSM}	2x120	Α
5/320 (TIA/EIA-IS-968 (replaces FCC Part 68), 9/720 μs voltage wave shape)			2x120	
10/560 (TIA/EIA-IS-968 (replaces FCC Part 68), 10/560 μs voltage wave shape)			2x100	
10/1000 (Telcordia GR-1089-CORE, 10/1000 voltage wave shape)			2x80	
Non-repetitive peak on-state current (see Notes 1 and 2)				
50 Hz, 1 cycle		1	2x25	Α
60 Hz, 1 cycle		I _{TSM}	2x30	^
1000 s 50 Hz/60 Hz a.c.			2x1.2	
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value < 50 A		di _T /dt	500	A/μs
Junction temperature		TJ	-40 to +150	°C
Storage temperature range		T _{stg}	-65 to +150	°C

NOTES: 1. Initially, the device must be in thermal equilibrium with T_J = 25 °C.

Recommended Operating Conditions

	Component	Min	Тур	Max	Unit
	Series resistor for GR-1089-CORE first-level surge survival	5			
	Series resistor for ITU-T recommendation K. 20/.45/.21 (coordination with 400 V GDT at 4 kV)	6.4			
R1, R2	Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 9/720 survival	0			Ω
	Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 10/560 survival	0			
	Series resistor for TIA/EIA-IS-968 (replaces FCC Part 68) 10/160 survival	2.5			

SEPTEMBER 2001 - REVISED JANUARY 2007 Specifications are subject to change without notice. Customers should verify actual device performance in their specific applications.

^{2.} These non-repetitive rated currents are peak values of either polarity. The rated current values are applied to the terminals 1 and 3 simultaneously (in this case the terminal 2 return current will be the sum of the currents applied to the terminals 1 and 3). The surge may be repeated after the device returns to its initial conditions.

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Electrical Characteristics for the 1 and 2 or the 3 and 2 Terminals, $\rm T_{\mbox{\scriptsize A}}$ = 25 $^{\circ}\mbox{\scriptsize C}$

	Parameter	Test Conditions		Min	Тур	Max	Unit
	Repetitive peak off-	V = V	T _A = 25 °C			±5	^
I _{DRM}	state current	$V_D = V_{DRM}$	$T_A = 85 ^{\circ}C$			±10	μΑ
			'3070			±70	
			'3080			±80	
			'3095			±95	
			'3115			±115	
			'3125			±125	
			'3145			±145	
V	AC breakover voltage	$dv/dt = \pm 250 \text{ V/ms}, R_{SOURCE} = 300 \Omega$	'3165			±165	V
v(BO)	AC breakover voltage	uvidi = ±250 v/iiis, K SOURCE = 500 \(\frac{1}{2} \)	'3180			±180	V
			'3200			±200	
			'3219			±219	
			'3250			±250	
			'3290			±290	
			'3350			±350	
			'3395			±395	
			'3070			±81	
			'3080			±91	
			'3095			±107	
			'3115			±128	
			'3125			±138	
		$dv/dt \le \pm 1000 \text{ V/}\mu\text{s}$, Linear voltage ramp,	'3145			±159	
V	Ramp breakover	Maximum ramp value = ±500 V	'3165			±179	V
$V_{(BO)}$	voltage	di/dt = ±20 A/μs, Linear current ramp,	'3180			±195	V
		Maximum ramp value = ±10 A	'3200			±215	
			'3219			±234	
			'3250			±265	
			'3290			±304	
			'3350			±361	
			'3395			±403	
$I_{(BO)}$	Breakover current	$dv/dt = \pm 250 \text{ V/ms}, R_{SOURCE} = 300 \Omega$				±800	mA
Ι _Η	Holding current	$I_T = \pm 5 \text{ A, di/dt} = +/-30 \text{ mA/ms}$		±150			mA
dv/dt	Critical rate of rise of off-state voltage	Linear voltage ramp, Maximum ramp value < 0.85V _{DRM}		±5			kV/μs
I _D	Off-state current	V _D = ±50 V	T _A = 85 °C			±10	μΑ

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Electrical Characteristics for the 1 and 2 or the 3 and 2 Terminals, T_A = 25 °C (Continued)

	Parameter		Test Condition	าร	Min	Тур	Max	Unit
		f = 1 MHz,	$Vd = 1 V rms, V_D = 0,$	'3070 thru '3095 '3115 thru '3219		95 69	114 83	
		f = 1 MHz,	$Vd = 1 V rms, V_D = -1 V$	'3250 thru '3395 '3070 thru '3095		51 90	62 108	
		5 4 MIL	Vd 4 V 7222 V 6 V	'3115 thru '3219 '3250 thru '3395 '3070 thru '3095		63 46 83	76 55 100	
C _{off}	Off-state capacitance	T = 1 MHZ,	$Vd = 1 V rms, V_D = -2 V$	'3115 thru '3219 '3250 thru '3395		59 42	70 51	pF
		f = 1 MHz,	$Vd = 1 V rms, V_D = -50 V$	'3070 thru '3095 '3115 thru '3219		43 29	51 35	
		1	Vd = 1 V rms, V _D = -100 V	'3250 thru '3395 '3250 thru '3395		20 16	24 19	
		(see Note 3	3)					

NOTE 3: These capacitance measurements employ a three terminal capacitance bridge incorporating a guard circuit. The unmeasured third terminal is connected to the guard terminal of the bridge.

Thermal Characteristics

Parameter	Test Conditions	Min	Тур	Max	Unit
R _{θJA} Junction to free air thermal resistance	EIA/JESD51-3 PCB, $I_T = I_{TSM(1000)}$, $T_A = 25$ °C, (see Note 4)			90	°C/W

NOTE 4: EIA/JESD51-2 environment and PCB has standard footprint dimensions connected with 5 A rated printed wiring track widths.

Parameter Measurement Information

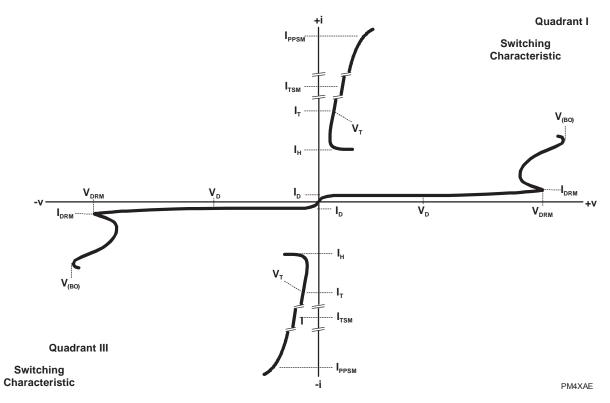


Figure 1. Voltage-Current Characteristic for Terminal Pairs 1-2 and 3-2 All Measurements are Referenced to Terminal 2

Typical Characteristics

OFF-STATE CURRENT VS JUNCTION TEMPERATURE TC4AH3AA TC4AHAAA TC4AH3AA TC4AHAAA TC4AHAAA TC4AHAAA TC4AHAAA TC4AHAAA TC

Figure 2.

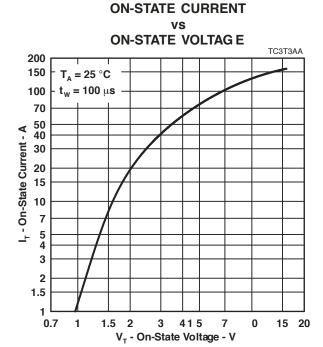


Figure 4.

NORMALIZED BREAKOVER VOLTAGE

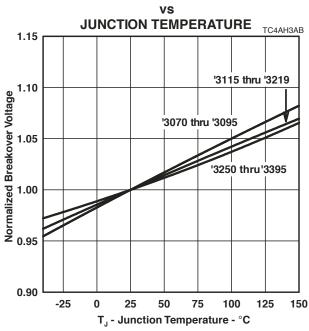


Figure 3.

NORMALIZED HOLDING CURRENT

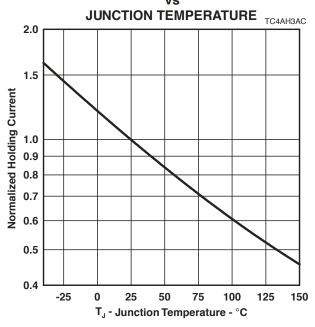


Figure 5.

Typical Characteristics

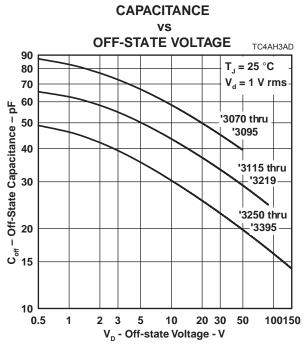


Figure 6.

OFF-STATE CAPACITANCE vs RATED REPETITIVE PEAK OFF-STATE VOLTAGE

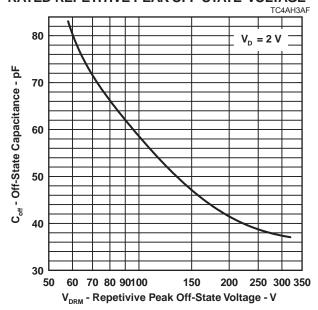


Figure 7.

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Rating and Thermal Information

NON-REPETITIVE PEAK ON-STATE CURRENT

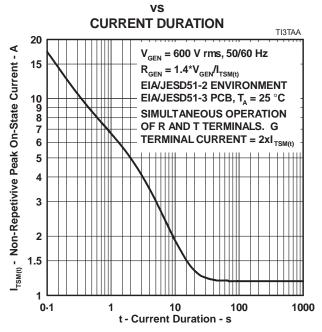


Figure 8.

V_{DRM} **DERATING FACTOR**

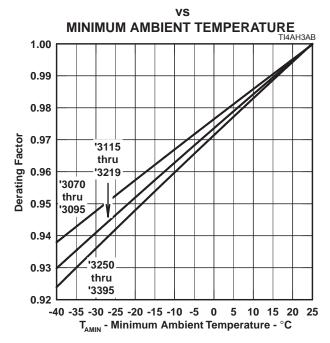
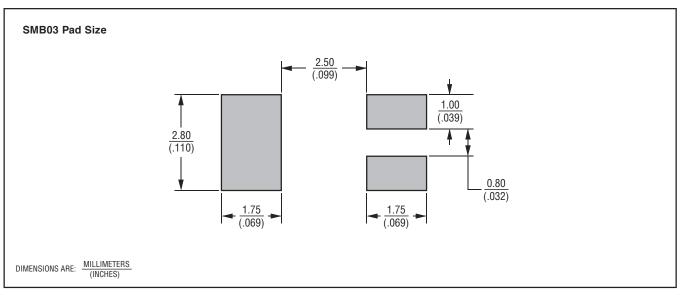


Figure 9.

MECHANICAL DATA

Recommended Printed Wiring Land Pattern Dimensions



MD3BJAAA

Device Symbolization Code

Devices will be coded as below.

Device	Symbolization Code
TISP3070T3	3070T3
TISP3080T3	3080T3
TISP3095T3	3095T3
TISP3115T3	3115T3
TISP3125T3	3125T3
TISP3145T3	3145T3
TISP3165T3	3165T3
TISP3180T3	3180T3
TISP3200T3	3200T3
TISP3219T3	3219T3
TISP3250T3	3250T3
TISP3290T3	3290T3
TISP3350T3	3350T3
TISP3395T3	3395T3

Carrier Information

For production quantities, the carrier will be embossed tape reel pack. Evaluation quantities may be shipped in bulk pack or embossed tape.

Package	Carrier	Standard Quantity
SMB	Embossed Tape Reel Pack	3000

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