

<u>DFLS140L</u>

1.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

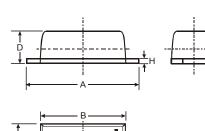
PowerDI[™]123

Features

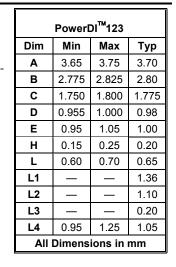
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- High Current Capability and Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDI™123
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202,Method 208 (2)
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.096 grams (approximate)



13



Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit		
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} VR	40	V		
RMS Reverse Voltage	V _{R(RMS)}	28	V		
Average Forward Current @ T _T = 120°C	I _{F(AV)}	1.0	А		
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	50	A		

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Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
Power Dissipation (Note 1)	PD	1.67	W		
Power Dissipation (Note 2)	PD	556	mW		
Thermal Resistance Junction to Soldering Point (Note 3)	$R_{\theta JS}$	10	°C/W		
Thermal Resistance Junction to Ambient (Note 1)	$R_{ ext{ heta}JA}$	60	°C/W		
Thermal Resistance Junction to Ambient (Note 2)	$R_{ ext{ heta}JA}$	180	°C/W		
Operating Temperature Range	Tj	-55 to +125	°C		
Storage Temperature Range	T _{STG}	-55 to +150	°C		

Notes: 1. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode. T_A = 25°C

2. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads.T_A = 25°C

3. Theoretical R_{BJS} calculated from the top center of the die straight down to the PCB cathode tab solder junction.

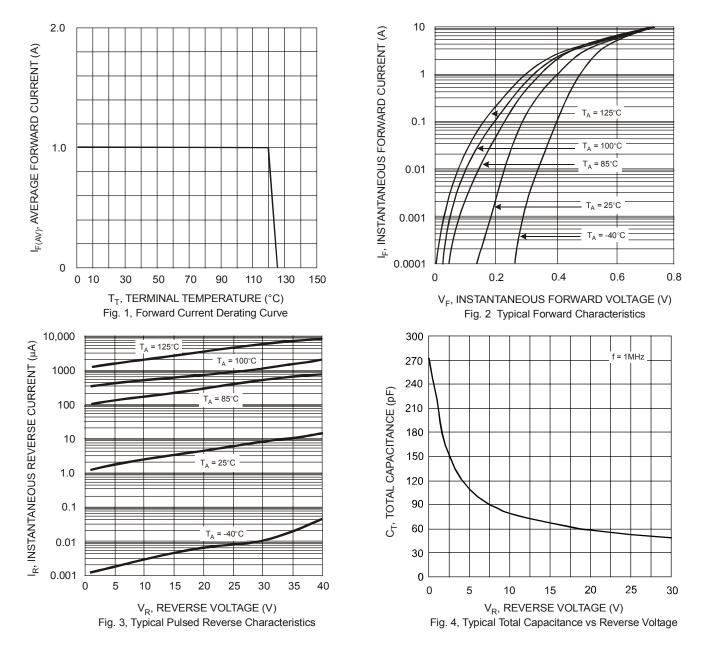
4. RoHS revision 13.2.2003. High temperature solder exemption applied, see EU Directive Annex Note 7.



Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
Reverse Breakdown Voltage (Note 5)	V _{(BR)R}	40	_	—	V	I _R = 500μA		
		_		0.36		I _F = 0.1A, T _J = 25°C		
Forward Voltage		—	—	0.30		I _F = 0.1A, T _J = 85°C		
	VF			0.55		I _F = 1.0A, T _J = 25°C		
	٧F	—		0.515	v	I _F = 1.0A, T _J = 85°C		
		_	—	0.85		I _F = 3.0A, T _J = 25°C		
		_	_	0.88		I _F = 3.0A, T _J = 85°C		
		_	_	0.5	mA	V _R = 40V, T _J = 25°C		
Leakage Current (Note 5)	1-	—	—	25		V _R = 40V, T _J = 85°C		
	IR	_		0.15		V _R = 20V, T _J = 25°C		
		_	—	18		V _R = 20V, T _J = 85°C		
Total Capacitance	CT	_	55	_	pF	V _R = 10V, f = 1.0MHz		

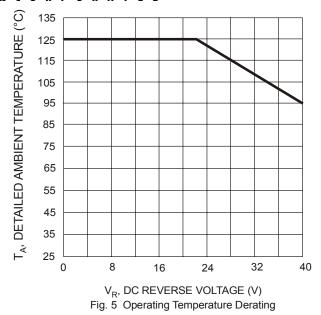
Notes: 5. Short duration pulse test to minimize self-heating effect.



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Ordering Information (Note 6)

Device	Packaging	Shipping		
DFLS140L-7	PowerDI™123	3000/Tape & Reel		

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



F06 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

Year	2004	20	05	2006	2007	20	08	2009	2010	20	11	2012
Code	R	93	6	Т	U	Ň	V	W	Х		Y	Z
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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