

Emitter common (dual digital transistors)

EMG8 / UMG8N / FMG8A

●Features

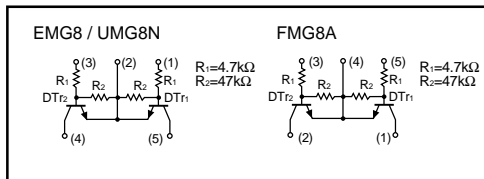
- 1) Two DTC143Z chips in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

●Structure

Epitaxial planar type
NPN silicon transistor
(Built-in resistor type)

The following characteristics apply to both the DT_{r1} and DT_{r2}.

●Equivalent circuit

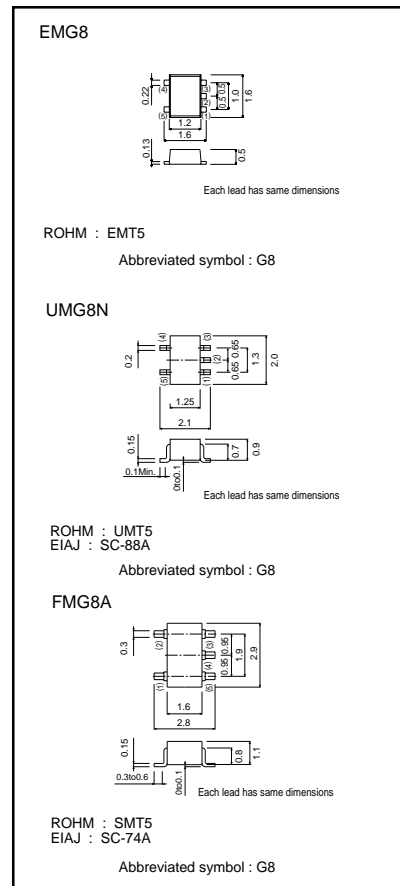


●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	30	V
		-5	
Output current	I _O	100	mA
	I _{C (Max.)}	100	
Power dissipation	P _d	150 (TOTAL)	*1 mW
		300 (TOTAL)	*2 mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C

*1 120mW per element must not be exceeded.
*2 200mW per element must not be exceeded.

●External dimensions (Unit : mm)



Transistors

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	0.5	V	$V_{CC}=5V, I_{O}=100\mu A$
	$V_{I(on)}$	1.3	—	—		$V_{O}=0.3V, I_{O}=5mA$
Output voltage	$V_{O(on)}$	—	0.1	0.3	V	$I_{O}=5mA, I_{I}=0.25mA$
Input current	I_{I}	—	—	1.8	mA	$V_{I}=5V$
Output current	$I_{O(off)}$	—	—	0.5	μA	$V_{CC}=50V, V_{I}=0V$
DC current gain	G_{I}	80	—	—	—	$V_{O}=5V, I_{O}=10mA$
Transition frequency	f_{T}	—	250	—	MHz	$V_{CE}=10V, I_{E}=-5mA, f=100MHz$ *
Input resistance	R_{I}	3.29	4.7	6.11	$k\Omega$	—
Resistance ratio	R_{2}/R_{1}	8	10	12	—	—

* Transition frequency of the device

●Packaging specifications

Type	Package	Taping		
	Code	T2R	TR	T148
	Basic ordering unit (pieces)	8000	3000	3000
EMG8	○	—	—	—
UMG8N	—	○	—	—
FMG8A	—	—	—	○

●Electrical characteristic curves

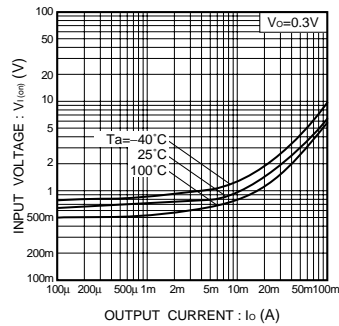


Fig.1 Input voltage vs. output current (ON characteristics)

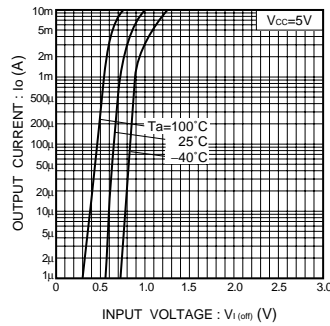


Fig.2 Output current vs. input voltage (OFF characteristics)

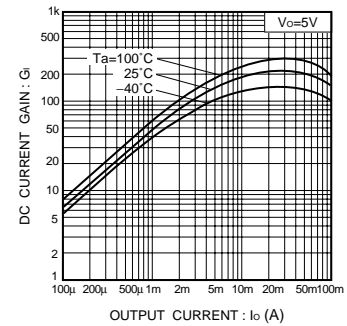


Fig.3 DC current gain vs. output current

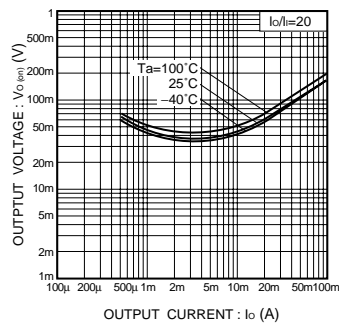


Fig.4 Output voltage vs. output current

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