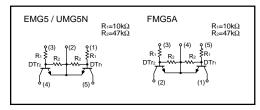
Emitter common (dual digital transistors)

EMG5 / UMG5N / FMG5A

Features

1) Two DTC114Y chips in a EMT or UMT or SMT package.

●Equivalent circuit

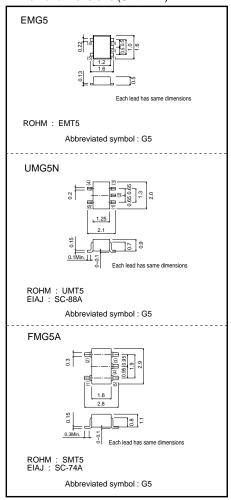


● Absolute maximum ratings (Ta = 25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	50	V	
Input voltage		Vin	40	V	
		VIN	-6		
Output current		lo	70	- A	
		Ic (Max.)	100	mA	
Power dissipation	EMG5, UMG5N	Pd	150 (TOTAL)	mW *1	
	FMG5A	Pu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	

- *1 120mW per element must not be exceeded. *2 200mW per element must not be exceeded

●External dimensions (Unit: mm)



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Input voltage	VI (off)	-	-	0.3	V	Vcc=5V, Io=100μA	
input voitage	VI (on)	1.4	-	_	V	Vo=0.3V, Io=1mA	
Output voltage	Vo (on)	-	0.1	0.3	V	lo=5mA, l⊫0.25mA	
Input current	lı	-	-	0.88	mA	Vi=5V	
Output current	IO (off)	-	-	0.5	μΑ	Vcc=50V, Vi=0V	
DC current gain	Gı	68	-	_	_	Vo=5V, Io=5mA	
Transition frequency	f⊤	-	250	_	MHz	VcE=10V, IE= -5mA, f=100MHz *	
Input resistance	R ₁	7	10	13	kΩ	-	
Resistance ratio	R ₂ /R ₁	3.7	4.7	5.7	-	_	

^{*} Transition frequency of the device

Packaging specifications

	Package		Taping	
	Code	T2R	TR	T148
Type	Basic ordering unit (pieces)	8000	3000	3000
EMG5		0	-	-
UMG5N		-	0	-
FMG5A		_	_	0

•Electrical characteristics curves

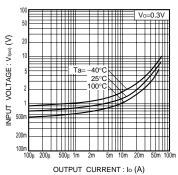


Fig.1 Input voltage vs. output current (ON characteristics)

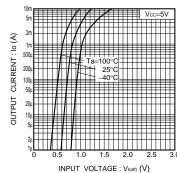


Fig.2 Output current vs. input voltage (OFF characteristics)

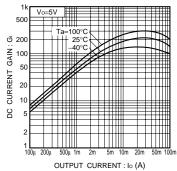


Fig.3 DC current gain vs. output current

Rev.A

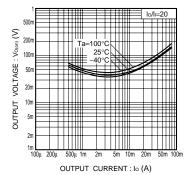


Fig.4 Output voltage vs. output current

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