

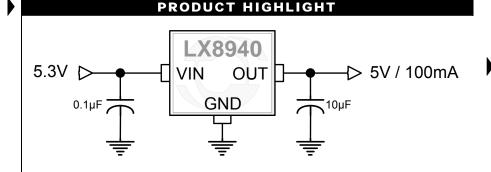
PRODUCTION DATA SHEET

DESCRIPTION

The LX8940 is a 5V, low dropout, low quiescent current regulator rated for 1A of output current. It can regulate with as low as 0.4V headroom between the input and output voltages, thus minimizing power dissipation. In addition, it can be used in applications where worst

case supplies require a low input-output differential to maintain regulation. This feature makes it ideal for computer monitors that have to comply with energy-efficient / "Green PC" programs, where the input voltage drops to only a few tenths of a volt above the output when power supply enters sleep-mode operation.

IMPORTANT: For the most current data, consult MICROSEMI's website: http://www.microsemi.com

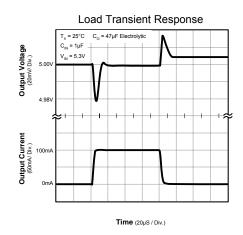


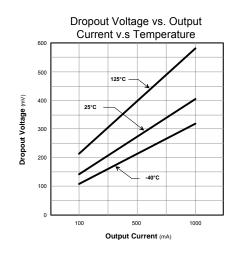
KEY FEATURES

- 2% Internally Trimmed Output
- Output Current In Excess Of
- Input-Output Differential Less Than 0.4V At 1A
- **Reverse Battery Protection**
- 60V Load Dump Protection
- -50V Reverse Transient Protection
- **Short Circuit Protection**
- Internal Thermal Overload Protection
- Available In 3-Lead Plastic TO-
- Drops In Most IM2940 Sockets

APPLICATIONS

- Small Headroom Battery **Applications**
- High Efficiency Linear Regulators
- Post Regulators For Switching Power Supplies
- Green PC Monitor Applications





PACKAGE ORDER INFO				
T _A (°C)	Plastic TO-220 3-Pin RoHS Compliant / Pb-free Transition DC: 0543			
0 to 125	LX8940CP			
-40 to 125	LX8940IP			

Note: Available in Tape & Reel. Append the letters "TR" to the part number. (i.e. LX8940IP-TR)

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ABSOLUTE MAXIMUM RATINGS

Input Voltage (VIN)	15V to 26V
Maximum Operating Junction Temperature	
Storage Temperature Range	65°C to 150°C
Peak Package Solder Reflow Temp. (40 seconds max. exposure)	260°C (+0,-5)

Note: Exceeding these ratings could cause damage to the device. All voltages are with respect to Ground. Currents are positive into, negative out of specified terminal. Pin numbers refer to DIL packages only

TAB is GND Output GND OTHER GRAD OUTPUT PPACKAGE (Top View) RoHS / Pb-free 100% matte Tin Lead Finish

THERMAL DATA

Plastic TO-220 3-Pin

THERMAL RESISTANCE-JUNCTION TO TAB, $ heta_{JT}$	3.0°C/W		
THERMAL RESISTANCE-JUNCTION TO AMBIENT, θ_{JA}	60°C/W		

Junction Temperature Calculation: $T_J = T_A + (P_D \times \theta_{JA})$.

The θ_{JA} numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	LX8940			Units
i arameter	Oyillboi	Min	Тур	Max	Ullits
Input Voltage	VIN	Note 2		26	V
Load Current (with adequate heatsinking)		5		1000	mA
Maximum Line Transient (Load Dump), VO ≤ 5.5V				60	V
Input Capacitor (VIN to GND)		0.1			μF
Output Capacitor with ESR of 10Ω max., (V_{OUT} to GND & V_{SB} to GND)		10			μF

Note 2: $V_{IN(MIN)} = 1.2\Delta V_{(MAX)}$ See Dropout Voltage Maximum Limit.



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ELECTRICAL CHARACTERISTICS

Unless otherwise specified, the following specifications apply over the operating ambient temperature -40°C to +125°C for the LX8940IP and 0°C to +125°C for LX8940CP; $V_{IN} = 10V$, $I_O = 1A$, $C_{OUT} = 22\mu F$, and for DC characteristics only. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

Parameter	Cumbal	Took Conditions		LX8940		
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
SECTION HEADER						
Output Voltage	Vo	I _O = 0A, T _A = 25°C	4.85	5	5.15	V
Line Regulation	ΔV_{OI}	7V <u><</u> V _{IN} <u><</u> 26, I _O =5mA		1	50	mV
Load Regulation	ΔV_{OL}	50mA ≤ I _O ≤ 1A		10	50	mV
Output Impedance (Note 3)	ro	100mA_{DC} and 20mA_{RMS} , $f_{O} = 120\text{Hz}$		200		mΩ
	IQ	$I_0 \le 5 \text{mA}, 7 \le V_{IN} \le 26 \text{V}$		3	15	mA
Quiescent Current		I _O = 500mA		30	50	mA
		I _O = 1000mA		115	180	mA
Output Noise Voltage (Note 3)	V _{ORMS}	$10Hz - 100kHz, I_0 = 5mA$		150		μV_{RMS}
Long Term Stability (Note 3)				20		mV / 1000h
Ripple Rejection (Note 3)	R_R	$f_O = 120Hz, 1V_{RMS}, I_O = 100mA$		66		dB
		I _O = 100mA		150	300	mV
Dropout Voltage	ΔV	I _O = 500mA		275	500	mV
		I _O = 1A		400	800	mV
Current Limit	I _{CL}	V _{IN} = 26V	1	1.2		Α
Maximum Operational Input Voltage	V _{IN(MAX)}		26	31		V
Maximum Line Transient	$V_{IN(TR)}$	$R_0 = 100\Omega, T < 100mS$		60		V

Note 3: These parameters, although guaranteed, are not tested in production.

SIMPLIFIED BLOCK DIAGRAM

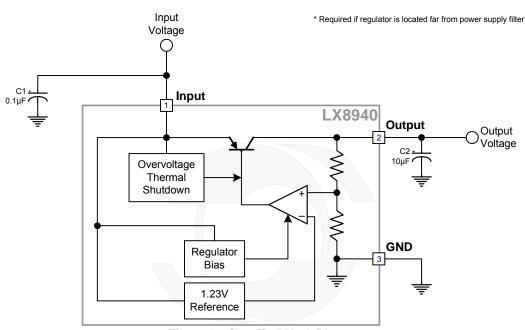


Figure 1 – Simplified Block Diagram



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APPLICATION NOTE

The advantages of using a low-dropout regulator such as the LX8940 is the need for less "headroom" for full regulation, and the inherent reverse polarity protection provided by the PNP output device. A typical NPN regulator design requires an input to output differential of approximately two volts minimum. This is due to the 2Vbe + Vcesat of the NPN Darlington used in the output, coupled with the voltage drop across the current limit resistor. In contrast, the "PNP Regulator" uses a single series pass transistor with its single Vcesat, thus the lower input to output voltage differential or dropout voltage. In some applications the regulator output voltage is used not only as a power supply but also as a voltage reference for control systems. In such cases not just the temperature stability of the output is important but also the initial accuracy. LX8940 fills this need as the internal bandgap reference is trimmed allowing a typical output voltage tolerance of $\pm 1\%$.

EXTERNAL CAPACITORS

To stabilize the outputs and prevent oscillation (perhaps by many volts) external capacitors are required. The minimum recommended value for the output capacitors is $10\mu F$, although the actual size and type will likely vary according to the particular application, e.g., operating temperature range and load. Another consideration is the effective series resistance (ESR) of the

capacitor. Capacitor ESR will vary by manufacturer. Consequently, some evaluation may be required to determine the minimum value of the output capacitors. Generally worst case occurs at the maximum load and minimum ambient temperature. The size of the output capacitor can be increased to any value above the minimum. One possible advantage of this would be to maintain the output voltage during brief periods of negative input transients The output capacitors chosen should be rated for the full range of ambient temperature over which the circuit will be exposed and expected to operate. For example, many aluminum type electrolytic capacitors change values at cold temperatures. The effective capacitance is reduced and regulator stability is affected. Tantalum capacitors are a good choice for these types of environments.

OUTPUT PROTECTION

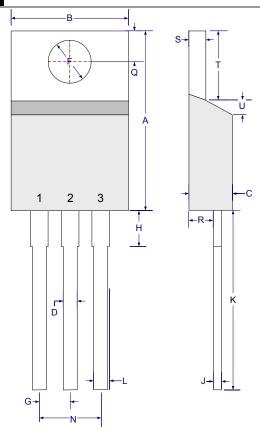
The output features fault protection against over voltage as well as a thermal shutdown feature. If the input voltage rises above 33V (load dump), the output shuts down automatically. The internal circuitry is thus protected and the IC is able to survive higher voltage transients than might otherwise be expected. The thermal shutdown output effectively guards against overheating of the die and protects the device from being damaged.



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PACKAGE DIMENSIONS

3-Pin Plastic TO-220



	MILLIM	ETERS	INCHES		
Dim	MIN	MAX	MIN	MAX	
Α	14.22	15.88	0.560	0.625	
В	9.65	10.67	0.380	0.420	
С	3.56	4.83	0.140	0.190	
D	0.51	1.14	0.020	0.045	
F	3.53	4.09	0.139	0.161	
G	2.54	BSC	0.100 BSC		
Н		6.35		0.250	
J	0.30	1.14	0.012	0.045	
K	12.70	14.73	0.500	0.580	
L	1.14	1.27	0.045	0.050	
N	5.08 TYP		0.200 TYP		
Q	2.54	3.05	0.100	0.120	
R	2.03	2.92	0.080	0.115	
S	1.14	1.40	0.045	0.055	
Т	5.84	6.86	0.230	0.270	
U	0.508	1.14	0.020	0.045	

Note:

 Dimensions do not include mold flash or protrusions; these shall not exceed 0.155mm(.006") on any side. Lead dimension shall not include solder coverage.



LX8940

5V Low Dropout Regulator

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NOTES

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