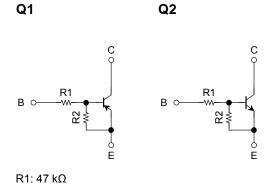
TOSHIBA Transistor Silicon PNP · NPN Epitaxial Type (PCT Process) (Bias Resistor Built-in Transistor)

# **RN4904FE**

Switching, Inverter Circuit, Interface Circuit and **Driver Circuit Applications** 

- Two devices are incorporated into an Extreme-Super-Mini (6-pin) package.
- Incorporating a bias resistor into a transistor reduces parts count. • Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

#### **Equivalent Circuit and Bias Resistor Values**

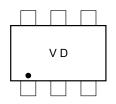


1.6±0.05 1.2±0.05 0.5 6 I.0±0.05 .6±0.05 0.2±0.05 5 0.5 2 22 0.55±0 0.12±0.05 (E1) (B1) 1. EMITTER1 2. BASE1 3. COLLECTOR2 (C2) 4. EMITTER2 (E2) 5. BASE2 5. BASE2 (B2) 6. COLLECTOR1 (C1) ES6 JEDEC \_ JEITA \_ TOSHIBA 2-2N1G

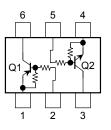
Weight: 0.003 g (typ.)

R2: 47 kΩ (Q1, Q2 common)

#### Marking



### Equivalent Circuit (top view)



Unit: mm

2004-07-01

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### Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-50	V
Collector-emitter voltage	V <sub>CEO</sub>	-50	V
Emitter-base voltage	V <sub>EBO</sub>	-10	V
Collector current	Ι <sub>C</sub>	-100	mA

### Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	10	V
Collector current	Ι <sub>C</sub>	100	mA

### Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P <sub>C</sub> (Note)	100	mW
Junction temperature	Тj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Total rating

## Electrical Characteristics (Ta = 25°C) (Q1)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = -50 \text{ V}, \ I_E = 0$	_	_	-100	nA
	I <sub>CEO</sub>	$V_{CE} = -50 \text{ V}, \text{ I}_{B} = 0$	_		-500	IIA.
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = -10 V, I_C = 0$	-0.082	_	-0.15	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	80	_	_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_{C} = -5 \text{ mA}, I_{B} = -0.25 \text{ mA}$		-0.1	-0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	$V_{CE}=-0.2 \ V, \ I_C=-5 \ mA$	-1.5	_	-5.0	V
Input voltage (OFF)	VI (OFF)	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	-1.0		-1.5	V
Transition frequency	f <sub>T</sub>	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = -10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$		3	6	pF

### Electrical Characteristics (Ta = 25°C) (Q2)

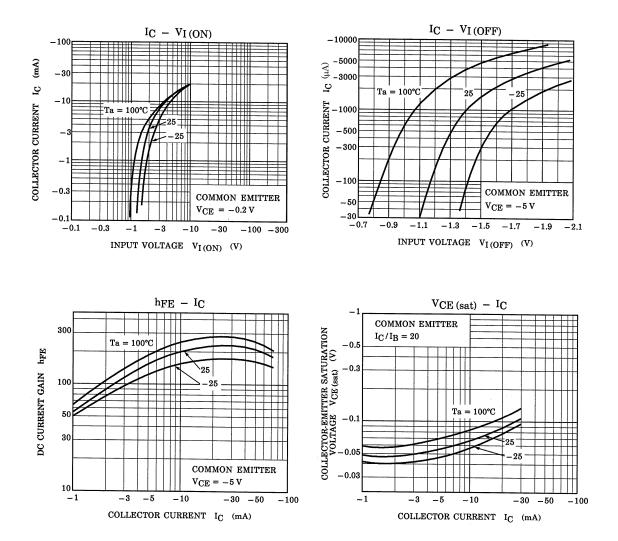
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB}=50~V,~I_{E}=0$		_	100	nA
	ICEO	$V_{CE}=50~V,~I_B=0$		_	500	
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB}=10~V,~I_C=0$	0.082	_	0.15	mA
DC current gain	h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, I_C = 10 \text{ mA}$	80	_		
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	$I_C=5\ m\text{A},\ I_B=0.25\ m\text{A}$		0.1	0.3	V
Input voltage (ON)	V <sub>I (ON)</sub>	$V_{CE}=0.2\ V,\ I_C=5\ mA$	1.5	_	5.0	V
Input voltage (OFF)	VI (OFF)	$V_{CE} = 5 \text{ V}, I_{C} = 0.1 \text{ mA}$	1.0	_	1.5	V
Transition frequency	f <sub>T</sub>	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$		250		MHz
Collector output capacitance	C <sub>ob</sub>	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 1 \text{ MHz}$		3	6	pF

### Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input resistor	R1	—	32.9	47	61.1	kΩ
Resistor ratio	R1/R2		0.9	1.0	1.1	

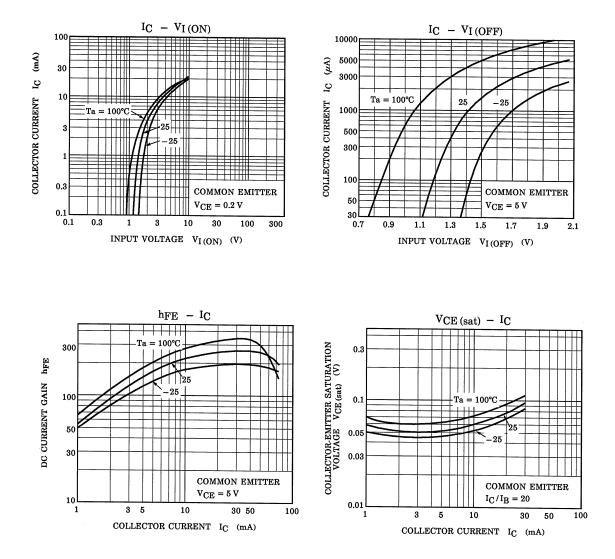
# **TOSHIBA**

Q1



# **TOSHIBA**

Q2



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