

DATA SHEET

PEMB3; PUMB3

**PNP/PNP resistor-equipped
transistors; R1 = 4.7 k Ω , R2 = open**

Product specification
Supersedes data of 2001 Sep 19

2003 Oct 15

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FEATURES

- Built-in bias resistors
- Simplified circuit design
- Reduction of component count
- Reduced pick and place costs.

APPLICATIONS

- Low current peripheral drivers
- Replacement of general purpose transistors in digital applications
- Control of IC inputs.

DESCRIPTION

PNP/PNP resistor-equipped transistors (see “Simplified outline, symbol and pinning” for package details).

QUICK REFERENCE DATA

SYMBOL	PARAMETER	TYP.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	—	–50	V
I_O	output current (DC)	—	–100	mA
TR1	PNP	—	—	—
TR2	PNP	—	—	—
R1	bias resistor	4.7	—	k Ω
R2	bias resistor	open	—	—

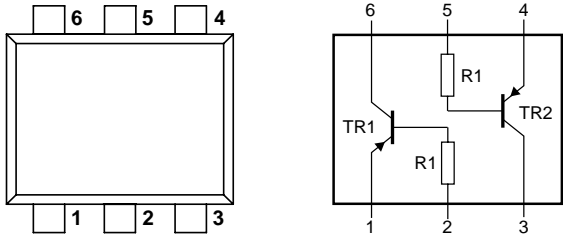
PRODUCT OVERVIEW

TYPE NUMBER	PACKAGE		MARKING CODE	NPN/PNP COMPLEMENT	NPN/PNP COMPLEMENT
	PHILIPS	EIAJ			
PEMB3	SOT666	—	Z3	PEMD6	PEMH7
PUMB3	SOT363	SC-88	B5 ^{*(1)}	PUMD6	PUMH7

Note

- * = p: Made in Hong Kong.
* = t: Made in Malaysia.
* = W: Made in China.

SIMPLIFIED OUTLINE, SYMBOL AND PINNING

TYPE NUMBER	SIMPLIFIED OUTLINE AND SYMBOL	PINNING	
		PIN	DESCRIPTION
PEMB3 PUMB3	 <p>Top view</p> <p>MAM462</p>	1 2 3 4 5 6	emitter TR1 base TR1 collector TR2 emitter TR2 base TR2 collector TR1

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ORDERING INFORMATION

TYPE NUMBER	PACKAGE		
	NAME	DESCRIPTION	VERSION
PEMB3	–	plastic surface mounted package; 6 leads	SOT666
PUMB3	–	plastic surface mounted package; 6 leads	SOT363

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per transistor					
V _{CBO}	collector-base voltage	open emitter	–	–50	V
V _{CEO}	collector-emitter voltage	open base	–	–50	V
V _{EBO}	emitter-base voltage	open collector	–	–5	V
I _O	output current (DC)		–	–100	mA
I _{CM}	peak collector current		–	–100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	SOT363	note 1	–	200	mW
	SOT666	notes 1 and 2	–	200	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	150	°C
T _{amb}	operating ambient temperature		–65	+150	°C
Per device					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C			
	SOT363	note 1	–	300	mW
	SOT666	notes 1 and 2	–	300	mW

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
2. Reflow soldering is the only recommended soldering method.

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
Per transistor				
R _{th j-a}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C		
	SOT363	note 1	625	K/W
	SOT666	notes 1 and 2	625	K/W
Per device				
R _{th j-a}	thermal resistance from junction to ambient	T _{amb} ≤ 25 °C		
	SOT363	note 1	416	K/W
	SOT666	note 1	416	K/W

Notes

1. Device mounted on an FR4 printed-circuit board, single-sided copper, standard footprint.
2. Reflow soldering is the only recommended soldering method.

CHARACTERISTICS

T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0	—	—	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -30 V; I _B = 0	—	—	-1	μA
		V _{CE} = -30 V; I _B = 0; T _j = 150 °C	—	—	-50	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0	—	—	-100	nA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -1 mA	200	—	—	
V _{CEsat}	saturation voltage	I _C = -5 mA; I _B = -0.25 mA	—	—	-100	mV
R1	input resistor		3.3	4.7	6.1	kΩ
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = -10 V; f = 1 MHz	—	—	3	pF

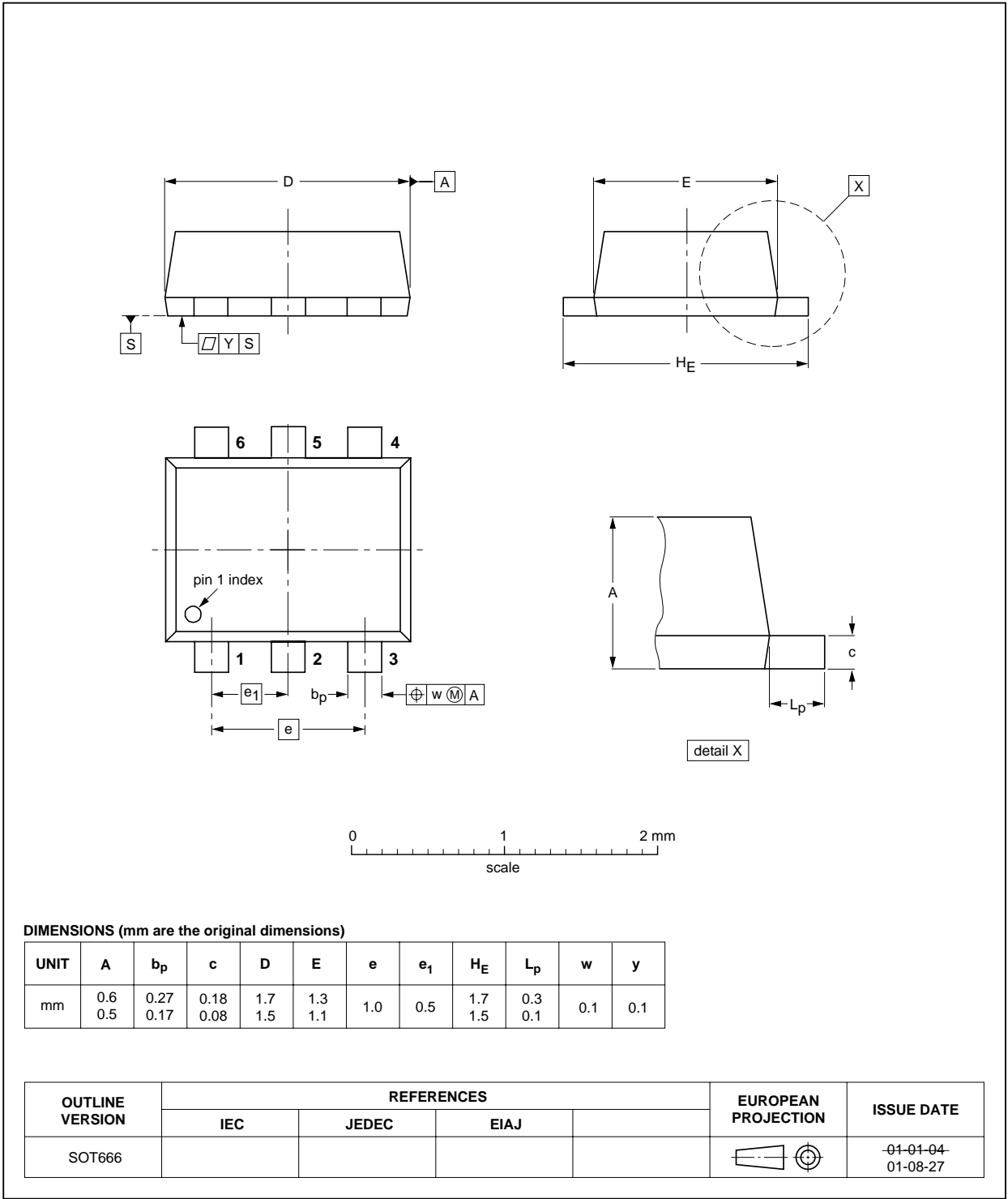
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PACKAGE OUTLINES

Plastic surface mounted package; 6 leads

SOT666

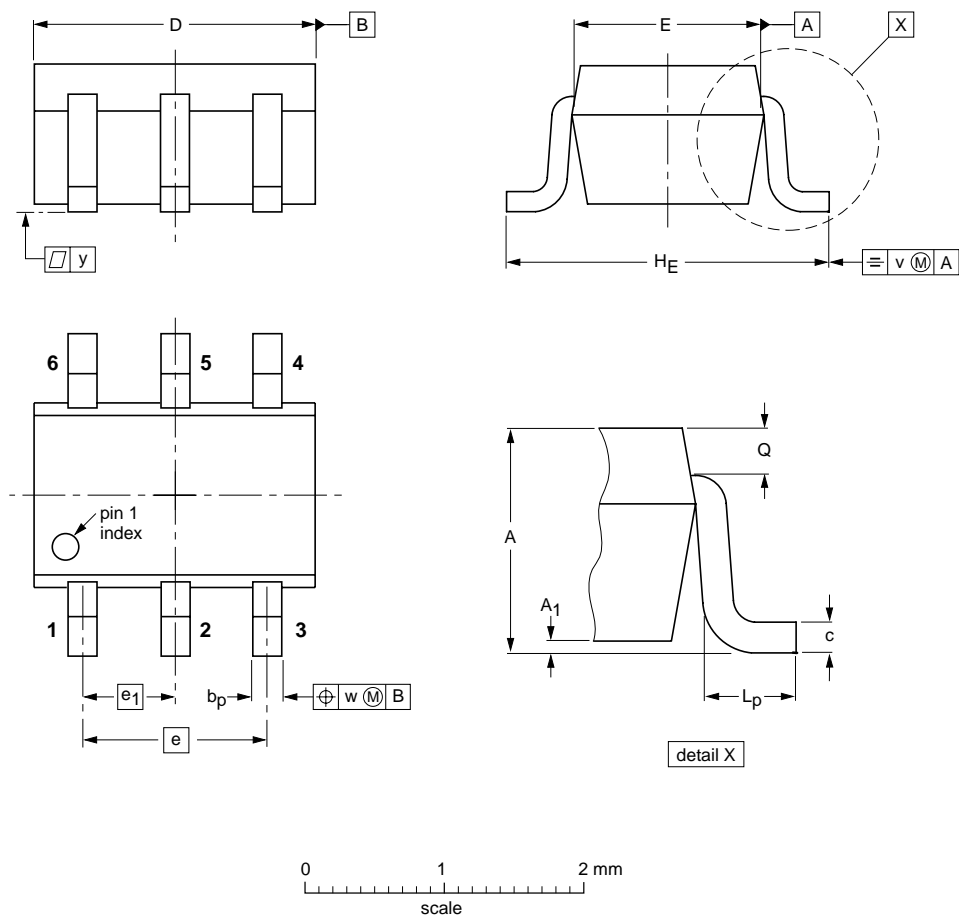


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SOT363



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w	y
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT363			SC-88			97-02-28

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DATA SHEET STATUS

LEVEL	DATA SHEET STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾⁽³⁾	DEFINITION
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
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3. For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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